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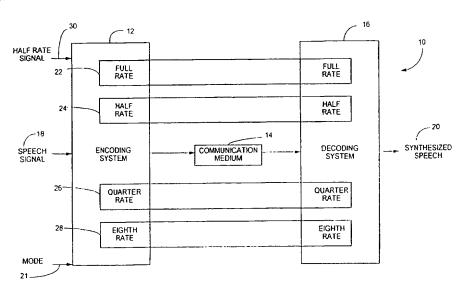
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(54) Title: MULTIMODE SPEECH ENCODER



(57) Abstract: A speech compression system (10) capable of encoding a speech signal (18) into a bitstream for subsequent decoding to generate synthesized speech (20) is disclosed. The speech compression system (10) optimizes the bandwidth consumed by the bitstream by balancing the desired average bit rate with the perceptual quality of the reconstructed speech. The speech compression system (10) comprises a full-rate codec (22), a half-rate codec (24), a quarter-rate codec (26) and an eighth-rate codec (28). The codes (22, 24, 26 and 28) are selectively activated based on a rate selection. In addition, the full and half-rate codecs (22 and 24) are selectively activated based on a type classification. Each codec (22, 24, 26 and 28) is selectively activated to encode and decode the speech signal (18) at different bit rates emphasizing different aspects of the speech signal (18) to enhance overall quality of the synthesized speech (20).





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MULTIMODE SPEECH ENCODER

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TECHNICAL FIELD

This invention relates to speech communication systems and, more particularly, to systems for digital speech coding.

BACKGROUND ART

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One prevalent mode of human communication is by the use of communication systems. Communication systems include both wireline and wireless radio based systems. Wireless communication systems are electrically connected with the wireline based systems and communicate with the mobile communication devices using radio frequency (RF) communication. Currently, the radio frequencies available for communication in cellular systems, for example, are in the cellular frequency range centered around 900 MHz and in the personal communication services (PCS) frequency range centered around 1900 MHz. Data and voice transmissions within the wireless system have a bandwidth that consumes a portion of the radio frequency. Due to increased traffic caused by the expanding popularity of wireless communication devices, such as cellular telephones, it is desirable to reduced bandwidth of transmissions within the wireless systems.

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Digital transmission in wireless radio communications is increasingly applied to both voice and data due to noise immunity, reliability, compactness of equipment and the ability to implement sophisticated signal processing functions using digital techniques. Digital transmission of speech signals involves the steps of: sampling an analog speech waveform with an analog-to-digital converter, speech compression (encoding), transmission, speech decompression (decoding), digital-to-analog conversion, and playback into an earpiece or a loudspeaker. The sampling of the analog speech waveform with the analog-to-digital converter creates a digital signal. However, the number of bits used in the digital signal to represent the analog speech waveform creates a

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relatively large bandwidth. For example, a speech signal that is sampled at a rate of 8000 Hz (once every 0.125 ms), where each sample is represented by 16 bits, will result in a bit rate of 128,000 (16x8000) bits per second, or 128 Kbps (Kilobits per second).

Speech compression may be used to reduce the number of bits that represent the speech signal thereby reducing the bandwidth needed for transmission. However, speech compression may result in degradation of the quality of decompressed speech. In general, a higher bit rate will result in higher quality, while a lower bit rate will result in lower quality. However, modern speech compression techniques, such as coding techniques, can produce decompressed speech of relatively high quality at relatively low bit rates. In general, modern coding techniques attempt to represent the perceptually important features of the speech signal, without preserving the actual speech waveform.

One coding technique used to lower the bit rate involves varying the degree of speech compression (i.e. varying the bit rate) depending on the part of the speech signal being compressed. Typically, parts of the speech signal for which adequate perceptual representation is more difficult (such as voiced speech, plosives, or voiced onsets) are coded and transmitted using a higher number of bits. Conversely, parts of the speech for which adequate perceptual representation is less difficult (such as unvoiced, or the silence between words) are coded with a lower number of bits. The resulting average bit rate for the speech signal will be relatively lower than would be the case for a fixed bit rate that provides decompressed speech of similar quality.

Speech compression systems, commonly called codecs, include an encoder and a decoder and may be used to reduce the bit rate of digital speech signals. Numerous algorithms have been developed for speech codecs that reduce the number of bits required to digitally encode the original speech while attempting to maintain high quality reconstructed speech. Code-Excited Linear Predictive (CELP) coding techniques, as discussed in the article entitled "Code-Excited Linear Prediction: High-Quality Speech at Very Low Rates," by M. R. Schroeder and B. S. Atal, Proc. ICASSP-85, pages 937-940, 1985, provide one effective speech coding algorithm. An example of a variable rate CELP based speech coder is TIA (Telecommunications Industry Association) IS-127 standard that is designed for CDMA (Code Division Multiple Access) applications. The CELP coding technique utilizes several prediction techniques to remove the redundancy from the speech signal. The CELP coding approach is frame-based in the sense that it stores sampled input speech signals into a block of samples called frames. The frames of data may then be processed to create a compressed speech signal in digital form.

The CELP coding approach uses two types of predictors, a short-term predictor and a long-term predictor. The short-term predictor typically is applied before the long-term predictor. A prediction error derived from the short-term predictor is commonly called short-term residual, and a prediction error derived from the long-term predictor is commonly called long-term residual. The long-term residual may be coded using a fixed codebook that includes a plurality of fixed codebook entries or vectors. One of the entries may be selected and multiplied by a fixed codebook gain to represent the long-term residual. The short-term predictor also can be referred to as an LPC (Linear Prediction Coding) or a spectral representation, and typically comprises 10 prediction parameters. The long-term predictor also can be referred to as a pitch predictor or an adaptive codebook and typically comprises a lag parameter and a long-term predictor gain parameter. Each lag parameter

also can be called a pitch lag, and each long-term predictor gain parameter can also be called an adaptive codebook gain. The lag parameter defines an entry or a vector in the adaptive codebook.

The CELP encoder performs an LPC analysis to determine the short-term predictor parameters. Following the LPC analysis, the long-term predictor parameters may be determined. In addition, determination of the fixed codebook entry and the fixed codebook gain that best represent the long-term residual occurs. The powerful concept of analysis-by-synthesis (ABS) is employed in CELP coding. In the ABS approach, the best contribution from the fixed codebook, the best fixed codebook gain, and the best long-term predictor parameters may be found by synthesizing them using an inverse prediction filter and applying a perceptual weighting measure. The short-term (LPC) prediction coefficients, the fixed-codebook gain, as well as the lag parameter and the long-term gain parameter may then be quantized. The quantization indices, as well as the fixed codebook indices, may be sent from the encoder to the decoder.

The CELP decoder uses the fixed codebook indices to extract a vector from the fixed codebook. The vector may be multiplied by the fixed-codebook gain, to create a long-term excitation also known as a fixed codebook contribution. A long-term predictor contribution may be added to the long-term excitation to create a short-term excitation that commonly is referred to simply as an excitation. The long-term predictor contribution comprises the short-term excitation from the past multiplied by the long-term predictor gain. The addition of the long-term predictor contribution alternatively can be viewed as an adaptive codebook contribution or as a long-term (pitch) filtering. The short-term excitation may be passed through a short-term inverse prediction filter (LPC) that uses the short-term (LPC) prediction coefficients quantized by the encoder to generate synthesized speech. The synthesized speech may then be passed through a post-filter that reduces perceptual coding noise.

These speech compression techniques have resulted in lowering the amount of bandwidth used to transmit a speech signal. However, further reduction in bandwidth is particular important in a communication system that has to allocate its resources to a large number of users. Accordingly, there is a need for systems and methods of speech coding that are capable of minimizing the average bit rate needed for speech representation, while providing high quality decompressed speech.

DISCLOSURE OF INVENTION

This invention provides systems for encoding and decoding speech signals. The embodiments may use the CELP coding technique and prediction based coding as a framework to employ signal-processing functions using waveform matching and perceptual related techniques. These techniques allow the generation of synthesized speech that closely resembles the original speech by including perceptual features while maintaining a relatively low bit rate. One application of the embodiments is in wireless communication systems. In this application, the encoding of original speech, or the decoding to generate synthesized speech, may occur at mobile communication devices. In addition, encoding and decoding may occur within wireline-based systems or within other wireless communication systems to provide interfaces to wireline-based systems.

One embodiment of a speech compression system includes a full-rate codec, a half-rate codec, a quarter-rate codec and an eighth-rate codec each capable of encoding and decoding speech signals. The full-rate, half-rate, quarter-rate and eighth-rate codecs encode the speech signals at bit rates of 8.5 Kbps, 4 Kbps, 2 Kbps and 0.8

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Kbps, respectively. The speech compression system performs a rate selection on a frame of a speech signal to select one of the codecs. The rate selection is performed on a frame-by-frame basis. Frames are created by dividing the speech signal into segments of a finite length of time. Since each frame may be coded with a different bit rate, the speech compression system is a variable-rate speech compression system that codes the speech at an average bit rate.

The rate selection is determined by characterization of each frame of the speech signal based on the portion of the speech signal contained in the particular frame. For example, frames may be characterized as stationary voiced, non-stationary voiced, unvoiced, background noise, silence etc. In addition, the rate selection is based on a Mode that the speech compression system is operating within. The different Modes indicate the desired average bit rate. The codecs are designed for optimized coding within the different characterizations of the speech signals. Optimal coding balances the desire to provide synthesized speech of the highest perceptual quality while maintaining the desired average bit rate, thereby maximizing use of the available bandwidth. During operation, the speech compression system selectively activates the codecs based on the Mode as well as characterization of the frame in an attempt to optimize the perceptual quality of the synthesized speech.

Once the full or the half-rate codec is selected by the rate selection, a type classification of the speech signal occurs to further optimize coding. The type classification may be a first type (i.e. a Type One) for frames containing a harmonic structure and a formant structure that do not change rapidly or a second type (i.e. a Type Zero) for all other frames. The bit allocation of the full-rate and half-rate codecs may be adjusted in response to the type classification to further optimize the coding of the frame. The adjustment of the bit allocation provides improved perceptual quality of the reconstructed speech signal by emphasizing different aspects of the speech signal within each frame.

Accordingly, the speech coder is capable of selectively activating the codecs to maximize the overall quality of a reconstructed speech signal while maintaining the desired average bit rate. Other systems, methods, features and advantages of the invention will be or will become apparent to one with skill in the art upon examination of the following figures and detailed description. It is intended that all such additional systems, methods, features and advantages be included within this description, be within the scope of the invention, and be protected by the accompanying claims.

BRIEF DESCRIPTION OF DRAWINGS

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The components in the figures are not necessarily to scale, emphasis instead being placed upon illustrating the principals of the invention. Moreover, in the figures, like reference numerals designate corresponding parts throughout the different views.

FIG. 1 is a block diagram of one embodiment of a speech compression system.

FIG. 2 is an expanded block diagram of one embodiment of the encoding system illustrated in Fig. 1.

FIG. 3 is an expanded block diagram of one embodiment of the decoding system illustrated in Fig. 1.

FIG. 4 is a table illustrating the bit allocation of one embodiment of the full-rate codec.

FIG. 5 is a table illustrating the bit allocation of one embodiment of the half-rate codec.

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FIG. 6 is a table illustrating the bit allocation of one embodiment of the quarter-rate codec.

- FIG. 7 is a table illustrating the bit allocation of one embodiment of the eighth-rate codec.
- FIG. 8 is an expanded block diagram of one embodiment of the pre-processing module illustrated in Fig. 2.
- FIG. 9 is an expanded block diagram of one embodiment of the initial frame-processing module illustrated in Fig. 2 for the full and half-rate codecs.
- FIG. 10 is an expanded block diagram of one embodiment of the first sub-frame processing module illustrated in Fig. 2 for the full and half-rate codecs.
- FIG. 11 is an expanded block diagram of one embodiment of the first frame processing module, the second sub-frame processing module and the second frame processing module illustrated in Fig. 2 for the full and half-rate codecs.
- FIG. 12 is an expanded block diagram of one embodiment of the decoding system illustrated in Fig. 3 for the full and half-rate codecs.

MODES FOR CARRYING OUT THE INVENTION

The embodiments are discussed with reference to speech signals, however, processing of any other signal is possible. It will also be understood that the numerical values disclosed may be numerically represented by floating point, fixed point, decimal, or other similar numerical representation that may cause slight variation in the values but will not compromise functionality. Further, functional blocks identified as modules are not intended to represent discrete structures and may be combined or further sub-divided in various embodiments.

FIG. 1 is a block diagram of one embodiment of the speech compression system 10. The speech compression system 10 includes an encoding system 12, a communication medium 14 and a decoding system 16 that may be connected as illustrated. The speech compression system 10 may be any system capable of receiving and encoding a speech signal 18, and then decoding it to create post-processed synthesized speech 20. In a typical communication system, the wireless communication system is electrically connected with a public switched telephone network (PSTN) within the wireline-based communication system. Within the wireless communication system, a plurality of base stations are typically used to provide radio communication with mobile communication devices such as a cellular telephone or a portable radio transceiver.

The speech compression system 10 operates to receive the speech signal 18. The speech signal 18 emitted by a sender (not shown) can be, for example, captured by a microphone (not shown) and digitized by an analog-to-digital converter (not shown). The sender may be a human voice, a musical instrument or any other device capable of emitting analog signals. The speech signal 18 can represent any type of sound, such as, voice speech, unvoiced speech, background noise, silence, music etc.

The encoding system 12 operates to encode the speech signal 18. The encoding system 12 may be part of a mobile communication device, a base station or any other wireless or wireline communication device that is capable of receiving and encoding speech signals 18 digitized by an analog-to-digital converter. The wireline communication devices may include Voice over Internet Protocol (VoIP) devices and systems. The encoding system 12 segments the speech signal 18 into frames to generate a bitstream. One embodiment of the speech

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compression system 10 uses frames that comprise 160 samples that, at a sampling rate of 8000 Hz, correspond to 20 milliseconds per frame. The frames represented by the bitstream may be provided to the communication medium 14.

The communication medium 14 may be any transmission mechanism, such as a communication channel, radio waves, microwave, wire transmissions, fiber optic transmissions, or any medium capable of carrying the bitstream generated by the encoding system 12. The communication medium 14 may also include transmitting devices and receiving devices used in the transmission of the bitstream. An example embodiment of the communication medium 14 can include communication channels, antennas and associated transceivers for radio communication in a wireless communication system. The communication medium 14 also can be a storage mechanism, such as, a memory device, a storage media or other device capable of storing and retrieving the bitstream generated by the encoding system 12. The communication medium 14 operates to transmit the bitstream generated by the encoding system 12 to the decoding system 16.

The decoding system 16 receives the bitstream from the communication medium 14. The decoding system 14 may be part of a mobile communication device, a base station or other wireless or wireline communication device that is capable of receiving the bitstream. The decoding system 16 operates to decode the bitstream and generate the post-processed synthesized speech 20 in the form of a digital signal. The post-processed synthesized speech 20 may then be converted to an analog signal by a digital-to-analog converter (not shown). The analog output of the digital-to-analog converter may be received by a receiver (not shown) that may be a human car, a magnetic tape recorder, or any other device capable of receiving an analog signal. Alternatively, a digital recording device, a speech recognition device, or any other device capable of receiving a digital signal may receive the post-processed synthesized speech 20.

One embodiment of the speech compression system 10 also includes a Mode line 21. The Mode line 21 carries a Mode signal that controls the speech compression system 10 by indicating the desired average bit rate for the bitstream. The Mode signal may be generated externally by, for example, a wireless communication system using a Mode signal generation module. The Mode signal generation module determines the Mode Signal based on a plurality of factors, such as, the desired quality of the post-processed synthesized speech 20, the available bandwidth, the services contracted by a user or any other relevant factor. The Mode signal is controlled and selected by the communication system that the speech compression system 10 is operating within. The Mode signal may be provided to the encoding system 12 to aid in the determination of which of a plurality of codecs may be activated within the encoding system 12.

The codecs comprise an encoder portion and a decoder portion that are located within the encoding system 12 and the decoding system 16, respectively. In one embodiment of the speech compression system 10 there are four codecs namely; a full-rate codec 22, a half-rate codec 24, a quarter-rate codec 26, and an eighth-rate codec 28. Each of the codecs 22, 24, 26, and 28 is operable to generate the bitstream. The size of the bitstream generated by each codec 22, 24, 26, and 28, and hence the bandwidth or capacity needed for transmission of the bitstream via the communication medium 14 is different.

In one embodiment, the full-rate codec 22, the half-rate codec 24, the quarter-rate codec 26 and the eighth-rate codec 28 generate 170 bits, 80 bits, 40 bits and 16 bits, respectively, per frame. The size of the

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bitstream of each frame corresponds to a bit rate, namely, 8.5 Kbps for the full-rate codec 22, 4.0 Kbps for the half-rate codec 24, 2.0 Kbps for the quarter-rate codec 26, and 0.8 Kbps for the eighth-rate codec 28. However, fewer or more codecs as well as other bit rates are possible in alternative embodiments. By processing the frames of the speech signal 18 with the various codecs, an average bit rate is achieved. The encoding system 12 determines which of the codecs 22, 24, 26, and 28 may be used to encode a particular frame based on characterization of the frame, and on the desired average bit rate provided by the Mode signal. Characterization of a frame is based on the portion of the speech signal 18 contained in the particular frame. For example, frames may be characterized as stationary voiced, non-stationary voiced, unvoiced, onset, background noise, silence etc.

The Mode signal on the Mode signal line 21 in one embodiment identifies a Mode 0, a Mode 1, and a Mode 2. Each of the three Modes provides a different desired average bit rate that can vary the percentage of usage of each of the codecs 22, 24, 26, and 28. Mode 0 may be referred to as a premium mode in which most of the frames may be coded with the full-rate codec 22; fewer of the frames may be coded with the half-rate codec 24; and frames comprising silence and background noise may be coded with the quarter-rate codec 26 and the eighth-rate codec 28. Mode 1 may be referred to as a standard mode in which frames with high information content, such as onset and some voiced frames, may be coded with the full-rate codec 22. In addition, other voiced and unvoiced frames may be coded with the half-rate codec 24, some unvoiced frames may be coded with the quarter-rate codec 26, and silence and stationary background noise frames may be coded with the eighth-rate codec 28.

Mode 2 may be referred to as an economy mode in which only a few frames of high information content may be coded with the full-rate codec 22. Most of the frames in Mode 2 may be coded with the half-rate codec 24 with the exception of some unvoiced frames that may be coded with the quarter-rate codec 26. Silence and stationary background noise frames may be coded with the eighth-rate codec 28 in Mode 2. Accordingly, by varying the selection of the codecs 22, 24, 26, and 28 the speech compression system 10 can deliver reconstructed speech at the desired average bit rate while attempting to maintain the highest possible quality. Additional Modes, such as, a Mode three operating in a super economy Mode or a half-rate max Mode in which the maximum codec activated is the half-rate codec 24 are possible in alternative embodiments.

Further control of the speech compression system 10 also may be provided by a half rate signal line 30. The half rate signal line 30 provides a half rate signaling flag. The half rate signaling flag may be provided by an external source such as a wireless communication system. When activated, the half rate signaling flag directs the speech compression system 10 to use the half-rate codec 24 as the maximum rate. Determination of when to activate the half rate signaling flag is performed by the communication system that the speech compression system 10 is operating within. Similar to the Mode signal determination, a half rate-signaling module controls activation of the half rate signaling flag based on a plurality of factors that are determined by the communication system. In alternative embodiments, the half rate signaling flag could direct the speech compression system 10 to use one codec 22, 24, 26, and 28 in place of another or identify one or more of the codecs 22, 24, 26, and 28 as the maximum or minimum rate.

In one embodiment of the speech compression system 10, the full and half-rate codecs 22 and 24 may be based on an eX-CELP (extended CELP) approach and the quarter and eighth-rate codecs 26 and 28 may be based

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on a perceptual matching approach. The eX-CELP approach extends the traditional balance between perceptual matching and waveform matching of traditional CELP. In particular, the eX-CELP approach categorizes the frames using a rate selection and a type classification that will be described later. Within the different categories of frames, different encoding approaches may be utilized that have different perceptual matching, different waveform matching, and different bit assignments. The perceptual matching approach of the quarter-rate codec 26 and the eighth-rate codec 28 do not use waveform matching and instead concentrate on the perceptual aspects when encoding frames.

The coding of each frame with either the eX-CELP approach or the perceptual matching approach may be based on further dividing the frame into a plurality of subframes. The subframes may be different in size and in number for each codec 22, 24, 26, and 28. In addition, with respect to the eX-CELP approach, the subframes may be different for each category. Within the subframes, speech parameters and waveforms may be coded with several predictive and non-predictive scalar and vector quantization techniques. In scalar quantization a speech parameter or element may be represented by an index location of the closest entry in a representative table of scalars. In vector quantization several speech parameters may be grouped to form a vector. The vector may be represented by an index location of the closest entry in a representative table of vectors.

In predictive coding, an element may be predicted from the past. The element may be a scalar or a vector. The prediction error may then be quantized, using a table of scalars (scalar quantization) or a table of vectors (vector quantization). The eX-CELP coding approach, similarly to traditional CELP, uses the powerful Analysis-by-Synthesis (ABS) scheme for choosing the best representation for several parameters. In particular, the parameters may be the adaptive codebook, the fixed codebook, and their corresponding gains. The ABS scheme uses inverse prediction filters and perceptual weighting measures for selecting the best codebook entries.

One implementation of an embodiment of the speech compression system 10 may be in a signal-processing device such as a Digital Signal Processing (DSP) chip, a mobile communication device or a radio transmission base station. The signal-processing device may be programmed with source code. The source code may be first translated into fixed point, and then translated into the programming language that is specific to the signal-processing device. The translated source code may then be downloaded and run in the signal-processing device. One example of source code is the C language computer program utilized by one embodiment of the speech compression system 10 that is included in the attached microfiche appendix as Appendix A and B.

FIG. 2 is a more detailed block diagram of the encoding system 12 illustrated in FIG. 1. One embodiment of the encoding system 12 includes a pre-processing module 34, a full-rate encoder 36, a half-rate encoder 38, a quarter-rate encoder 40 and an eighth-rate encoder 42 that may be connected as illustrated. The rate encoders 36, 38, 40, and 42 include an initial frame-processing module 44 and an excitation-processing module 54.

The speech signal 18 received by the encoding system 12 is processed on a frame level by the pre-processing module 34. The pre-processing module 34 is operable to provide initial processing of the speech signal 18. The initial processing can include filtering, signal enhancement, noise removal, amplification and other similar techniques capable of optimizing the speech signal 18 for subsequent encoding.

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The full, half, quarter and eighth-rate encoders 36, 38, 40, and 42 are the encoding portion of the full, half, quarter and eighth-rate codecs 22, 24, 26, and 28, respectively. The initial frame-processing module 44 performs initial frame processing, speech parameter extraction and determines which of the rate encoders 36, 38, 40, and 42 will encode a particular frame. The initial frame-processing module 44 may be illustratively subdivided into a plurality of initial frame processing modules, namely, an initial full frame processing module 46, an initial half frame-processing module 48, an initial quarter frame-processing module 50 and an initial eighth frame-processing module 52. However, it should be noted that the initial frame-processing module 44 performs processing that is common to all the rate encoders 36, 38, 40, and 42 and particular processing that is particular to each rate encoder 36, 38, 40, and 42. The sub-division of the initial frame-processing module 44 into the respective initial frame processing modules 46, 48, 50, and 52 corresponds to a respective rate encoder 36, 38, 40, and 42.

The initial frame-processing module 44 performs common processing to determine a rate selection that activates one of the rate encoders 36, 38, 40, and 42. In one embodiment, the rate selection is based on the characterization of the frame of the speech signal 18 and the Mode the speech compression system 10 is operating within. Activation of one of the rate encoders 36, 38, 40, and 42 correspondingly activates one of the initial frame-processing modules 46, 48, 50, and 52.

The particular initial frame-processing module 46, 48, 50, and 52 is activated to encode aspects of the speech signal 18 that are common to the entire frame. The encoding by the initial frame-processing module 44 quantizes parameters of the speech signal 18 contained in a frame. The quantized parameters result in generation of a portion of the bitstream. In general, the bitstream is the compressed representation of a frame of the speech signal 18 that has been processed by the encoding system 12 through one of the rate encoders 36, 38, 40, and 42.

In addition to the rate selection, the initial frame-processing module 44 also performs processing to determine a type classification for each frame that is processed by the full and half-rate encoders 36 and 38. The type classification of one embodiment classifies the speech signal 18 represented by a frame as a first type (i.e., a Type One) or as a second type (i.e., a Type Zero). The type classification of one embodiment is dependent on the nature and characteristics of the speech signal 18. In an alternate embodiment, additional type classifications and supporting processing may be provided.

Type One classification includes frames of the speech signal 18 that exhibit stationary behavior. Frames exhibiting stationary behavior include a harmonic structure and a formant structure that do not change rapidly. All other frames may be classified with the Type Zero classification. In alternative embodiments, additional type classifications may classify frames into additional classification based on time-domain, frequency domain, etc. The type classification optimizes encoding by the initial full-rate frame-processing module 46 and the initial half-rate frame-processing module 48, as will be later described. In addition, both the type classification and the rate selection may be used to optimize encoding by portions of the excitation-processing module 54 that correspond to the full and half-rate encoders 36 and 38.

One embodiment of the excitation-processing module 54 may be sub-divided into a full-rate module 56, a half-rate module 58, a quarter-rate module 60, and an eighth-rate module 62. The rate modules 56, 58, 60, and 62 correspond to the rate encoders 36, 38, 40, and 42 as illustrated in FIG. 2. The full and half-rate modules 56

and 58 of one embodiment both include a plurality of frame processing modules and a plurality of subframe processing modules that provide substantially different encoding as will be discussed.

The portion of the excitation processing module 54 for both the full and half-rate encoders 36 and 38 include type selector modules, first subframe processing modules, second subframe processing modules, first frame processing modules and second subframe processing modules. More specifically, the full-rate module 56 includes an F type selector module 68, an F0 first subframe processing module 70, an F1 first frame-processing module 72, an F1 second subframe processing module 74 and an F1 second frame-processing module 76. The term "F" indicates full-rate, and "0" and "1" signify Type Zero and Type One, respectively. Similarly, the half-rate module 58 includes an H type selector module 78, an H0 first subframe processing module 80, an H1 first frame-processing module 82, an H1 second subframe processing module 84, and an H1 second frame-processing module 86.

The F and H type selector modules 68,78 direct the processing of the speech signals 18 to further optimize the encoding process based on the type classification. Classification as Type One indicates the frame contains a harmonic structure and a formant structure that do not change rapidly, such as stationary voiced speech. Accordingly, the bits used to represent a frame classified as Type One may be allocated to facilitate encoding that takes advantage of these aspects in representing the frame. Classification as Type Zero indicates the frame may exhibit non-stationary behavior, for example, a harmonic structure and a formant structure that changes rapidly or the frame may exhibit stationary unvoiced or noise-like characteristics. The bit allocation for frames classified as Type Zero may be consequently adjusted to better represent and account for this behavior.

For the full rate module 56, the F0 first subframe-processing module 70 generates a portion of the bitstream when the frame being processed is classified as Type Zero. Type Zero classification of a frame activates the F0 first subframe-processing module 70 to process the frame on a subframe basis. The F1 first frame-processing module 72, the F1 second subframe processing module 74, and the F1 second frame-processing modules 76 combine to generate a portion of the bitstream when the frame being processed is classified as Type One. Type One classification involves both subframe and frame processing within the full rate module 56.

Similarly, for the half rate module 58, the H0 first subframe-processing module 80 generates a portion of the bitstream on a sub-frame basis when the frame being processed is classified as Type Zero. Further, the H1 first frame-processing module 82, the H1 second subframe processing module 84, and the H1 second frame-processing module 86 combine to generate a portion of the bitstream when the frame being processed is classified as Type One. As in the full rate module 56, the Type One classification involves both subframe and frame processing.

The quarter and eighth-rate modules 60 and 62 are part of the quarter and eighth-rate encoders 40 and 42, respectively, and do not include the type classification. The type classification is not included due to the nature of the frames that are processed. The quarter and eighth-rate modules 60 and 62 generate a portion of the bitstream on a subframe basis and a frame basis, respectively, when activated.

The rate modules 56, 58, 60, and 62 generate a portion of the bitstream that is assembled with a respective portion of the bitstream that is generated by the initial frame processing modules 46, 48, 50, and 52 to create a digital representation of a frame. For example, the portion of the bitstream generated by the initial full-

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rate frame-processing module 46 and the full-rate module 56 may be assembled to form the bitstream generated when the full-rate encoder 36 is activated to encode a frame. The bitstreams from each of the encoders 36, 38, 40, and 42 may be further assembled to form a bitstream representing a plurality of frames of the speech signal 18. The bitstream generated by the encoders 36, 38, 40, and 42 is decoded by the decoding system 16.

FIG. 3 is an expanded block diagram of the decoding system 16 illustrated in FIG. 1. One embodiment of the decoding system 16 includes a full-rate decoder 90, a half-rate decoder 92, a quarter-rate decoder 94, an eighth-rate decoder 96, a synthesis filter module 98 and a post-processing module 100. The full, half, quarter and eighth-rate decoders 90, 92, 94, and 96, the synthesis filter module 98 and the post-processing module 100 are the decoding portion of the full, half, quarter and eighth-rate codecs 22, 24, 26, and 28.

The decoders 90, 92, 94, and 96 receive the bitstream and decode the digital signal to reconstruct different parameters of the speech signal 18. The decoders 90, 92, 94, and 96 may be activated to decode each frame based on the rate selection. The rate selection may be provided from the encoding system 12 to the decoding system 16 by a separate information transmittal mechanism, such as a control channel in a wireless communication system. In this example embodiment, the rate selection may be provided to the mobile communication devices as part of broadcast beacon signals generated by the base stations within the wireless communications system. In general, the broadcast beacon signals are generated to provide identifying information used to establish communications between the base stations and the mobile communication devices.

The synthesis filter 98 and the post-processing module 100 are part of the decoding process for each of the decoders 90, 92, 94, and 96. Assembling the parameters of the speech signal 18 that are decoded by the decoders 90, 92, 94, and 96 using the synthesis filter 98, generates synthesized speech. The synthesized speech is passed through the post-processing module 100 to create the post-processed synthesized speech 20.

One embodiment of the full-rate decoder 90 includes an F type selector 102 and a plurality of excitation reconstruction modules. The excitation reconstruction modules comprise an F0 excitation reconstruction module 104 and an F1 excitation reconstruction module 106. In addition, the full-rate decoder 90 includes a linear prediction coefficient (LPC) reconstruction module 107. The LPC reconstruction module 107 comprises an F0 LPC reconstruction module 108 and an F1 LPC reconstruction module 110.

Similarly, one embodiment of the half-rate decoder 92 includes an H type selector 112 and a plurality of excitation reconstruction modules. The excitation reconstruction modules comprise an H0 excitation reconstruction module 114 and an H1 excitation reconstruction module 116. In addition, the half-rate decoder 92 comprises a linear prediction coefficient (LPC) reconstruction module that is an H LPC reconstruction module 118. Although similar in concept, the full and half-rate decoders 90 and 92 are designated to decode bitstreams from the corresponding full and half-rate encoders 36 and 38, respectively.

The F and H type selectors 102 and 112 selectively activate respective portions of the full and half-rate decoders 90 and 92 depending on the type classification. When the type classification is Type Zero, the F0 or H0 excitation reconstruction modules 104 or 114 are activated. Conversely, when the type classification is Type One, the F1 or H1 excitation reconstruction modules 106 or 116 are activated. The F0 or F1 LPC reconstruction modules 108 or 110 are activated by the Type Zero and Type One type classifications, respectively. The H LPC reconstruction module 118 is activated based solely on the rate selection.

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The quarter-rate decoder 94 includes a Q excitation reconstruction module 120 and a Q LPC reconstruction module 122. Similarly, the eighth-rate decoder 96 includes an E excitation reconstruction module 124 and an E LPC reconstruction module 126. Both the respective Q or E excitation reconstruction modules 120 or 124 and the respective Q or E LPC reconstruction modules 122 or 126 are activated based solely on the rate selection.

Each of the excitation reconstruction modules is operable to provide the short-term excitation on a short-term excitation line 128 when activated. Similarly, each of the LPC reconstruction modules operate to generate the short-term prediction coefficients on a short-term prediction coefficients line 130. The short-term excitation and the short-term prediction coefficients are provided to the synthesis filter 98. In addition, in one embodiment, the short-term prediction coefficients are provided to the post-processing module 100 as illustrated in FIG. 3.

The post-processing module 100 can include filtering, signal enhancement, noise modification, amplification, tilt correction and other similar techniques capable of improving the perceptual quality of the synthesized speech. The post-processing module 100 is operable to decrease the audible noise without degrading the synthesized speech. Decreasing the audible noise may be accomplished by emphasizing the formant structure of the synthesized speech or by suppressing only the noise in the frequency regions that are perceptually not relevant for the synthesized speech. Since audible noise becomes more noticeable at lower bit rates, one embodiment of the post-processing module 100 may be activated to provide post-processing of the synthesized speech differently depending on the rate selection. Another embodiment of the post-processing module 100 may be operable to provide different post-processing to different groups of the decoders 90, 92, 94, and 96 based on the rate selection.

During operation, the initial frame-processing module 44 illustrated in FIG. 2 analyzes the speech signal 18 to determine the rate selection and activate one of the codecs 22, 24, 26, and 28. If for example, the full-rate codec 22 is activated to process a frame based on the rate selection, the initial full-rate frame-processing module 46 determines the type classification for the frame and generates a portion of the bitstream. The full-rate module 56, based on the type classification, generates the remainder of the bitstream for the frame.

The bitstream may be received and decoded by the full-rate decoder 90 based on the rate selection. The full-rate decoder 90 decodes the bitstream utilizing the type classification that was determined during encoding. The synthesis filter 98 and the post-processing module 100 use the parameters decoded from the bitstream to generate the post-processed synthesized speech 20. The bitstream that is generated by each of the codecs 22, 24, 26, and 28 contains significantly different bit allocations to emphasize different parameters and/or characteristics of the speech signal 18 within a frame.

1.0 BIT ALLOCATION

FIGS. 4, 5, 6 and 7 are tables illustrating one embodiment of the bit-allocation for the full-rate codec 22, the half-rate codec 24, the quarter-rate codec 26, and the eighth-rate codec 28, respectively. The bit-allocation designates the portion of the bitstream generated by the initial frame-processing module 44, and the portion of the bitstream generated by the excitation-processing module 54 within a respective encoder 36, 38, 40, and 42. In addition the bit-allocation designates the number of bits in the bitstream that represent a frame. Accordingly, the

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bit rate varies depending on the codec 22, 24, 26, and 28 that is activated. The bitstream may be classified into a first portion and a second portion depending on whether the representative bits are generated on a frame basis or on a subframe basis, respectively, by the encoding system 12. As will be described later, the first portion and the second portion of the bitstream vary depending on the codec 22, 24, 26, and 28 selected to encode and decode a frame of the speech signal 18.

1.1 Bit Allocation for the Full-Rate Codec

Referring now to FIGS. 2, 3, and 4, the full-rate bitstream of the full-rate codec 22 will be described. Referring now to FIG. 4, the bit allocation for the full-rate codec 22 includes a line spectrum frequency (LSF) component 140, a type component 142, an adaptive codebook component 144, a fixed codebook component 146 and a gain component 147. The gain component 147 comprises an adaptive codebook gain component 148 and a fixed codebook gain component 150. The bitstream allocation is further defined by a Type Zero column 152 and a Type One column 154. The Type Zero and Type One columns 152 and 154 designate the allocation of the bits in the bitstream based on the type classification of the speech signal 18 as previously discussed. In one embodiment, the Type Zero column 152 and the Type One column 154 both use 4 subframes of 5 milliseconds each to process the speech signals 18.

The LSF component 140 is generated based on the short-term predictor parameters. The short-term predictor parameters are converted to a plurality of line spectrum frequencies (LSFs). The LSFs represent the spectral envelope of a frame. In addition, a plurality of predicted LSFs from the LSFs of previous frames are determined. The predicted LSFs are subtracted from the LSFs to create an LSFs prediction error. In one embodiment, the LSFs prediction error comprises a vector of 10 parameters. The LSF prediction error is combined with the predicted LSFs to generate a plurality of quantized LSFs. The quantized LSFs are interpolated and converted to form a plurality of quantized LPC coefficients Aq(z) for each subframe as will be discussed in detail later. In addition, the LSFs prediction error is quantized to generate the LSF component 140 that is transmitted to the decoding system 16.

When the bitstream is received at the decoding system 16, the LSF component 140 is used to locate a quantized vector representing a quantized LSFs prediction error. The quantized LSFs prediction error is added to the predicted LSFs to generate quantized LSFs. The predicted LSFs are determined from the LSFs of previous frames within the decoding system 16 similarly to the encoding system 12. The resulting quantized LSFs may be interpolated for each subframe using a predetermined weighting. The predetermined weighting defines an interpolation path that may be fixed or variable. The interpolation path is between the quantized LSFs of the previous frame and the quantized LSFs of the current frame. The interpolation path may be used to provide a spectral envelope representation for each subframe in the current frame.

For frames classified as Type Zero, one embodiment of the LSF component 140 is encoded utilizing a plurality of stages 156 and an interpolation element 158 as illustrated in FIG. 4. The stages 156 represent the LSFs prediction error used to code the LSF component 140 for a frame. The interpolation element 158 may be used to provide a plurality of interpolation paths between the quantized LSFs of the previous frame and the

quantized LSFs of the frame currently being processed. In general, the interpolation element 158 represents selectable adjustment in the contour of the line spectrum frequencies (LSFs) during decoding. Selectable adjustment may be used due to the non-stationary spectral nature of frames that are classified as Type Zero. For frames classified as Type One, the LSF component 140 may be encoded using only the stages 156 and a predetermined linear interpolation path due to the stationary spectral nature of such frames.

One embodiment of the LSF component 140 includes 2 bits to encode the interpolation element 158 for frames classified as Type Zero. The bits identify the particular interpolation path. Each of the interpolation paths adjust the weighting of the previous quantized LSFs for each subframe and the weighting of the current quantized LSFs for each subframe. Selection of an interpolation path may be determined based on the degree of variations in the spectral envelope between subsequent subframes. For example, if there is substantial variation in the spectral envelope in the middle of the frame, the interpolation element 158 selects an interpolation path that decreases the influence of the quantized LSFs from the previous frame. One embodiment of the interpolation element 158 can represent any one of four different interpolation paths for each subframe.

The predicted LSFs may be generated using a plurality of moving average predictor coefficients. The predictor coefficients determine how much of the LSFs of past frames are used to predict the LSFs of the current frame. The predictor coefficients within the full-rate codec 22 use an LSF predictor coefficients table. The table may be generally illustrated by the following matrix:

	E1 _{1,}	E12,	 E1 _n	
1	•	•	 •	ļ
:	•		 •	
:		•	 •	i
;	•	•	 . `	i
· ·	•	•	 •	
	Em ₁ ,	Em_2	 Em_n	

TABLE 1

In one embodiment, m equals 2 and n equals 10. Accordingly, the prediction order is two and there are two vectors of predictor coefficients, each comprising 10 elements. One embodiment of the LSF predictor coefficients table is titled "Float64 B_85k" and is included in Appendix B of the attached microfiche appendix.

Once the predicted LSFs have been determined, the LSFs prediction error may be calculated using the actual LSFs. The LSFs prediction error may be quantized using a full dimensional multi-stage quantizer. An LSF prediction error quantization table containing a plurality of quantization vectors represents each stage 156 that may be used with the multi-stage quantizer. The multistage quantizer determines a portion of the LSF component 140 for each stage 156. The determination of the portion of the LSF component 140 is based on a pruned search approach. The pruned search approach determines promising quantization vector candidates from each stage. At the conclusion of the determination of candidates for all the stages, a decision occurs simultaneously that selects the best quantization vectors for each stage.

In the first stage, the multistage quantizer determines a plurality of candidate first stage quantization errors. The candidate first stage quantization errors are the difference between the LSFs prediction error and the closest matching quantization vectors located in the first stage. The multistage quantizer then determines a

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plurality of candidate second stage quantization errors by identifying the quantization vectors located in the second stage that best match the candidate first stage quantization errors. This iterative process is completed for each of the stages and promising candidates are kept from each stage. The final selection of the best representative quantization vectors for each stage simultaneously occurs when the candidates have been determined for all the stages. The LSF component 140 includes index locations of the closest matching quantization vectors from each stage. One embodiment of the LSF component 140 includes 25 bits to encode the index locations within the stages 156. The LSF prediction error quantization table for the quantization approach may be illustrated generally by the following matrix:

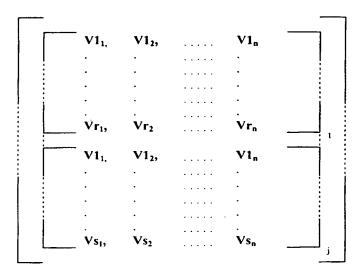


TABLE 2

One embodiment of the quantization table for both the Type Zero and the Type One classification uses four stages (j=4) in which each quantization vector is represented by 10 elements (n=10). The stages 156 of this embodiment include 128 quantization vectors (r=128) for one of the stages 156, and 64 quantization vectors (s=64) in the remaining stages 156. Accordingly, the index location of the quantization vectors within the stages 156 may be encoded using 7 bits for the one of the stages 156 that includes 128 quantization vectors. In addition, index locations for each of the stages 156 that include 64 quantization vectors may be encoded using 6 bits. One embodiment of the LSF prediction error quantization table used for both the Type Zero and Type One classification is titled "Float64 CBes_85k" and is included in Appendix B of the attached microfiche appendix.

Within the decoding system 16, the F0 or F1 LPC reconstruction modules 108, 110 in the full-rate decoder 90 obtain the LSF component 140 from the bitstream as illustrated in FIG. 3. The LSF component 140 may be used to reconstruct the quantized LSFs as previously discussed. The quantized LSFs may be interpolated and converted to form the linear prediction coding coefficients for each subframe of the current frame.

For Type Zero classification, reconstruction may be performed by the F0 LPC reconstruction module 108. Reconstruction involves determining the predicted LSFs, decoding the quantized LSFs prediction error and reconstructing the quantized LSFs. In addition, the quantized LSFs may be interpolated using the identified interpolation path. As previously discussed, one of the four interpolation paths is identified to the F0 LPC

reconstruction module 108 by the interpolation element 158 that forms a part of the LSF component 140. Reconstruction of the Type One classification involves the use of the predetermined linear interpolation path and the LSF prediction error quantization table by the F1 LPC reconstruction module 110. The LSF component 140 forms part of the first portion of the bitstream since it is encoded on a frame basis in both the Type Zero and the Type One classifications.

The type component 142 also forms part of the first portion of the bitstream. As illustrated in FIG. 2, the F type selector module 68 generates the type component 142 to represent the type classification of a particular frame. Referring now to FIG. 3, the F type selector module 102 in the full-rate decoder 90 receives the type component 142 from the bitstream.

One embodiment of the adaptive codebook component 144 may be an open loop adaptive codebook component 144a or a closed loop adaptive codebook component 144b. The open or closed loop adaptive codebook component 144a, 144b is generated by the initial full frame-processing module 46 or the F0 first subframe-processing module 70, respectively, as illustrated in FIG. 2. The open loop adaptive codebook component 144a may be replaced by the closed loop adaptive codebook component 144b in the bitstream when the frame is classified as Type Zero. In general, the open loop designation refers to processing on a frame basis that does not involve analysis-by-synthesis (ABS). The closed loop processing is performed on a subframe basis and includes analysis-by-synthesis (ABS).

Encoding the pitch lag, which is based on the periodicity of the speech signal 18, generates the adaptive codebook component 144. The open loop adaptive codebook component 144a is generated for a frame; whereas the closed loop adaptive codebook component 144b is generated on a subframe basis. Accordingly, the open loop adaptive codebook component 144a is part of the first portion of the bitstream and the closed loop adaptive codebook component 144b is part of the second portion of the bitstream. In one embodiment, as illustrated in Fig. 4, the open loop adaptive codebook component 144a comprises 8 bits and the closed loop adaptive codebook component 144b comprises 26 bits. The open loop adaptive codebook component 144a and the closed loop adaptive codebook component 144b may be generated using an adaptive codebook vector that will be described later. Referring now to FIG. 3, the decoding system 16 receives the open or closed loop adaptive codebook component 144a or 144b. The open or closed loop adaptive codebook component 144a or 144b is decoded by the F0 or F1 excitation reconstruction module 104 or 106, respectively.

One embodiment of the fixed codebook component 146 may be a Type Zero fixed codebook component 146a or a Type One fixed codebook component 146b. The Type Zero fixed codebook component 146a is generated by the F0 first subframe-processing module 70 as illustrated in FIG. 2. The F1 subframe-processing module 72 generates the Type One fixed codebook component 146b. The Type Zero or Type One fixed codebook component 146a or 146b is generated using a fixed codebook vector and synthesis-by-analysis on a subframe basis that will be described later. The fixed codebook component 146 represents the long-term residual of a subframe using an n-pulse codebook, where n is the number of pulses in the codebook.

Referring now to Fig. 4, the Type Zero fixed codebook component 146a of one embodiment comprises 22 bits per subframe. The Type Zero fixed codebook component 146a includes identification of one of a plurality of n-pulse codebooks, pulse locations in the codebook, and the signs of representative pulses (quantity "n") that

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correspond to the pulse locations. In an example embodiment, up to two bits designate which one of three n-pulse codebooks has been encoded. Specifically, the first of the two bits is set to "1" to designate the first of the three n-pulse codebooks is used. If the first bit is set to "0," the second of the two bits designates whether the second or the third of the three n-pulse codebooks are used. Accordingly, in the example embodiment, the first of the three n-pulse codebooks has 21 bits to represent the pulse locations and signs, and the second and third of the three n-pulse codebooks have 20 bits available.

Each of the representative pulses within one of the n-pulse codebooks includes a corresponding track. The track is a list of sample locations in a subframe where each sample location in the list is one of the pulse locations. A subframe being encoded may be divided into a plurality of sample locations where each of the sample locations contains a sample value. The tracks of the corresponding representative pulses list only a portion of the sample locations from a subframe. Each of the representative pulses within one of the n-pulse codebooks may be represented by one of the pulse locations in the corresponding track.

During operation, each of the representative pulses is sequentially placed in each of the pulse locations in the corresponding track. The representative pulses are converted to a signal that may be compared to the sample values in the sample locations of the subframe using ABS. The representative pulses are compared to the sample values in those sample locations that are later in time than the sample location of the pulse location. The pulse location that minimizes the difference between the representative pulse and the sample values that are later in time forms a portion of the Type Zero fixed codebook component 146a. Each of the representative pulses in a selected n-pulse codebook may be represented by a corresponding pulse location that forms a portion of the Type Zero fixed codebook component 146a. The tracks are contained in track tables that can generally be represented by the following matrix:

	P1 ₁ , P2 ₁ , P3 ₁ , P4 ₁ ,	P1 ₂ , P2 ₂ , P3 ₂ , P4 ₂ ,	 P1 _f P2 _g P3 _h P4 _i	
•		•	 •	
	•	•	 •	
	Pn ₁ ,	Pn ₂	 Pn_j	

TABLE 3

One embodiment of the track tables is the tables entitled "static short track_5_4_0," "static short track_5_3_2," and "static short track_5_3_1" within the library titled "tracks.tab" that is included in Appendix B of the attached microfiche appendix.

In the example embodiment illustrated in Fig. 4, the n-pulse codebooks are three 5-pulse codebooks 160 where the first of the three 5-pulse codebooks 160 includes 5 representative pulses therefore n=5. A first representative pulse has a track that includes 16 (f=16) of the 40 sample locations in the subframe. The first representative pulse from the first of the three 5-pulse codebooks 160 are compared with the sample values in the sample locations. One of the sample locations present in the track associated with the first representative pulse is identified as the pulse location using 4 bits. The sample location that is identified in the track is the sample

location in the subframe that minimizes the difference between the first representative pulse and the sample values that are later in time as previously discussed. Identification of the pulse location in the track forms a portion of the Type Zero fixed codebook component 146a.

In this example embodiment, the second and fourth representative pulses have corresponding tracks with 16 sample locations (g and i = 16) and the third and fifth representative pulses have corresponding tracks with 8 sample locations (h and j = 8). Accordingly, the pulse locations for the second and fourth representative pulses are identified using 4 bits and the pulse locations of the third and fifth representative pulses are identified using 3 bits. As a result, the Type Zero fixed codebook component 146a for the first of the three 5-pulse codebooks 160 includes 18 bits for identifying the pulse locations.

The signs of the representative pulses in the identified pulse locations may also be identified in the Type Zero fixed codebook component 146a. In the example embodiment, one bit represents the sign for the first representative pulse, one bit represents a combined sign for both the second and fourth representative pulses and one bit represents the combined sign for the third and the fifth representative pulses. The combined sign uses the redundancy of the information in the pulse locations to transmit two distinct signs with a single bit. Accordingly, the Type Zero fixed codebook component 146a for the first of the three 5-pulse codebooks 160 includes three bits for the sign designation for a total of 21 bits.

In an example embodiment, the second and third of the three 5-pulse codebooks 160 also include 5 representative pulses (n=5) and the tracks in the track table each comprise 8 sample locations (f,g,h,i,j = 8). Accordingly, the pulse locations for each of the representative pulses in the second and third of the three 5-pulse codebook 160 are identified using 3 bits. In addition, in this example embodiment, the signs for each of the pulse locations are identified using 1 bit.

For frames classified as Type One, in an example embodiment, the n-pulse codebook is an 8-pulse codebook 162 (n=8). The 8-pulse codebook 162 is encoded using 30 bits per subframe to create one embodiment of the Type One fixed codebook component 146b. The 30 bits includes 26 bits identifying pulse locations using tracks as in the Type Zero classification, and 4 bits identifying the signs. One embodiment of the track table is the table entitled "static INT16 track_8_4_0" within the library titled "tracks.tab" that is included in Appendix B of the attached microfiche appendix.

In the example embodiment, the tracks associated with the first and fifth representative pulses comprise 16 sample locations that are encoded using 4 bits. The tracks associated with the remaining representative pulses comprise 8 sample locations that are encoded using 3 bits. The first and fifth representative pulses, the second and sixth representative pulses, the third and seventh representative pulses, and the fourth and eighth representative pulses use the combined signs for both respective representative pulses. As illustrated in FIG. 3, when the bitstream is received by the decoding system 16, the F0 or the F1 excitation reconstruction modules 104 or 106 decode the pulse locations of the tracks. The pulse locations of the tracks are decoded by the F0 or the F1 excitation reconstruction modules 104 or 106 for one of the three 5-pulse codebooks 160 or the 8-pulse codebook 162, respectively. The fixed codebook component 146 is part of the second portion of the bitstream since it is generated on a subframe basis.

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Referring again to FIG. 4, the gain component 147, in general, represents the adaptive and fixed codebook gains. For Type Zero classification, the gain component 147 is a Type Zero adaptive and fixed codebook gain component 148a, 150a representing both the adaptive and the fixed codebook gains. The Type Zero adaptive and fixed codebook gain component 148a, 150a is part of the second portion of the bitstream since it is encoded on a subframe basis. As illustrated in FIG. 2, the Type Zero adaptive and fixed codebook gain component 148a, 150a is generated by the F0 first subframe-processing module 70.

For each subframe of a frame classified as Type Zero, the adaptive and fixed codebook gains are jointly coded by a two-dimensional vector quantizer (2D VQ) 164 to generate the Type Zero adaptive and fixed codebook gain component 148a, 150a. In one embodiment, quantization involves translating the fixed codebook gain into a fixed codebook energy in units of decibels (dB). In addition, a predicted fixed codebook energy may be generated from the quantized fixed codebook energy values of previous frames. The predicted fixed codebook energy may be derived using a plurality of fixed codebook predictor coefficients.

Similar to the LSFs predictor coefficients, the fixed codebook predictor coefficients determine how much of the fixed codebook energy of past frames may be used to predict the fixed codebook energy of the current frame. The predicted fixed codebook energy is subtracted from the fixed codebook energy to generate a prediction fixed codebook energy error. By adjusting the weighting of the previous frames and the current frames for each subframe, the predicted fixed codebook energy may be calculated to minimize the prediction fixed codebook error.

The prediction fixed codebook energy error is grouped with the adaptive codebook gain to form a two-dimensional vector. Following quantization of the prediction fixed codebook energy error and the adaptive codebook gain, as later described, the two-dimensional vector may be referred to as a quantized gain vector (\hat{g}_{ac}). The two-dimensional vector is compared to a plurality of predetermined vectors in a 2D gain quantization table. An index location is identified that is the location in the 2D gain quantization table of the predetermined vector that best represents the two-dimensional vector. The index location is the adaptive and fixed codebook gain component 148a and 150a for the subframe. The adaptive and fixed codebook gain component 148a and 150a for the frame represents the indices identified for each of the subframes.

The predetermined vectors comprise 2 elements, one representing the adaptive codebook gain, and one representing the prediction fixed codebook energy error. The 2D gain quantization table may be generally represented by:

TABLE 4

The two-dimensional vector quantizer (2D VQ) 164, of one embodiment, utilizes 7 bits per subframe to identify the index location of one of 128 quantization vectors (n=128). One embodiment of the 2D gain

quantization table is entitled "Float64 gainVQ_2_128_8_5" and is included in Appendix B of the attached microfiche appendix.

For frames classified as Type One, a Type One adaptive codebook gain component 148b is generated by the F1 first frame-processing module 72 as illustrated in FIG. 2. Similarly, the F1 second frame-processing module 76 generates a Type One fixed codebook gain component 150b. The Type One adaptive codebook gain component 148b and the Type One fixed codebook gain component 150b are generated on a frame basis to form part of the first portion of the bitstream.

Referring again to FIG. 4, the Type One adaptive codebook gain component 148b is generated using a multi-dimensional vector quantizer that is a four-dimensional pre vector quantizer (4D pre VQ) 166 in one embodiment. The term "pre" is used to highlight that, in one embodiment, the adaptive codebook gains for all the subframes in a frame are quantized prior to the search in the fixed codebook for any of the subframes. In an alternative embodiment, the multi-dimensional quantizer is an n dimensional vector quantizer that quantizes vectors for n subframes where n may be any number of subframes.

The vector quantized by the four-dimensional pre vector quantizer (4D pre VQ) 166 is an adaptive codebook gain vector, with elements that represent each of the adaptive codebook gains from each of the subframes. Following quantization, as will be later discussed, the adaptive codebook gain vector can also be referred to as a quantized pitch gain (\hat{g}^k_a). Quantization of the adaptive codebook gain vector to generate the adaptive codebook gain component 148b is performed by searching in a pre-gain quantization table. The pre-gain quantization table includes a plurality of predetermined vectors that may be searched to identify the predetermined vector that best represents the adaptive codebook gain vector. The index location of the identified predetermined vector within the pre-gain quantization table is the Type One adaptive codebook component 148b. The adaptive codebook gain component 148b of one embodiment comprises 6 bits.

In one embodiment, the predetermined vectors comprise 4 elements, 1 element for each subframe. Accordingly, the pre-gain quantization table may be generally represented as:

TABLE 5

One embodiment of the pre-gain quantization table includes 64 predetermined vectors (n=64). An embodiment of the pre-gain quantization table is entitled "Float64 gp4_tab" and is included in Appendix B of the attached microfiche appendix.

The Type One fixed codebook gain component 150b may be similarly encoded using a multi-dimensional vector quantizer for n subframes. In one embodiment, the multi-dimensional vector quantizer is a four-dimensional delayed vector quantizer (4D delayed VQ) 168. The term "delayed" highlights that the quantization of the fixed codebook gains for the subframes occurs only after the search in the fixed codebook for

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all the subframes. Referring again to FIG. 2, the F1 second frame-processing module 76 determines the fixed codebook gain for each of the subframes. The fixed codebook gain may be determined by first buffering parameters generated on a sub-frame basis until the entire frame has been processed. When the frame has been processed, the fixed codebook gains for all of the subframes are quantized using the buffered parameters to generate the Type One fixed codebook gain component 150b. In one embodiment, the Type One fixed codebook gain component 150b comprises 10 bits as illustrated in FIG. 4.

The Type One fixed codebook gain component 150b is generated by representing the fixed-codebook gains with a plurality of fixed codebook energies in units of decibels (dB). The fixed codebook energies are quantized to generate a plurality of quantized fixed codebook energies, which are then translated to create a plurality of quantized fixed-codebook gains. In addition, the fixed codebook energies are predicted from the quantized fixed codebook energy errors of the previous frames to generate a plurality of predicted fixed codebook energies. The difference between the predicted fixed codebook energies and the fixed codebook energies is a plurality of prediction fixed codebook energy errors. In one embodiment, different prediction coefficients may be used for each of 4 subframes to generate the predicted fixed codebook energies. In this example embodiment, the predicted fixed codebook energies of the first, the second, the third, and the fourth subframe are predicted from the 4 quantized fixed codebook energy errors of the previous frame. The prediction coefficients for the first, second, third, and fourth subframes of this example embodiment may be {0.7, 0.6, 0.4, 0.2}, {0.4, 0.2, 0.1, 0.05}, {0.3, 0.2, 0.075, 0.025}, and {0.2, 0.075, 0.025, 0.0}, respectively.

The prediction fixed codebook energy errors may be grouped to form a fixed codebook gain vector that, when quantized, may be referred to as a quantized fixed codebook gain (\hat{g}^k_c) . In one embodiment, the prediction fixed codebook energy error for each subframe represent the elements in the vector. The prediction fixed codebook energy errors are quantized using a plurality of predetermined vectors in a delayed gain quantization table. During quantization, a perceptual weighing measure may be incorporated to minimize the quantization error. An index location that identifies the predetermined vector in the delayed gain quantization table is the fixed codebook gain component 150b for the frame.

The predetermined vectors in the delayed gain quantization table of one embodiment includes 4 elements. Accordingly, the delayed gain quantization table may be represented by the previously discussed Table 5. One embodiment of the delayed gain quantization table includes 1024 predetermined vectors (n=1024). An embodiment of the delayed gain quantization table is entitled "Float64 gainVQ_4_1024" and is included in Appendix B of the attached microfiche appendix.

Referring again to FIG. 3, the fixed and adaptive codebook gain components 148 and 150 may be decoded by the full-rate decoder 90 within the decoding system 16 based on the type classification. The F0 excitation reconstruction module 104 decodes the Type Zero adaptive and fixed codebook gain component 148a, 150a. Similarly, the Type One adaptive codebook gain component 148b and the Type One fixed gain component 150b are decoded by the F1 excitation reconstruction module 106.

Decoding of the fixed and adaptive codebook gain components 158 and 160 involves generation of the respective predicted gains, as previously discussed, by the full-rate decoder 90. The respective quantized vectors from the respective quantization tables are then located using the respective index locations. The respective

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quantized vectors are then assembled with the respective predicted gains to generate respective quantized codebook gains. The quantized codebook gains generated from the Type Zero fixed and adaptive gain component 148a and 150a represent the values for both the fixed and adaptive codebook gains for a subframe. The quantized codebook gain generated from the Type One adaptive codebook gain component 148b and the Type One fixed codebook gain component 150b represents the values for the fixed and adaptive codebook gains, respectively, for each subframe in a frame.

1.2 Bit Allocation for the Half-Rate Codec

Referring now to FIGS. 2, 3 and 5, the half-rate bitstream of the half-rate codec 24 will be described. The half-rate codec 24 is in many respects similar to the full-rate codec 22 but has a different bit allocation. As such, for purposes of brevity, the discussion will focus on the differences. Referring now to FIG. 5, the bitstream allocation of one embodiment of the half-rate codec 24 includes a line spectrum frequency (LSF) component 172, a type component 174, an adaptive codebook component 176, a fixed codebook component 178, and a gain component 179. The gain component 179 further comprises an adaptive codebook gain component 180 and a fixed codebook gain component 182. The bitstream of the half-rate codec 24 also is further defined by a Type Zero column 184 and a Type One column 186. In one embodiment, the Type Zero column 184 uses two subframes of 10 milliseconds each containing 80 samples. The Type One column 186, of one embodiment, uses three subframes where the first and second subframes contain 53 samples and the third subframe contains 54 samples.

Although generated similarly to the full-rate codec 22, the LSF component 172 includes a plurality of stages 188 and a predictor switch 190 for both the Type Zero and the Type One classifications. In addition, one embodiment of the LSF component 172 comprises 21 bits that form part of the first portion of the bitstream. The initial half frame-processing module 48 illustrated in FIG. 2, generates the LSF component 172 similarly to the full-rate codec 22. Referring again to FIG. 5, the half-rate codec 24 of one embodiment includes three stages 188, two with 128 vectors and one with 64 vectors. The three stages 188 of the half-rate codec 24 operate similarly to the full-rate codec 22 for frames classified as Type One with the exception of the selection of a set of predictor coefficients as discussed later. The index location of each of the 128 vectors is identified with 7 bits and the index location of each of the 64 vectors is identified with 6 bits. One embodiment of the LSF prediction error quantization table for the half-rate codec 24 is titled "Float64 CBes_40k" and is included in Appendix B of the attached microfiche appendix.

The half-rate codec 24 also differs from the full-rate codec 22 in selecting between sets of predictor coefficients. The predictor switch 190 of one embodiment identifies one of two possible sets of predictor coefficients using one bit. The selected set of predictor coefficients may be used to determine the predicted line spectrum frequencies (LSFs), similar to the full-rate codec 22. The predictor switch 190 determines and identifies which of the sets of predictor coefficients will best minimize the quantization error. The sets of predictor coefficients may be contained in an LSF predictor coefficient table that may be generally illustrated by the following matrix:

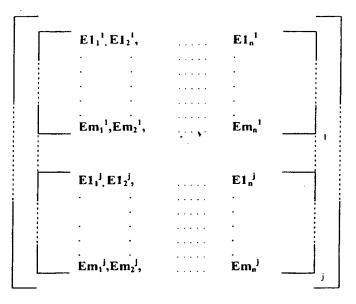


TABLE 6

In one embodiment there are four predictor coefficients (m=4) in each of two sets (j=2) that comprise 10 elements each (n=10). The LSF predictor coefficient table for the half-rate codec 24 in one embodiment is titled "Float64 B_40k" and is included in Appendix B of the attached microfiche appendix. Referring again to FIG. 3, the LSF prediction error quantization table and the LSF predictor coefficient table are used by the H LPC reconstruction module 118 within the decoding system 16. The H LPC reconstruction module 118 receives and decodes the LSF component 172 from the bitstream to reconstruct the quantized frame LSFs. Similar to the full-rate codec 22, for frames classified as Type One, the half-rate codec 24 uses a predetermined linear interpolation path. However, the half-rate codec 24 uses the predetermined linear interpolation path for frames classified as both Type Zero and Type One.

The adaptive codebook component 176 in the half-rate codec 24 similarly models the pitch lag based on the periodicity of the speech signal 18. The adaptive codebook component 176 is encoded on a subframe basis for the Type Zero classification and a frame basis for the Type One classification. As illustrated in FIG. 2, the initial half frame-processing module 48 encodes an open loop adaptive codebook component 176a for frames with the Type One classification. For frames with the Type Zero classification, the H0 first subframe-processing module 80 encodes a closed loop adaptive codebook component 176b.

Referring again to FIG. 5, one embodiment of the open loop adaptive codebook component 176a is encoded by 7 bits per frame and the closed loop adaptive codebook component 176b is encoded by 7 bits per subframe. Accordingly, the Type Zero adaptive codebook component 176a is part of the first portion of the bitstream, and the Type One adaptive codebook component 176b is part of the second portion of the bitstream. As illustrated in FIG. 3, the decoding system 16 receives the closed loop adaptive codebook component 176b. The closed loop adaptive codebook component 176b is decoded by the half-rate decoder 92 using the H0 excitation reconstruction module 114. Similarly, the H1 excitation reconstruction module 116 decodes the open loop adaptive codebook component 176a.

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One embodiment of the fixed codebook component 178 for the half-rate codec 24 is dependent on the type classification to encode the long-term residual as in the full-rate codec 22. Referring again to FIG. 2, a Type Zero fixed codebook component 178a or a Type One fixed codebook component 178b is generated by the H0 first subframe-processing module 80 or the H1 second subframe-processing module 84, respectively. Accordingly, the Type Zero and Type One fixed codebook components 178a and 178b form a part of the second portion of the bitstream.

Referring again to FIG. 5, the Type Zero fixed codebook component 178a of an example embodiment is encoded using 15 bits per subframe with up to two bits identify the codebook to be used as in the full-rate codec 22. Encoding the Type Zero fixed codebook component 178a involves use of a plurality of n-pulse codebooks that are a 2-pulse codebook 192 and a 3-pulse codebook 194 in the example embodiment. In addition, in this example embodiment, a gaussian codebook 195 is used that includes entries that are random excitation. For the n-pulse codebooks, the half-rate codec 24 uses the track tables similarly to the full-rate codec 22. In one embodiment, the track table entitled "static INT16 track_2_7_1," "static INT16 track_1_3_0," and "static INT16 track_3_2_0" included in the library entitled "tracks.tab" in Appendix B of the microfiche appendix are used.

In an example embodiment of the 2-pulse codebook 192, each track in the track table includes 80 sample locations for each representative pulse. The pulse locations for both the first and second representative pulses are encoded using 13 bits. Encoding 1 of the 80 possible pulse locations is accomplished in 13 bits by identifying the pulse location for the first representative pulse, multiplying the pulse location by 80 and adding the pulse location of the second representative pulse to the result. The end result is a value that can be encoded in 13 bits with an additional bit used to represent the signs of both representative pulses as in the full-rate codec 22.

In an example embodiment of the 3-pulse codebook 194, the pulse locations are generated by the combination of a general location, that may be one of 16 sample locations defined by 4 bits, and a relative displacement there from. The relative displacement may be 3 values representing each of the 3 representative pulses in the 3-pulse codebook 194. The values represent the location difference away from the general location and may be defined by 2 bits for each representative pulse. The signs for the three representative pulses may be each defined by one bit such that the total bits for the pulse location and the signs is 13 bits.

The gaussian codebook 195 generally represents noise type speech signals that may be encoded using two orthogonal basis random vectors. The Type Zero fixed codebook component 178a represents the two orthogonal based random vectors generated from the gaussian codebook 195. The Type Zero fixed codebook component 178a represents how to perturbate a plurality of orthogonal basis random vectors in a gaussian table to increase the number of orthogonal basis random vectors without increasing the storage requirements. In an example embodiment, the number of orthogonal basis random vectors is increased from 32 vectors to 45 vectors. A gaussian table that includes 32 vectors with each vector comprising 40 elements represents the gaussian codebook of the example embodiment. In this example embodiment, the two orthogonal basis random vectors used for encoding are interleaved with each other to represent 80 samples in each subframe. The gaussian codebook may be generally represented by the following matrix:

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	G1 _{1,} G2 _{1,}	G1 ₂ , G2 ₂ ,	 G1 _n G2 _n
:	•	•	 •
:		•	 •
		•	
	G32 ₁ ,	G32 ₂	 G32 _n

TABLE 7

One embodiment of the gaussian codebook 195 is titled "double bv" and is included in Appendix B of the attached microfiche appendix. For the example embodiment of the gaussian codebook 195, 11 bits identify the combined indices (location and perturbation) of both of the two orthogonal basis random vectors used for encoding, and 2 bits define the signs of the orthogonal basis random vectors.

Encoding the Type One fixed codebook component 178b involves use of a plurality of n-pulse codebooks that are a 2-pulse codebook 196 and a 3-pulse codebook 197 in the example embodiment. The 2-pulse codebook 196 and the 3-pulse codebook 197 function similarly to the 2-pulse codebook 192 and the 3-pulse codebook 194 of the Type Zero classification, however the structure is different. The Type One fixed codebook component 178b of an example embodiment is encoded using 13 bits per subframe. Of the 13 bits, 1 bit identifies the 2-pulse codebook 196 or the 3-pulse codebook 197 and 12 bits represent the respective pulse locations and the signs of the representative pulses. In the 2-pulse codebook 196 of the example embodiment, the tracks include 32 sample locations for each representative pulse that are encoded using 5 bits with the remaining 2 bits used for the sign of each representative pulse. In the 3-pulse codebook 197, the general location includes 8 sample locations that are encoded using 4 bits. The relative displacement is encoded by 2 bits and the signs for the representative pulses are encoded in 3 bits similar to the frames classified as Type Zero.

Referring again to FIG. 3, the decoding system 16 receives the Type Zero or Type One fixed codebook components 178a and 178b. The Type Zero or Type One fixed codebook components 178a and 178b are decoded by the H0 excitation reconstruction module 114 or the H1 reconstruction module 116, respectively. Decoding of the Type Zero fixed codebook component 178a occurs using an embodiment of the 2-pulse codebook 192, the 3-pulse codebook 194, or the gaussian codebook 195. The Type One fixed codebook component 178b is decoded using the 2-pulse codebook 196 or the 3-pulse codebook 197.

Referring again to FIG. 5, one embodiment of the gain component 179 comprises a Type Zero adaptive and fixed codebook gain component 180a and 182a. The Type Zero adaptive and fixed codebook gain component 180a and 182a may be quantized using the two-dimensional vector quantizer (2D VQ) 164 and the 2D gain quantization table (Table 4), used for the full-rate codec 22. In one embodiment, the 2D gain quantization table is entitled "Float64 gainVQ 3 128", and is included in Appendix B of the attached microfiche appendix.

Type One adaptive and fixed codebook gain components 180b and 182b may also be generated similarly to the full-rate codec 22 using multi-dimensional vector quantizers. In one embodiment, a three-dimensional pre vector quantizer (3D preVQ) 198 and a three-dimensional delayed vector quantizer (3D delayed VQ) 200 are used for the adaptive and fixed gain components 180b and 182b, respectively. The vector quantizers 198 and 200

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perform quantization using respective gain quantization tables. In one embodiment, the gain quantization tables are a pre-gain quantization table and a delayed gain quantization table for the adaptive and fixed codebook gains, respectively. The multi-dimensional gain tables may be similarly structured and include a plurality of predetermined vectors. Each multi-dimensional gain table in one embodiment comprises 3 elements for each subframe of a frame classified as Type One.

Similar to the full-rate codec 22, the three-dimensional pre vector quantizer (3D preVQ) 198 for the adaptive gain component 180b may quantize directly the adaptive gains. In addition, the three-dimensional delayed vector quantizer (3D delayed VQ) 200 for the fixed gain component 182b may quantize the fixed codebook energy prediction error. Different prediction coefficients may be used to predict the fixed codebook energy for each subframe. In one preferred embodiment, the predicted fixed codebook energies of the first, the second, and the third subframes are predicted from the 3 quantized fixed codebook energy errors of the previous frame. In this example embodiment, the predicted fixed codebook energies of the first, the second, and the third subframes are predicted using the set of coefficients {0.6, 0.3, 0.1}, {0.4, 0.25, 0.1}, and {0.3, 0.15, 0.075}, respectively.

The gain quantization tables for the half-rate codec 24 may be generally represented as:

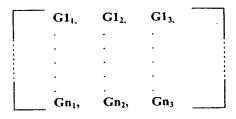


TABLE 8

One embodiment of the pre-gain quantization table used by the three-dimensional pre vector quantizer (3D preVQ) 198 includes 16 vectors (n=16). The three-dimensional delayed vector quantizer (3D delayed VQ) 200 uses one embodiment of the delayed gain quantization table that includes 256 vectors (n=256). The gain quantization tables for the pre vector quantizer (3D preVQ) 198 and the delayed vector quantizer (3D delayed VQ) 200 of one embodiment are entitled "Float64 gp3_tab" and "Float64 gainVQ_3_256", respectively, and are included in Appendix B of the attached microfiche appendix.

Referring again to FIG. 2, the Type Zero adaptive and fixed codebook gain component 180a and 182a is generated by the H0 first subframe-processing module 80. The H1 first frame-processing module 82 generates the Type One adaptive codebook gain component 180b. Similarly, the Type One fixed codebook gain component 182b is generated by the H1 second frame-processing module 86. Referring again to FIG. 3, the decoding system 16 receives the Type Zero adaptive and fixed codebook gain component 180a and 182a. The Type Zero adaptive and fixed codebook gain component 180a and 182a is decoded by the H0 excitation reconstruction module 114 based on the type classification. Similarly, the H1 excitation reconstruction module 116 decodes the Type One adaptive gain component 180b and the Type One fixed codebook gain component 182b.

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1.3 Bit Allocation for the Quarter-Rate Codec

Referring now to FIGS. 2, 3 and 6, the quarter-rate bitstream of the quarter-rate codec 26 will now be explained. The illustrated embodiment of the quarter-rate codec 26 operates on both a frame basis and a subframe basis but does not include the type classification as part of the encoding process as in the full and half-rate codecs 22 and 24. Referring now to FIG. 6, the bitstream generated by quarter-rate codec 26 includes an LSF component 202 and an energy component 204. One embodiment of the quarter-rate codec 26 operates using two subframes of 10 milliseconds each to process frames using 39 bits per frame.

The LSF component 202 is encoded on a frame basis using a similar LSF quantization scheme as the full-rate codec 22 when the frame is classified as Type Zero. The quarter-rate codec 26 utilizes an interpolation element 206 and a plurality of stages 208 to encode the LSFs to represent the spectral envelope of a frame. One embodiment of the LSF component 202 is encoded using 27 bits. The 27 bits represent the interpolation element 206 that is encoded in 2 bits and four of the stages 208 that are encoded in 25 bits. The stages 208 include one stage encoded using 7 bits and three stages encoded using 6 bits. In one embodiment, the quarter rate codec 26 uses the exact quantization table and predictor coefficients table used by the full rated codec 22. The quantization table and the predictor coefficients table of one embodiment are titled "Float64 CBes_85k" and "Float64 B_85k", respectively, and are included in Appendix B of the attached microfiche appendix.

The energy component 204 represents an energy gain that may be multiplied by a vector of similar yet random numbers that may be generated by both the encoding system 12 and the decoding system 16. In one embodiment, the energy component 204 is encoded using 6 bits per subframe. The energy component 204 is generated by first determining the energy gain for the subframe based on the random numbers. In addition, a predicted energy gain is determined for the subframe based on the energy gain of past frames.

The predicted energy gain is subtracted from the energy gain to determine an energy gain prediction error. The energy gain prediction error is quantized using an energy gain quantizer and a plurality of predetermined scalars in an energy gain quantization table. Index locations of the predetermined scalars for each subframe may be represented by the energy component 204 for the frame.

The energy gain quantization table may be generally represented by the following matrix:

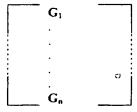


TABLE 9

In one embodiment, the energy gain quantization table contains 64 (n=64) of the predetermined scalars. An embodiment of the energy gain quantization table is entitled "Float64 gainSQ_1_64" and is included in Appendix B of the attached microfiche appendix.

In FIG. 2, the LSF component 202 is encoded on a frame basis by the initial quarter frame-processing module 50. Similarly, the energy component 204 is encoded by the quarter rate module 60 on a subframe basis.

BNSDOCID: <WO___0122402A1_!_>

Referring now to FIG. 3, the decoding system 16 receives the LSF component 202. The LSF component 202 is decoded by the Q LPC reconstruction module 122 and the energy component 204 is decoded by the Q excitation reconstruction module 120. Decoding the LSF component 202 is similar to the decoding methods for the full-rate codec 22 for frames classified as Type One. The energy component 204 is decoded to determine the energy gain. A vector of similar yet random numbers generated within the decoding system 16 may be multiplied by the energy gain to generate the short-term excitation.

1.4 Bit Allocation for the Eighth-Rate Codec

In FIGS. 2, 3, and 7, the eighth-rate bitstream of the eighth-rate codec 28 may not include the type classification as part of the encoding process and may operate on a frame basis only. Referring now to Fig. 7, similar to the quarter rate codec 26, the bitstream of the eighth-rate codec 28 includes an LSF component 240 and an energy component 242. The LSF component 240 may be encoded using a similar LSF quantization scheme as the full-rate codec 22, when the frame is classified as Type One. The eighth-rate codec 28 utilizes a plurality of stages 244 to encode the short-term predictor or spectral representation of a frame. One embodiment of the LSF component 240 is encoded using 11 bits per frame in three stages 244. Two of the three stages 244 are encoded in 4 bits and the last of the three stages 244 is encoded in 3 bits.

The quantization approach to generate the LSF component 240 for the eighth-rate codec 28 involves an LSF prediction error quantization table and a predictor coefficients table similar to the full-rate codec 22. The LSF prediction error quantization table and the LSF predictor coefficients table can be generally represented by the previously discussed Tables 1 and 2. In an example embodiment, the LSF quantization table for the eighth-rate codec 28 includes 3 stages (j=3) with 16 quantization vectors in two stages (r=16) and 8 quantization vectors in one stage (s=8) each having 10 elements (n=10). The predictor coefficient table of one embodiment includes 4 vectors (m=4) of 10 elements each (n=10). The quantization table and the predictor coefficients table of one embodiment are titled "Float64 CBes_08k" and "Float64 B_08k," respectively, and are included in Appendix B of the attached microfiche appendix.

In FIG. 2, the LSF component 240 is encoded on a frame basis by the initial eighth frame-processing module 52. The energy component 242 also is encoded on a frame basis by the eighth-rate module 62. The energy component 242 represents an energy gain that can be determined and coded similarly to the quarter rate codec 26. One embodiment of the energy component 242 is represent by 5 bits per frame as illustrated in Fig. 7.

Similar to the quarter rate codec 26, the energy gain and the predicted energy gain may be used to determine an energy prediction error. The energy prediction error is quantized using an energy gain quantizer and a plurality of predetermined scalars in an energy gain quantization table. The energy gain quantization table may be generally represented by Table 9 as previously discussed. The energy gain quantizer of one embodiment uses an energy gain quantization table containing 32 vectors (n=32) that is entitled "Float64 gainSQ_1_32" and is included in Appendix B of the attached microfiche appendix.

In FIG. 3, the LSF component 240 and the energy component 242 may be decoded following receipt by the decoding system 16. The LSF component 240 and the energy component 242 are decoded by the E LPC reconstruction module 126 and the E excitation reconstruction module 124, respectively. Decoding of the LSF

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component 240 is similar to the full-rate codec 22 for frames classified as Type One. The energy component 242 may be decoded by applying the decoded energy gain to a vector of similar yet random numbers as in the quarter rate codec 26.

An embodiment of the speech compression system 10 is capable of creating and then decoding a bitstream using one of the four codecs 22, 24, 26 and 28. The bitstream generated by a particular codec 22, 24, 26 and 28 may be encoded emphasizing different parameters of the speech signal 18 within a frame depending on the rate selection and the type classification. Accordingly, perceptual quality of the post-processed synthesized speech 20 decoded from the bitstream may be optimized while maintaining the desired average bit rate.

A detailed discussion of the configuration and operation of the speech compression system modules illustrated in the embodiments of FIGS. 2 and 3 is now provided. The reader is encouraged to review the source code included in Appendix A of the attached microfiche appendix in conjunction with the discussion to further enhance understanding.

2.0 PRE-PROCESSING MODULE

Referring now to Fig. 8, an expanded block diagram of the pre-processing module 34 illustrated in Fig. 2 is provided. One embodiment of the pre-processing module 34 includes a silence enhancement module 302, a high-pass filter module 304, and a noise suppression module 306. The pre-processing module 34 receives the speech signal 18 and provides a pre-processed speech signal 308.

The silence enhancement module 302 receives the speech signal 18 and functions to track the minimum noise resolution. The silence enhancement function adaptively tracks the minimum resolution and levels of the speech signal 18 around zero, and detects whether the current frame may be "silence noise." If a frame of "silence noise" is detected, the speech signal 18 may be ramped to the zero-level. Otherwise, the speech signal 18 may not be modified. For example, the A-law coding scheme can transform such an inaudible "silence noise" into a clearly audible noise. A-law encoding and decoding of the speech signal 18 prior to the pre-processing module 34 can amplify sample values that are nearly 0 to values of about + 8 or -8 thereby transforming a nearly inaudible noise into an audible noise. After processing by the silence enhancement module 302, the speech signal 18 may be provided to the high-pass filter module 304.

The high-pass filter module 304 may be a 2^{nd} order pole-zero filter, and may be given by the following transfer function H(z):

$$H(z) = \frac{0.92727435 - 1.8544941z^{-1} + 0.92727435z^{-2}}{1 - 1.9059465z^{-1} + 0.9114024z^{-2}}$$
 (Equation 1)

The input may be scaled down by a factor of 2 during the high-pass filtering by dividing the coefficients of the numerator by 2.

Following processing by the high-pass filter, the speech signal 18 may be passed to the noise suppression module 306. The noise suppression module 306 employs noise subtraction in the frequency domain and may be one of the many well-known techniques for suppressing noise. The noise suppression module 306 may include a Fourier transform program used by a noise suppression algorithm as described in section 4.1.2 of the TIA/EIA IS-

127 standard entitled "Enhanced Variable Rate Codec, Speech Service Option 3 for Wideband Spread Spectrum Digital Systems."

The noise suppression module 306 of one embodiment transforms each frame of the speech signal 18 to the frequency domain where the spectral amplitudes may be separated from the spectral phases. The spectral amplitudes may be grouped into bands, which follow the human auditory channel bands. An attenuation gain may be calculated for each band. The attenuation gains may be calculated with less emphasis on the spectral regions that are likely to have harmonic structure. In such regions, the background noise may be masked by the strong voiced speech. Accordingly, any attenuation of the speech can distort the quality of the original speech, without any perceptual improvement in the reduction of the noise.

Following calculation of the attenuation gain, the spectral amplitudes in each band may be multiplied by the attenuation gain. The spectral amplitudes may then be combined with the original spectral phases, and the speech signal 18 may be transformed back to the time domain. The time-domain signal may be overlapped-and-added to generate the pre-processed speech signal 308. The pre-processed speech signal 308 may be provided to the initial frame-processing module 44.

3.0 INITIAL FRAME PROCESSING MODULE

FIG. 9 is a block diagram of the initial frame-processing module 44, illustrated in FIG. 2. One embodiment of the initial frame-processing module 44 includes an LSF generation section 312, a perceptual weighting filter module 314, an open loop pitch estimation module 316, a characterization section 318, a rate selection module 320, a pitch pre-processing module 322, and a type classification module 324. The characterization section 318 further comprises a voice activity detection (VAD) module 326 and a characterization module 328. The LSF generation section 312 comprises an LPC analysis module 330, an LSF smoothing module 332, and an LSF quantization module 334. In addition, within the full-rate encoder 36, the LSF generation section 312 includes an interpolation module 338 and within the half-rate encoder 38, the LSF generation section includes a predictor switch module 336.

Referring to FIG. 2, the initial frame-processing module 44 operates to generate the LSF components 140, 172, 202 and 240, as well as determine the rate selection and the type classification. The rate selection and type classification control the processing by the excitation-processing module 54. The initial frame-processing module 44 illustrated in Fig. 9 is illustrative of one embodiment of the initial full frame-processing module 46 and the initial half frame-processing module 48. Embodiments of the initial quarter frame-processing module 50 and the initial eighth frame-processing module 52 differ to some degree.

As previously discussed, in one embodiment, type classification does not occur for the initial quarter-rate frame-processing module 50 and the initial eighth-rate frame-processing module 52. In addition, the long-term predictor and the long-term predictor residual are not processed separately to represent the energy component 204 and 242 illustrated in FIGS. 6 and 7. Accordingly, only the LSF section 312, the characterization section 318 and the rate selection module 320 illustrated in Fig. 9 are operable within the initial quarter-rate frame-processing module 50 and the initial eighth-rate frame-processing module 52.

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To facilitate understanding of the initial frame-processing module 44, a general overview of the operation will first be discussed followed by a detailed discussion. Referring now to FIG. 9, the pre-processed speech signal 308 initially is provided to the LSF generation section 312, the perceptual weighting filter thodule 314 and the characterization section 318. However, some of the processing within the characterization section 318 is dependent on the processing that occurs within the open loop pitch estimation module 316. The LSF generation section 312 estimates and encodes the spectral representation of the pre-processed speech signal 308. The perceptual weighting filter module 314 operates to provide perceptual weighting during coding of the pre-processed speech signal 308 according to the natural masking that occurs during processing by the human auditory system. The open loop pitch estimation module 316 determines the open loop pitch lag for each frame. The characterization section 318 analyzes the frame of the pre-processed speech signal 308 and characterizes the frame to optimize subsequent processing.

During, and following, the processing by the characterization section 318, the resulting characterizations of the frame may be used by the pitch pre-processing module 322 to generate parameters used in generation of the closed loop pitch lag. In addition, the characterization of the frame is used by the rate selection module 320 to determine the rate selection. Based on parameters of the pitch lag determined by the pitch pre-processing module 322 and the characterizations, the type classification is determined by the type classification module 324.

3.1 LPC Analysis Module

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The pre-processed speech signal 308 is received by the LPC analysis module 330 within the LSF generation section 312. The LPC analysis module 330 determines the short-term prediction parameters used to generate the LSF component 312. Within one embodiment of the LPC analysis module 330, there are three 10th order LPC analyses performed for a frame of the pre-processed speech signal 308. The analyses may be centered within the second quarter of the frame, the fourth quarter of the frame, and a lookahead. The lookahead is a speech segment that overhangs into the next frame to reduce transitional effects. The analysis within the lookahead includes samples from the current frame and from the next frame of the pre-processed speech signal 308.

Different windows may be used for each LPC analysis within a frame to calculate the linear prediction coefficients. The LPC analyses in one embodiment are performed using the autocorrelation method to calculate autocorrelation coefficients. The autocorrelation coefficients may be calculated from a plurality of data samples within each window. During the LPC analysis, bandwidth expansion of 60Hz and a white noise correction factor of 1.0001 may be applied to the autocorrelation coefficients. The bandwidth expansion provides additional robustness against signal and round-off errors during subsequent encoding. The white noise correction factor effectively adds a noise floor of –40dB to reduce the spectral dynamic range and further mitigate errors during subsequent encoding.

A plurality of reflection coefficients may be calculated using a Leroux-Gueguen algorithm from the autocorrelation coefficients. The reflection coefficients may then be converted to the linear prediction coefficients. The linear prediction coefficients may be further converted to the LSFs (Line Spectrum Frequencies), as previously discussed. The LSFs calculated within the fourth quarter may be quantized and sent

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to the decoding system 16 as the LSF component 140, 172, 202, 240. The LSFs calculated within the second quarter may be used to determine the interpolation path for the full-rate encoder 36 for frames classified as Type Zero. The interpolation path is selectable and may be identified with the interpolation element 158. In addition, the LSFs calculated within the second quarter and the lookahead may be used in the encoding system 12 to generate the short term residual and a weighted speech that will be described later.

3.2 LSF Smoothing Module

During stationary background noise, the LSFs calculated within the fourth quarter of the frame may be smoothed by the LSF smoothing module 332 prior to quantizing the LSFs. The LSFs are smoothed to better preserve the perceptual characteristic of the background noise. The smoothing is controlled by a voice activity determination provided by the VAD module 326 that will be later described and an analysis of the evolution of the spectral representation of the frame. An LSF smoothing factor is denoted β_{lsf} . In an example embodiment:

- 1. At the beginning of "smooth" background noise segments, the smoothing factor may be ramped quadratically from 0 to 0.9 over 5 frames.
- 2. During "smooth" background noise segments the smoothing factor may be 0.9.
- 3. At the end of "smooth" background noise segments the smoothing factor may be reduced to 0 instantaneously.
- 4. During non-"smooth" background noise segments the smoothing factor may be 0. According to the LSF smoothing factor the LSFs for the quantization may be calculated as: $lsf_n(k) = \beta_{lsf} \cdot lsf_{n-1}(k) + \left(1 \beta_{lsf}\right) \cdot lsf_2(k), \qquad k = 1,2,...,10$ (Equation 2)

where $lsf_n(k)$ and $lsf_{n-1}(k)$ represents the smoothed LSFs of the current and previous frame, respectively, and $lsf_2(k)$ represents the LSFs of the LPC analysis centered at the last quarter of the current frame.

3.3 LSF Quantization Module

The 10th order LPC model given by the smoothed LSFs (Equation 2) may be quantized in the LSF domain by the LSF quantization module 334. The quantized value is a plurality of quantized LPC coefficients A_q(z) 342. The quantization scheme uses an nth order moving average predictor. In one embodiment, the quantization scheme uses a 2nd order moving average predictor for the full-rate codec 22 and the quarter rate codec 26. For the half-rate codec 24, a 4th order moving average switched predictor may be used. For the eighth rate codec 28, a 4th order moving average predictor may be used. The quantization of the LSF prediction error may be performed by multi-stage codebooks, in the respective codecs as previously discussed.

The error criterion for the LSFs quantization is a weighted mean squared error measure. The weighting for the weighted mean square error is a function of the LPC magnitude spectrum. Accordingly, the objective of the quantization may be given by:

$$\left\{ \hat{lsf}_{n}(1), \hat{lsf}_{n}(1), ..., \hat{lsf}_{n}(10) \right\} = \arg\min \left\{ \sum_{k=1}^{10} w_{i} \cdot \left(\hat{lsf}_{n}(k) - \hat{lsf}_{n}(k) \right)^{2} \right\},$$
(Equation 3)

where the weighting may be:

$$\mathbf{w}_{i} = \left| P(\mathsf{lsf}_{n}(i)) \right|^{0.4}, \tag{Equation 4}$$

and |P(f)| is the LPC power spectrum at frequency f (the index n denotes the frame number). In the example embodiment, there are 10 coefficients.

In one embodiment, the ordering property of the quantized LPC coefficients $A_q(z)$ 342 is checked. If one LSF pair is flipped they may be re-ordered. When two or more LSF pairs are flipped, the quantized LPC coefficients $A_q(z)$ 342 may be declared erased and may be reconstructed using the frame erasure concealment of the decoding system 16 that will be discussed later. In one embodiment, a minimum spacing of 50Hz between adjacent coefficients of the quantized LPC coefficients $A_q(z)$ 342 may be enforced.

3.4 Predictor Switch Module

The predictor switch module 336 is operable within the half-rate codec 24. The predicted LSFs may be generated using moving average predictor coefficients as previously discussed. The predictor coefficients determine how much of the LSFs of past frames are used to predict the LSFs of the current frame. The predictor switch module 336 is coupled with the LSFs quantization module 334 to provide the predictor coefficients that minimize the quantization error as previously discussed.

3.5 LSF Interpolation Module

The quantized and unquantized LSFs may also be interpolated for each subframe within the full-rate codec 22. The quantized and unquantized LSFs are interpolated to provide quantized and unquantized linear prediction parameters for each subframe. The LSF interpolation module 338 chooses an interpolation path for frames of the full-rate codec 22 with the Type Zero classification, as previously discussed. For all other frames, a predetermined linear interpolation path may be used.

The LSF interpolation module 338 analyzes the LSFs of the current frame with respect to the LSFs of previous frames and the LSFs that were calculated at the second quarter of the frame. An interpolation path may be chosen based on the degree of variations in the spectral envelope between the subframes. The different interpolation paths adjust the weighting of the LSFs of the previous frame and the weighting of the LSFs of the current frame for the current subframe as previously discussed. Following adjustment by the LSF interpolation module 338, the interpolated LSFs may be converted to predictor coefficients for each subframe.

For Type One classification within the full-rate codec 22, as well as for the half-rate codec 24, the quarter-rate codec 26, and the eighth-rate codec 28, the predetermined linear interpolation path may be used to adjust the weighting. The interpolated LSFs may be similarly converted to predictor coefficients following interpolation. In addition, the predictor coefficients may be further weighted to create the coefficients that are used by perceptual weighting filter module 314.

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3.6 Perceptual Weighting Filter Module

The perceptual weighting filter module 314 is operable to receive and filter the pre-processed speech signal 308. Filtering by the perceptual weighting filter module 314 may be performed by emphasizing the valley areas and de-emphasizing the peak areas of the pre-processed speech signal 308. One embodiment of the perceptual weighting filter module 314 has two parts. The first part may be the traditional pole-zero filter given by:

$$W_1(z) = \frac{A(\frac{z}{\gamma_1})}{A(\frac{z}{\gamma_2})},$$
 (Equation 5)

where $A(z/\gamma_1)$ and $1/A(z/\gamma_2)$ are a zeros-filter and a poles-filter, respectively. The prediction coefficients for the zeros-filter and the poles-filter may be obtained from the interpolated LSFs for each subframe and weighted by γ_1 and γ_2 , respectively. In an example embodiment of the perceptual weighting filter module 314, the weighting is $\gamma_1 = 0.9$ and $\gamma_2 = 0.5$. The second part of the perceptual weighting filter module 314 may be an adaptive low-pass filter given by:

$$W_2(z) = \frac{1}{1 - \eta z^{-1}}$$
 (Equation 6)

where η is a function of stationary long-term spectral characteristics that will be later discussed. In one embodiment, if the stationary long-term spectral characteristics have the typical tilt associated with public switched telephone network (PSTN), then $\eta = 0.2$, otherwise, $\eta = 0.0$. The typical tilt is commonly referred to as a modified IRS characteristic or spectral tilt. Following processing by the perceptual weighting filter module 314, the pre-processed speech signal 308 may be described as a weighted speech 344. The weighted speech 344 is provided to the open loop pitch estimation module 316.

3.7 Open Loop Pitch Estimation Module

The open loop pitch estimation module 316 generates the open loop pitch lag for a frame. In one embodiment, the open loop pitch lag actually comprises three open loop pitch lags, namely, a first pitch lag for the first half of the frame, a second pitch lag for the second half of the frame, and a third pitch lag for the lookahead portion of the frame.

For every frame, the second and third pitch lags are estimated by the open loop pitch estimation module 316 based on the current frame. The first open loop pitch lag is the third open loop pitch lag (the lookahead) from the previous frame that may be further adjusted. The three open loop pitch lags are smoothed to provide a continuous pitch contour. The smoothing of the open loop pitch lags employs a set of heuristic and ad-hoc decision rules to preserve the optimal pitch contour of the frame. The open-loop pitch estimation is based on the weighted speech 344 denoted by $s_w(n)$. The values estimated by the open loop pitch estimation module 316 in one embodiment are lags that range from 17 to 148.

The first, second and third open loop pitch lags may be determined using a normalized correlation, R(k) that may be calculated according to

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$$R(k) = \frac{\sum_{n=0}^{79} s_{w}(n) \cdot s_{w}(n-k)}{\sqrt{\left(\sum_{n=0}^{79} s_{w}(n) \cdot s_{w}(n)\right) \left(\sum_{n=0}^{79} s_{w}(n-k) \cdot s_{w}(n-k)\right)}}$$
(Equation 7)

Where n=79 in the example embodiment to represent the number of samples in the subframe. The maximum normalized correlation R(k) for each of a plurality of regions is determined. The regions may be four regions that represent four sub-ranges within the range of possible lags. For example, a first region from 17-33 lags, a second region from 34-67 lags, a third region from 68-137 lags, and a fourth region from 138-148 lags. One open loop pitch lag corresponding to the lag that maximizes the normalized correlation values R(k) from each region are the initial pitch lag candidates. A best candidate from the initial pitch lag candidates is selected based on the normalized correlation, characterization information, and the history of the open loop pitch lag. This procedure may be performed for the second pitch lag and for the third pitch lag.

Finally, the first, second, and third open loop pitch lags may be adjusted for an optimal fitting to the overall pitch contour and form the open loop pitch lag for the frame. The open loop pitch lag is provided to the pitch pre-processing module 322 for further processing that will be described later. The open loop pitch estimation module 316 also provides the pitch lag and normalized correlation values at the pitch lag. The normalized correlation values at the pitch lag are called a pitch correlation and are notated as R_p . The pitch correlation R_p is used in characterizing the frame within the characterization section 318.

3.8 Characterization Section

The characterization section 318 is operable to analyze and characterize each frame of the pre-processed speech signal 308. The characterization information is utilized by a plurality of modules within the initial frame-processing module 44 as well by the excitation-processing module 54. Specifically, the characterization information is used in the rate selection module 320 and the type classification module 324. In addition, the characterization information may be used during quantization and coding, particularly in emphasizing the perceptually important features of the speech using a class-dependent weighting approach that will be described later.

Characterization of the pre-processed speech signal 308 by the characterization section 318 occurs for each frame. Operation of one embodiment of the characterization section 318 may be generally described as six categories of analysis of the pre-processed speech signal 308. The six categories are: voice activity determination, the identification of unvoiced noise-like speech, a 6-class signal characterization, derivation of a noise-to-signal ratio, a 4-grade characterization, and a characterization of a stationary long term spectral characteristic.

3.9 Voice Activity Detection (VAD) Module

The voice activity detection (VAD) module 326 performs voice activity determination as the first step in characterization. The VAD module 326 operates to determine if the pre-processed speech signal 308 is some form of speech or if it is merely silence or background noise. One embodiment of the VAD module 326 detects voice activity by tracking the behavior of the background noise. The VAD module 326 monitors the difference

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between parameters of the current frame and parameters representing the background noise. Using a set of predetermined threshold values, the frame may be classified as a speech frame or as a background noise frame.

The VAD module 326 operates to determine the voice activity based on monitoring a plurality of parameters, such as, the maximum of the absolute value of the samples in the frame, as well as the reflection coefficients, the prediction error, the LSFs and the 10th order autocorrelation coefficients provided by the LPC analysis module 330. In addition, an example embodiment of the VAD module 326 uses the parameters of the pitch lag and the adaptive codebook gains used by the VAD module 326 are from the previous frames since pitch lags and adaptive codebook gains of the current frame are not yet available. The voice activity determination performed by the VAD module 326 may be used to control several aspects of the encoding system 12, as well as forming part of a final class characterization decision by the characterization module 328.

3.10 Characterization Module

Following the voice activity determination by the VAD module 326, the characterization module 328 is activated. The characterization module 328 performs the second, third, fourth and fifth categories of analysis of the pre-processed speech signal 308 as previously discussed. The second category is the detection of unvoiced noise-like speech frames.

3.10.1 Unvoiced Noise-Like Speech Detection

In general, unvoiced noise-like speech frames do not include a harmonic structure, whereas voiced frames do. The detection of an unvoiced noise-like speech frame, in one embodiment, is based on the preprocessed speech signal 308, and a weighted residual signal $R_w(z)$ given by:

$$R_{w}(Z) = A(\frac{z}{\gamma_{1}}) \cdot S(z)$$
 (Equation 8)

Where $A(z/\gamma_1)$ represents a weighted zeros-filter with the weighting γ_1 and S(z) is the pre-processed speech signal 308. A plurality of parameters, such as the following six parameters may be used to determine if the current frame is unvoiced noise-like speech:

- 1. The energy of the pre-processed speech signal 308 over the first \(\frac{1}{2} \) of the frame.
- 2. A count of the speech samples within the frame that are under a predetermined threshold.
- 3. A residual sharpness determined using a weighted residual signal and the frame size. The sharpness is given by the ratio of the average of the absolute values of the samples to the maximum of the absolute values of the samples. The weighted residual signal may be determined from Equation 8.
- 4. A first reflection coefficient representing the tilt of the magnitude spectrum of the pre-process speechsignal 308.
- 5. The zero crossing rate of the pre-processed speech signal 308.
 - 6. A prediction measurement between the pre-processed speech signal 308 and the weighted residual signal. In one embodiment, a set of predetermined threshold values are compared to the above listed parameters in making the determination of whether a frame is unvoiced noise-like speech. The resulting determination may

be used in controlling the pitch pre-processing module 322, and in the fixed codebook search, both of which will be described later. In addition, the unvoiced noise-like speech determination is used in determining the 6-class signal characterization of the pre-processed speech signal 308.

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3.10.2 6-Class Signal Characterization

The characterization module 328 may also perform the third category of analysis that is the 6-class signal characterization. The 6-class signal characterization is performed by characterizing the frame into one of 6 classes according to the dominant features of the frame. In one embodiment, the 6 classes may be described as:

- 0. Silence/Background Noise
- 1. Stationary Noise-Like Unvoiced Speech
- 2. Non-Stationary Unvoiced
- Onset
- 4. Non-Stationary Voiced
- 5. Stationary Voiced

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In an alternative embodiment, other classes are also included such as frames characterized as plosive. Initially, the characterization module 328 distinguishes between silence/background noise frames (class 0), non-stationary unvoiced frames (class 2), onset frames (class 3), and voiced frames represented by class 4 and 5. Characterization of voiced frames as Non-Stationary (class 4) and Stationary (class 5) may be performed during activation of the pitch pre-processing module 322. Furthermore, the characterization module 328 may not initially distinguish between stationary noise-like unvoiced frames(class 1) and non-stationary unvoiced frames(class 2). This characterization class may also be identified during processing by the pitch pre-processing module 322 using the determination by the unvoiced noise-like speech algorithm previously discussed.

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The characterization module 328 performs characterization using, for example, the pre-processed speech signal 308 and the voice activity detection by the VAD module 326. In addition, the characterization module 328 may utilize the open loop pitch lag for the frame and the normalized correlation R_p corresponding to the second open loop pitch lag.

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A plurality of spectral tilts and a plurality of absolute maximums may be derived from the pre-processed speech signal 308 by the characterization module 328. In an example embodiment, the spectral tilts for 4 overlapped segments comprising 80 samples each are calculated. The 4 overlapped segments may be weighted by a Hamming window of 80 samples. The absolute maximums of an example embodiment are derived from 8 overlapped segments of the pre-processed speech signal 308. In general, the length of each of the 8 overlapped segments is about 1.5 times the period of the open loop pitch lag. The absolute maximums may be used to create a smoothed contour of the amplitude envelope.

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The spectral tilt, the absolute maximum, and the pitch correlation R_p parameters may be updated or interpolated multiple times per frame. Average values for these parameters may also be calculated several times for frames characterized as background noise by the VAD module 326. In an example embodiment, 8 updated estimates of each parameter are obtained using 8 segments of 20 samples each. The estimates of the parameters

for the background noise may be subtracted from the estimates of parameters for subsequent frames not characterized as background noise to create a set of "noise cleaned" parameters.

A set of statistically based decision parameters may be calculated from the "noise clean" parameters and the open loop pitch lag. Each of the statistically based decision parameters represents a statistical property of the original parameters, such as, averaging, deviation, evolution, maximum, or minimums. Using a set of predetermined threshold parameters, initial characterization decisions may be made for the current frame based on the statistical decision parameters. Based on the initial characterization decision, past characterization decisions, and the voice activity decision of the VAD module 326, an initial class decision may be made for the frame. The initial class decision characterizes the frame as one of the classes 0, 2, 3, or as a voiced frame represented by classes 4 and 5.

3.10.3 Noise-to-Signal Ratio Derivation

In addition to the frame characterization, the characterization module 328 of one embodiment also performs the fourth category of analysis by deriving a noise-to-signal ratio (NSR). The NSR is a traditional distortion criterion that may be calculated as the ratio between an estimate of the background noise energy and the frame energy of a frame. One embodiment of the NSR calculation ensures that only true background noise is included in the ratio by using a modified voice activity decision. The modified voice activity decision is derived using the initial voice activity decision by the VAD module 326, the energy of the frame of the pre-processed speech signal 308 and the LSFs calculated for the lookahead portion. If the modified voice activity decision indicates that the frame is background noise, the energy of the background noise is updated.

The background noise is updated from the frame energy using, for example, moving average. If the energy level of the background noise is larger than the energy level of the frame energy, it is replaced by the frame energy. Replacement by the frame energy can involve shifting the energy level of the background noise lower and truncating the result. The result represents the estimate of the background noise energy that may be used in the calculation of the NSR.

Following calculation of the NSR, the characterization module 328 performs correction of the initial class decision to a modified class decision. The correction may be performed using the initial class decision, the voice activity determination and the unvoiced noise-like speech determination. In addition, previously calculated parameters representing, for example, the spectrum expressed by the reflection coefficients, the pitch correlation R_p, the NSR, the energy of the frame, the energy of the previous frames, the residual sharpness and a sharpness of the weighted speech may also be used. The correction of the initial class decision is called characterization tuning. Characterization tuning can change the initial class decision, as well as set an onset condition flag and a noisy voiced flag if these conditions are identified. In addition, tuning can also trigger a change in the voice activity decision by the VAD module 326.

3.10.4 4-Grade Characterization

The characterization module 328 can also generate the fifth category of characterization, namely, the 4-grade characterization. The 4-grade characterization is a parameter that controls the pitch pre-processing module

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322. One embodiment of the 4-grade characterization distinguishes between 4 categories. The categories may be labeled numerically from 1 to 4. The category labeled 1 is used to reset the pitch pre-processing module 322 in order to prevent accumulated delay that exceeds a delay budget during pitch pre-processing. In general, the remaining categories indicate increasing voicing strength. Increasing voicing strength is a measure of the periodicity of the speech. In an alternative embodiment, more or less categories could be included to indicate the levels of voicing strength.

3.10.5 Stationary Long-Term Spectral Characteristics

The characterization module 328 may also performs the sixth category of analysis by determining the stationary long-term spectral characteristics of the pre-processed speech signal 308. The stationary long-term spectral characteristic is determined over a plurality of frames using, for example, spectral information such as the LSFs, the 6-class signal characterization and the open loop pitch gain. The determination is based on long-term averages of these parameters.

3.11 Rate Selection Module

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Following the modified class decision by the characterization module 328, the rate selection module 320 can make an initial rate selection called an open loop rate selection. The rate-selection module 320 can use, for example, the modified class decision, the NSR, the onset flag, the residual energy, the sharpness, the pitch correlation Rp, and spectral parameters such as the reflection coefficients in determining the open-loop rate selection. The open loop rate selection may also be selected based on the Mode that the speech compression system 10 is operating within. The rate selection module 320 is tuned to provide the desired average bit rate as indicated by each of the Modes. The initial rate selection may be modified following processing by the pitch preprocessing module 322 that will be described later.

3.12 Pitch Pre-Processing Module

The pitch pre-processing module 322 operates on a frame basis to perform analysis and modification of the weighted speech 344. The pitch pre-processing module 322 may, for example, uses compression or dilation techniques on pitch cycles of the weighted speech 344 in order to improve the encoding process. The open loop pitch lag is quantized by the pitch pre-processing module 322 to generate the open loop adaptive codebook component 144a or 176a, as previously discussed with reference to FIGS. 2, 4 and 5. If the final type classification of the frame is Type One, this quantization represents the pitch lag for the frame. However, if the type classification is changed following processing by the pitch pre-processing module 322, the pitch lag quantization also is changed to represent the closed loop adaptive codebook component 144b or 176b, as previously discussed with reference to FIGS. 2, 4 and 5.

The open loop pitch lag for the frame that was generated by the open loop pitch estimation module 316 is quantized and interpolated, to create a pitch track 348. In general, the pitch pre-processing module 322 attempts to modify the weighted speech 344 to fit the pitch track 348. If the modification is successful, the final type

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classification of the frame is Type One. If the modification is unsuccessful the final type classification of the frame is Type Zero.

As further detailed later, the pitch pre-processing modification procedure can perform continuous time warping of the weighted speech 344. The warping introduces a variable delay. In one example embodiment, the maximum variable delay within the encoding system 12 is 20 samples (2.5 ms). The weighted speech 344 may be modified on a pitch cycle-by-pitch cycle basis, with certain overlap between adjacent pitch cycles to avoid discontinuities between the reconstructed/modified segments. The weighted speech 344 may be modified according to the pitch track 348 to generate a modified weighted speech 350. In addition, a plurality of unquantized pitch gains 352 are generated by the pitch pre-processing module 322. If the type classification of the frame is Type One, the unquantized pitch gains 352 are used to generate the Type One adaptive codebook gain component 148b (for full rate codec 22) or 180b (for half-rate codec 24). The pitch track 348, the modified weighted speech 350 and the unquantized pitch gains 352 are provided to the excitation-processing module 54.

As previously discussed, the 4-grade characterization by the characterization module 328 controls the pitch pre-processing. In one embodiment, if the frame is predominantly background noise or unvoiced with low pitch correlation, such as, category 1, the frame remains unchanged and the accumulated delay of the pitch pre-processing is reset to zero. If the frame is pre-dominantly pulse-like unvoiced, such as, category 2, the accumulated delay may be maintained without any warping of the signal except for a simple time shift. The time shift may be determined according to the accumulated delay of the input speech signal 18. For frames with the remaining 4-grade characterizations, the core of the pitch pre-processing algorithm may be executed in order to optimally warp the signal.

In general, the core of the pitch pre-processing module 322 in one embodiment performs three main tasks. First, the weighted speech 344 is modified in an attempt to match the pitch track 348. Second, a pitch gain and a pitch correlation for the signal are estimated. Finally, the characterization of the speech signal 18 and the rate selection is refined based on the additional signal information obtained during the pitch pre-processing analysis. In another embodiment, additional pitch pre-processing may be included, such as, waveform interpolation. In general, waveform interpolation may be used to modify certain irregular transition segments using forward-backward waveform interpolation techniques to enhance the regularities and suppress the irregularities of the weighted speech 344.

3.12.1 Modification

Modification of the weighted speech 344 provides a more accurate fit of the weighted speech 344 into a pitch-coding model that is similar to the Relaxed Code Excited Linear Prediction (RCELP) speech coding approach. An example of an implementation of RCELP speech coding is provided in the TIA (Telecommunications Industry Association) IS-127 standard. Performance of the modification without any loss of perceptual quality can include a fine pitch search, estimation of a segment size, target signal warping, and signal warping. The fine pitch search may be performed on a frame level basis while the estimation of a segment size, the target signal warping, and the signal warping may be executed for each pitch cycle.

3.12.1.1 Fine Pitch Search

The fine pitch search may be performed on the weighted speech 344, based on the previously determined second and third pitch lags, the rate selection, and the accumulated pitch pre-processing delay. The fine pitch search searches for fractional pitch lags. The fractional pitch lags are non-integer pitch lags that combine with the quantization of the lags. The combination is derived by searching the quantization tables of the lags used to quantize the open loop pitch lags and finding lags that maximize the pitch correlation of the weighted speech 344. In one embodiment, the search is performed differently for each codec due to the different quantization techniques associated with the different rate selections. The search is performed in a search area that is identified by the open loop pitch lag and is controlled by the accumulated delay.

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3.12.1.2 Estimate segment size

The segment size follows the pitch period, with some minor adjustments. In general, the pitch complex (the main pulses) of the pitch cycle are located towards the end of a segment in order to allow for maximum accuracy of the warping on the perceptual most important part, the pitch complex. For a given segment the starting point is fixed and the end point may be moved to obtain the best model fit. Movement of the end point effectively stretches or compresses the time scale. Consequently, the samples at the beginning of the segment are hardly shifted, and the greatest shift will occur towards the end of the segment.

3.12.1.3 Target signal for warping

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One embodiment of the target signal for time warping is a synthesis of the current segment derived from the modified weighted speech 350 that is represented by $s_w'(n)$ and the pitch track 348 represented by $L_p(n)$. According to the pitch track 348, $L_p(n)$, each sample value of the target signal $s_w'(n)$, $n = 0,..., N_s - 1$ may be obtained by interpolation of the modified weighted speech 350 using a 21^{st} order Hamming weighted Sinc window,

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$$s'_{w}(n) = \sum_{i=-10}^{10} w_{s}(f(L_{p}(n)), i) \cdot s'_{w}(n - i(L_{p}(n))), \text{ for } n = 0,..., N_{s} - 1$$

(Equation 9)

where $i(L_p(n))$ and $f(L_p(n))$ are the integer and fractional parts of the pitch lag, respectively; $w_s(f,i)$ is the Hamming weighted Sinc window, and N_s is the length of the segment. A weighted target, $s_w^{wt}(n)$, is given by $s_w^{wt}(n) = w_e(n) \cdot s_w^t(n)$. The weighting function, $w_e(n)$, may be a two-piece linear function, which emphasizes the pitch complex and de-emphasizes the "noise" in between pitch complexes. The weighting may be adapted according to the 4-grade classification, by increasing the emphasis on the pitch complex for segments of higher periodicity.

The integer shift that maximizes the normalized cross correlation between the weighted target $s_w^{wr}(n)$ and the weighted speech 344 is $s_w(n+\tau_{\rm acc})$, where $s_w(n+\tau_{\rm acc})$ is the weighted speech 344 shifted according to an accumulated delay $\tau_{\rm acc}$ may be found by maximizing

$$R(\tau_{\text{shift}}) = \frac{\sum_{n=0}^{N_{-}-1} s_{w}^{\text{vr}}(n) \cdot s_{w}(n + \tau_{\text{acc}} + \tau_{\text{shift}})}{\sqrt{\left(\sum_{n=0}^{N_{+}-1} s_{w}^{\text{vr}}(n)^{2}\right) \cdot \left(\sum_{n=0}^{N_{+}-1} s_{w}(n + \tau_{\text{acc}} + \tau_{\text{shift}})^{2}\right)}}.$$
(Equation 10)

A refined (fractional) shift may be determined by searching an upsampled version of $R(\tau_{\rm shift})$ in the vicinity of $\tau_{\rm shift}$. This may result in a final optimal shift $\tau_{\rm opt}$ and the corresponding normalized cross correlation $R_n(\tau_{\rm opt})$.

3.12.1.4 Signal Warping

The modified weighted speech 350 for the segment may be reconstructed according to the mapping given

$$\left[s_w(n+\tau_{\rm acc}), s_w(n+\tau_{\rm acc}+\tau_{\rm c}+\tau_{\rm opt})\right] \rightarrow \left[s_w'(n), s_w'(n+\tau_{\rm c}-1)\right], \text{ (Equation 11)}$$

and

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$$\left[s_w(n+\tau_{\rm acc}+\tau_c+\tau_{\rm opt}), s_w(n+\tau_{\rm acc}+\tau_{\rm opt}+N_s-1)\right] \rightarrow \left[s_w'(n+\tau_c), s_w'(n+N_s-1)\right]$$
(Equation 12)

where τ_c is a parameter defining the warping function. In general, τ_c specifies the beginning of the pitch complex. The mapping given by Equation 11 specifies a time warping, and the mapping given by Equation 12 specifies a time shift (no warping). Both may be carried out using a Hamming weighted Sinc window function.

3.12.2 Pitch Gain and Pitch Correlation Estimation

The pitch gain and pitch correlation may be estimated on a pitch cycle basis and are defined by Equations 11 and 12, respectively. The pitch gain is estimated in order to minimize the mean squared error between the target $s'_w(n)$, defined by Equation 9, and the final modified signal $s'_w(n)$, defined by Equations 11 and 12, and may be given by

$$g_{a} = \frac{\sum_{n=0}^{N_{s}-1} s'_{w}(n) \cdot s'_{w}(n)}{\sum_{n=0}^{N_{s}-1} s'_{w}(n)^{2}}.$$
 (Equation 13)

25 The pitch gain is provided to the excitation-processing module 54 as the unquantized pitch gains 352. The pitch correlation may be given by

$$R_{a} = \frac{\sum_{n=0}^{N_{w}-1} s'_{w}(n) \cdot s'_{w}(n)}{\sqrt{\left(\sum_{n=0}^{N_{w}-1} s'_{w}(n)^{2}\right) \cdot \left(\sum_{n=0}^{N_{w}-1} s'_{w}(n)^{2}\right)}}.$$
 (Equation 14)

Both parameters are available on a pitch cycle basis and may be linearly interpolated.

3.12.3 Refined Classification and Refined Rate Selection

Following pitch pre-processing by the pitch pre-processing module 322, the average pitch correlation and the pitch gains are provided to the characterization module 328 and the rate selection module 320. The characterization module 328 and the rate selection module 320 create a final characterization class and a final rate selection, respectively, using the pitch correlation and the pitch gains. The final characterization class and the final rate selection may be determined by refining the 6-class signal characterization and the open loop rate selection of the frame.

Specifically, the characterization module 328 determines whether a frame with a characterization as a voiced frame should be characterized as class 4 - "Non-Stationary Voiced", or class 5 - "Stationary Voiced." In addition, a final determination that a particular frame is stationary noise-like unvoiced speech may occur based on the previous determination that the particular frame is modified unvoiced noise-like speech. Frames confirmed to be noise-like unvoiced speech may be characterized as class 1, "Stationary Noise-Like Unvoiced Speech."

Based on the final characterization class, the open loop rate selection by the rate selection module 320 and the half rate signaling flag on the half rate signal line 30 (FIG. 1), a final rate selection may be determined. The final rate selection is provided to the excitation-processing module 54 as a rate selection indicator 354. In addition, the final characterization class for the frame is provided to the excitation-processing module 54 as control information 356.

3.13 Type Classification Module

For the full rate codec 22 and the half rate codec 24, the final characterization class may also be used by the type classification module 324. A frame with a final characterization class of class 0 to 4 is determined to be a Type Zero frame, and a frame of class 5 is determined to be a Type One frame. The type classification is provided to the excitation-processing module 54 as a type indicator 358.

4.0 EXCITATION PROCESSING MODULE

The type indicator 358 from the type classification module 324 selectively activates either the full-rate module 54 or the half-rate module 56, as illustrated in FIG. 2, depending on the rate selection. FIG. 10 is a block diagram representing the F0 or H0 first subframe-processing module 70 or 80 illustrated in FIG. 2 that is activated for the Type Zero classification. Similarly, FIG. 11 is a block diagram representing the F1 or H1 first frame processing module 72 or 82, the F1 or H1 second subframe processing module 74 or 84 and the F1 or H1 second

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frame processing module 76 or 86 that are activated for Type One classification. As previously discussed, the "F" and "H" represent the full-rate codec 22 and the half-rate codec 24, respectively.

Activation of the quarter-rate module 60 and the eighth-rate module 62 illustrated in FIG. 2 may be based on the rate selection. In one embodiment, a pseudo-random sequence is generated and scaled to represent the short-term excitation. The energy component 204 and 242 (FIG. 2) represents the scaling of the pseudo-random sequence, as previously discussed. In one embodiment, the "seed" used for generating the pseudo-random sequence is extracted from the bitstream, thereby providing synchronicity between the encoding system 12 and the decoding system 16.

As previously discussed, the excitation processing module 54 also receives the modified weighted speech 350, the unquantized pitch gains 352, the rate indicator 354 and the control information 356. The quarter and eighth rate codecs 26 and 28 do not utilize these signals during processing. However, these parameters may be used to further process frames of the speech signal 18 within the full-rate codec 22 and the half-rate codec 24. Use of these parameters by the full-rate codec 22 and the half-rate codec 24, as described later, depends on the type classification of the frame as Type Zero or Type One.

4.1 Excitation Processing Module For Type Zero Frames Of The Full-Rate Codec And The Half-Rate Codec

Referring now to FIG. 10, one embodiment of the F0 or H0 first subframe-processing module 70, 80 comprises an adaptive codebook section 362, a fixed codebook section 364 and a gain quantization section 366. The processing and coding for frames of Type Zero is somewhat similar to the traditional CELP encoding, for example, of TIA (Telecommunications Industry Association) standard IS-127. For the full-rate codec 22, the frame may be divided into four subframes, while for the half-rate codec 24, the frame may be divided into two subframes, as previously discussed. The functions represented in FIG. 10 are executed on a subframe basis.

The F0 or H0 first subframe-processing module 70 and 80 (FIG. 2) operate to determine the closed loop pitch lag and the corresponding adaptive codebook gain for the adaptive codebook. In addition, the long-term residual is quantized using the fixed codebook, and the corresponding fixed codebook gain is also determined. Quantization of the closed loop pitch lag and joint quantization of the adaptive codebook gain and the fixed codebook gain are also performed.

4.1.1 Adaptive Codebook Section

The adaptive codebook section 362 includes an adaptive codebook 368, a first multiplier 370, a first synthesis filter 372, a first perceptual weighting filter 374, a first subtractor 376 and a first minimization module 378. The adaptive codebook section 362 performs a search for the best closed loop pitch lag from the adaptive codebook 368 using the analysis-by-synthesis (ABS) approach.

A segment from the adaptive codebook 368 corresponding to the closed loop pitch lag may be referred to as an adaptive codebook vector (v_a) 382. The pitch track 348 from the pitch pre-processing module 322 of FIG. 9 may be used to identify an area in the adaptive codebook 368 to search for vectors for the adaptive codebook vector (v_a) 382. The first multiplier 370 multiplies the selected adaptive codebook vector (v_a) 382 by a gain (g_a)

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384. The gain (g_a) 384 is unquantized and represents an initial adaptive codebook gain that is calculated as will be described later. The resulting signal is passed to the first synthesis filter 372 that performs a function that is the inverse of the LPC analysis previously discussed. The first synthesis filter 372 receives the quantized LPC coefficients A_q(z) 342 from the LSF quantization module 334 and together with the first perceptual weighting filter module 374, creates a first resynthesized speech signal 386. The first subtractor 376 subtracts the first resynthesized speech signal 386 from the modified weighted speech 350 to generate a long-term error signal 388. The modified weighted speech 350 is the target signal for the search in the adaptive codebook 368.

The first minimization module 378 receives the long-term error signal 388 that is a vector representing the error in quantizing the closed loop pitch lag. The first minimization module 378 performs calculation of the energy of the vector and determination of the corresponding weighted mean squared error. In addition, the first minimization module 378 controls the search and selection of vectors from the adaptive codebook 368 for the adaptive codebook vector (v_a) 382 in order to reduce the energy of the long-term error signal 388.

The search process repeats until the first minimization module 378 has selected the best vector for the adaptive codebook vector (v_a) 382 from the adaptive codebook 368 for each subframe. The index location of the best vector for the adaptive codebook vector (v_a) 382 within the adaptive codebook 368 forms part of the closed loop adaptive codebook component 144b, 176b (FIG. 2). This search process effectively minimizes the energy of the long-term error signal 388. The best closed loop pitch lag is selected by selecting the best adaptive codebook vector (v_a) 382 from the adaptive codebook 368. The resulting long-term error signal 388 is the modified weighted speech signal 350 less the filtered best vector for the adaptive codebook vector (v_a) 382.

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4.1.1.1 Closed-Loop Adaptive Codebook Search for the Full-Rate Codec

The closed loop pitch lag for the full-rate codec 22 is represented in the bitstream by the closed loop adaptive codebook component 144b. For one embodiment of the full-rate codec 22, the closed loop pitch lags for the first and the third subframes are represented with 8 bits, and the closed loop pitch lags for the second and the fourth subframes are represented with 5 bits, as previously discussed. In one embodiment, the lag is in a range of 17 to 148 lags. The 8 bits and the 5 bits may represent the same pitch resolution. However, the 8 bits may also represent the full range of the closed loop pitch lag for a subframe and the 5 bits may represent a limited value of closed loop pitch lags around the previous subframe closed loop pitch lag. In an example embodiment, the closed loop pitch lag resolution is 0.2, uniformly, between lag 17 and lag 33. From lag 33 to lag 91 of the example embodiment, the resolution is gradually increased from 0.2 to 0.5, and the resolution from lag 91 to lag 148 is 1.0, uniformly.

The adaptive codebook section 362 performs an integer lag search for closed loop integer pitch lags. For the first and the third subframes (i.e. those represented with 8 bits), the integer lag search may be performed on the range of $[L_p-3,...,L_p+3]$. Where L_p is the subframe pitch lag. The subframe pitch lag is obtained from the pitch track 348, which is used to identify a vector in the adaptive codebook 368. The cross-correlation function, R(l), for the integer lag search range may be calculated according to

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$$R(l) = \frac{\sum_{n=0}^{39} t(n) \cdot (e(n-l) * h(n))}{\sqrt{\sum_{n=0}^{39} (e(n-l) * h(n))^2}},$$
 (Equation 15)

where t(n) is the target signal that is the modified weighted speech 350, e(n) is the adaptive codebook contribution represented by the adaptive codebook vector (v_a) 382, h(n) is the combined response of the first synthesis filter 372 and the perceptual weighting filter 374. In the example embodiment, there are 40 samples in a subframe, although more or less samples could be used.

The closed loop integer pitch lag that maximizes R(l) may be choosen as a refined integer lag. The best vector from the adaptive codebook 368 for the adaptive codebook vector (v_a) 382 may be determined by upsampling the cross-correlation function R(l) using a 9th order Hamming weighted Sinc. Upsampling is followed by a search of the vectors within the adaptive codebook 368 that correspond to closed loop pitch lags that are within 1 sample of the refined integer lag. The index location within the adaptive codebook 368 of the best vector for the adaptive codebook vector (v_a) 382 for each subframe is represented by the closed loop adaptive codebook component 144b in the bitstream.

The initial adaptive codebook gain may be estimated according to:

$$g = \frac{\sum_{n=0}^{39} t(n) \cdot (e(n - L_p^{opt}) * h(n))}{\sum_{n=0}^{39} (e(n - L_p^{opt}) * h(n))^2},$$
 (Equation 16)

where L_p^{opt} represents the lag of the best vector for the adaptive codebook vector (v_a) 382 and $e(n-L_p^{\text{opt}})$ represents the best vector for the adaptive codebook vector (v_a) 382. In addition, in this example embodiment, the estimate is bounded by $0.0 \le g \le 1.2$, and n represents 40 samples in a subframe. A normalized adaptive codebook correlation is given by R(l) when $l=L_p^{\text{opt}}$. The initial adaptive codebook gain may be further normalized according to the normalized adaptive codebook correlation, the initial class decision and the sharpness of the adaptive codebook contribution. The normalization results in the gain (g_a) 384. The gain (g_a) 384 is unquantized and represents the initial adaptive codebook gain for the closed loop pitch lag.

4.1.1.2 Closed-Loop Adaptive Codebook Search for Half-Rate Coding

The closed loop pitch lag for the half-rate codec 24 is represented by the closed loop adaptive codebook component 176b (FIG. 2). For the half-rate codec 24 of one embodiment, the closed loop pitch lags for each of the two subframes are encoded in 7 bits each with each representing a lag in the range of 17 to 127 lags. The integer lag search may be performed on the range of $[L_p - 3,...,L_p + 3]$ as opposed to the fractional search performed in the full-rate codec 22. The cross-correlation function R(I) may be calculated as in Equation 15, where the summation is performed on an example embodiment subframe size of 80 samples. The closed loop pitch lag that maximizes R(I) is choosen as the refined integer lag. The index location within the adaptive codebook 368 of the

best vector for the adaptive codebook vector (v_a) 382 for each subframe is represented by the closed loop adaptive codebook component 176b in the bitstream.

The initial value for the adaptive codebook gain may be calculated according to Equation 16, where the summation is performed on an example embodiment subframe size of 80 samples. The normalization procedures as previously discussed may then be applied resulting in the gain (g_a) 384 that is unquantized.

The long-term error signal 388 generated by either the full-rate codec 22 or the half-rate codec 24 is used during the search by the fixed codebook section 364. Prior to the fixed codebook search, the voice activity decision from the VAD module 326 of FIG. 9 that is applicable to the frame is obtained. The voice activity decision for the frame may be sub-divided into a subframe voice activity decision for each subframe. The subframe voice activity decision may be used to improve perceptual selection of the fixed-codebook contribution.

4.1.2 Fixed Codebook Section

The fixed codebook section 364 includes a fixed codebook 390, a second multiplier 392, a second synthesis filter 394, a second perceptual weighting filter 396, a second subtractor 398, and a second minimization module 400. The search for the fixed codebook contribution by the fixed codebook section 364 is similar to the search within the adaptive codebook section 362.

A fixed codebook vector (v_e) 402 representing the long-term residual for a subframe is provided from the fixed codebook 390. The second multiplier 392 multiplies the fixed codebook vector (v_e) 402 by a gain (g_e) 404. The gain (g_e) 404 is unquantized and is a representation of the initial value of the fixed codebook gain that may be calculated as later described. The resulting signal is provided to the second synthesis filter 394. The second synthesis filter 394 receives the quantized LPC coefficients $\Lambda_q(z)$ 342 from the LSF quantization module 334 and together with the second perceptual weighting filter 396, creates a second resynthesized speech signal 406. The second subtractor 398 subtracts the resynthesized speech signal 406 from the long-term error signal 388 to generate a vector that is a fixed codebook error signal 408.

The second minimization module 400 receives the fixed codebook error signal 408 that represents the error in quantizing the long-term residual by the fixed codebook 390. The second minimization module 400 uses the energy of the fixed codebook error signal 408 to control the selection of vectors for the fixed codebook vector (v_c) 402 from the fixed codebook 292 in order to reduce the energy of the fixed codebook error signal 408. The second minimization module 400 also receives the control information 356 from the characterization module 328 of FIG. 9.

The final characterization class contained in the control information 356 controls how the second minimization module 400 selects vectors for the fixed codebook vector (v_c) 402 from the fixed codebook 390. The process repeats until the search by the second minimization module 400 has selected the best vector for the fixed codebook vector (v_c) 402 from the fixed codebook 390 for each subframe. The best vector for the fixed codebook vector (v_c) 402 minimizes the error in the second resynthesized speech signal 406 with respect to the long-term error signal 388. The indices identify the best vector for the fixed codebook vector (v_c) 402 and, as previously discussed, may be used to form the fixed codebook component 146a and 178a.

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4.1.2.1 Fixed Codebook Search for the Full-Rate Codec

As previously discussed with reference to FIGs. 2 and 4, the fixed codebook component 146a for frames of Type Zero classification may represent each of four subframes of the full-rate codec 22 using the three 5-pulse codebooks 160. When the search is initiated, vectors for the fixed codebook vector (v_c) 402 within the fixed codebook 390 may be determined using the long-term error signal 388 that is represented by:

$$t'(n) = t(n) - g_a \cdot \left(e(n - L_p^{opt}) * h(n) \right). \tag{Equation 17}$$

Pitch enhancement may be applied to the three 5-pulse codebooks 160 (illustrated in Fig. 4) within the fixed codebook 390 in the forward direction during the search. The search is an iterative, controlled complexity search for the best vector for the fixed codebook vector (v_c) 402. An initial value for fixed codebook gain represented by the gain (g_c) 404 may be found simultaneously with the search for the best vector for the fixed codebook vector (v_c) 402.

In an example embodiment, the search for the best vector for the fixed codebook vector (v_c) 402 is completed in each of the three 5-pulse codebooks 160. At the conclusion of the search process within each of the three 5-pulse codebooks 160, candidate best vectors for the fixed codebook vector (v_c) 402 have been identified. Selection of one of the three 5-pulse codebooks 160 and which of the corresponding candidate best vectors will be used may be determined using the corresponding fixed codebook error signal 408 for each of the candidate best vectors. Determination of the weighted mean squared error (WMSE) for each of the corresponding fixed codebook error signals 408 by the second minimization module 400 is first performed. For purposes of this discussion, the weighted mean squared errors (WMSEs) for each of the candidate best vectors from each of the three 5-pulse codebooks 160 will be referred to as first, second and third fixed codebook WMSEs.

The first, second, and third fixed codebook WMSEs may be first weighted. Within the full-rate codec 22, for frames classified as Type Zero, the first, second, and third fixed codebook WMSEs may be weighted by the subframe voice activity decision. In addition, the weighting may be provided by a sharpness measure of each of the first, second, and third fixed codebook WMSEs and the NSR from the characterization module 328 of FIG. 9. Based on the weighting, one of the three 5-pulse fixed codebooks 160 and the best candidate vector in that codebook may be selected.

The selected 5-pulse codebook 160 may then be fine searched for a final decision of the best vector for the fixed codebook vector (v_c) 402. The fine search is performed on the vectors in the selected one of the three 5-pulse codebook 160 that are in the vicinity of the best candidate vector chosen. The indices that identify the best vector for the fixed codebook vector (v_c) 402 within the selected one of the three 5-pulse codebook 160 are part of the fixed codebook component 178a in the bitstream.

4.1.2.2 Fixed Codebook Search for the Half-Rate Codec

For frames of Type Zero classification, the fixed codebook component 178a represents each of the two subframes of the half-rate codec 24. As previously discussed, with reference to FIG. 5, the representation may be based on the pulse codebooks 192, 194 and the gaussian codebook 195. The initial target for the fixed codebook gain represented by the gain (g_c) 404 may be determined similarly to the full-rate codec 22. In addition, the search for the fixed codebook vector (v_c) 402 within the fixed codebook 390 may be weighted similarly to the

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full-rate codec 22. In the half-rate codec 24, the weighting may be applied to the best candidate vectors from each of the pulse codebooks 192 and 194 as well as the gaussian codebook 195. The weighting is applied to determine the most suitable fixed codebook vector (v_c) 402 from a perceptual point of view. In addition, the weighting of the weighted mean squared error (WMSE) in the half-rate codec 24 may be further enhanced to emphasize the perceptual point of view. Further enhancement may be accomplished by including additional parameters in the weighting. The additional factors may be the closed loop pitch lag and the normalized adaptive codebook correlation.

In addition to the enhanced weighting, prior to the search of the codebooks 192, 194, 195 for the best candidate vectors, some characteristics may be built into the entries of the pulse codebooks 192, 194. These characteristics can provide further enhancement to the perceptual quality. In one embodiment, enhanced perceptual quality during the searches may be achieved by modifying the filter response of the second synthesis filter 394 using three enhancements. The first enhancement may be accomplished by injecting high frequency noise into the fixed codebook, which modifies the high-frequency band. The injection of high frequency noise may be incorporated into the response of the second synthesis filter 394 by convolving the high frequency noise impulse response with the impulse response of the second synthesis filter 394.

The second enhancement may be used to incorporate additional pulses in locations that can be determined by high correlations in the previously quantized subframe. The amplitude of the additional pulses may be adjusted according to the correlation strength, thereby allowing the decoding system 16 to perform the same operation without the necessity of additional information from the encoding system 12. The contribution from these additional pulses also may be incorporated into the impulse response of the second synthesis filter 394. The third enhancement filters the fixed codebook 390 with a weak short-term spectral filter to compensate for the reduction in the formant sharpness resulting from bandwidth expansion and the quantization of the LSFs.

The search for the best vector for the fixed codebook vector (v_c) 402 is based on minimizing the energy of the fixed codebook error signal 408, as previously discussed. The search may first be performed on the 2-pulse codebook 192. The 3-pulse codebook 194 may be searched next, in two steps. The first step can determine a center for the second step that may be referred to as a focused search. Backward and forward weighted pitch enhancement may be applied for the search in both pulse codebooks 192 and 194. The gaussian codebook 195 may be searched last, using a fast search routine that is used to determine the two orthogonal basis vectors for encoding as previously discussed.

The selection of one of the codebooks 192, 194 and 195 and the best vector for the fixed codebook vector (v_c) 402 may be performed similarly to the full-rate codec 22. The indices that identify the best vector for the fixed codebook vector (v_c) 402 within the selected codebook are part of the fixed codebook component 178a in the bitstream.

At this point, the best vectors for the adaptive codebook vector (v_a) 382 and the fixed codebook vector (v_c) 402 have been found within the adaptive and fixed codebooks 368, 390, respectively. The unquantized initial values for the gain (g_a) 384 and the gain (g_c) 404 now may be replaced by the best gain values. The best gain values may be determined based on the best vectors for the adaptive codebook vector (v_a) 382 and the fixed

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codebook vector (v_c) 402 previously determined. Following determination of the best gains, they are jointly quantized. Determination and quantization of the gains occurs within the gain quantization section 366.

4.1.3 Gain Quantization Section

The gain quantization section 366 of one embodiment includes a 2D VQ gain codebook 412, a third multiplier 414, a fourth multiplier 416, an adder 418, a third synthesis filter 420, a third perceptual weighting filter 422, a third subtractor 424, a third minimization module 426, and an energy modification section 428. The energy modification section 428 of one embodiment includes an energy analysis module 430 and an energy adjustment module 432. Determination and quantization of the fixed and adaptive codebook gains may be performed within the gain quantization section 366. In addition, further modification of the modified weighted speech 350 occurs in the energy modification section 428, as will be discussed, to form a modified target signal 434 that may be used for the quantization.

Determination and quantization involves searching to determine a quantized gain vector (\hat{g}_{ac}) 433 that represents the joint quantization of the adaptive codebook gain and the fixed codebook gain. The adaptive and fixed codebook gains, for the search, may be obtained by minimizing the weighted mean square error according to:

$$\{g_a, g_c\} = \arg\min \left\{ \sum_{n=0}^{79} (t(n) - ((g_a v_a(n) * h(n)) + (g_c v_c(n) * h(n))))^2 \right\}.$$

(Equation 18)

Where $v_a(n)$ is the best vector for the adaptive codebook vector (v_a) 382, and $v_c(n)$ is the best vector for the fixed codebook vector (v_c) 402 as previously discussed. In the example embodiment, the summation is based on a frame that contains 80 samples, such as, in one embodiment of the half-rate codec 24. The minimization may be obtained jointly (obtaining g_a and g_c concurrently) or sequentially (obtaining g_a first and then g_c), depending on a threshold value of the normalized adaptive codebook correlation. The gains may then be modified in part, to smooth the fluctuations of the reconstructed speech in the presence of background noise. The modified gains are denoted g'_a and g'_c . The modified target signal 434 may be generated using the modified gains by:

$$t''(n) = g'_{n} v_{n}(n) * h(n) + g'_{n} v_{n}(n) * h(n)$$
 (Equation 19)

A search for the best vector for the quantized gain vector (\hat{g}_{ac}) 433 is performed within the 2D VQ gain codebook 412. The 2D VQ gain codebook 412 may be the previously discussed 2D gain quantization table illustrated as Table 4. The 2D VQ gain codebook 412 is searched for vectors for the quantized gain vector (\hat{g}_{ac}) 433 that minimize the mean square error, i.e., minimizing

$$E = \sum_{n=0}^{79} (t''(n) - (\hat{g}_a v_a(n) * h(n) + \hat{g}_c v_c(n) * h(n)))^2,$$
 (Equation 20)

where a quantized fixed codebook gain (\hat{g}_a) 435 and a quantized adaptive codebook gain (\hat{g}_c) 436 may be derived from the 2D VQ gain codebook 412. In the example embodiment, the summation is based on a frame that contains 80 samples, such as, in one embodiment of the half-rate codec 24. The quantized vectors in the 2D VQ

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gain codebook 412 actually represent the adaptive codebook gain and a correction factor for the fixed codebook gain as previously discussed.

Following determination of the modified target signal 434, the quantized gain vector (\hat{g}_{ac}) 433 is passed to multipliers 414, 416. The third multiplier 414 multiplies the best vector for the adaptive codebook vector (v_a) 382 from the adaptive codebook 368 with the quantized adaptive codebook gain (\hat{g}_a) 435. The output from the third multiplier 414 is provided to the adder 418. Similarly, the fourth multiplier 416 multiplies the quantized fixed codebook gain (\hat{g}_c) 436 with the best vector for the fixed codebook vector (v_c) 402 from the fixed codebook 390. The output from the fourth multiplier 416 is also provided to the adder 418. The adder 418 adds the outputs from the multipliers 414, 416 and provides the resulting signal to the third synthesis filter 420.

The combination of the third synthesis filter 420 and the perceptual weighting filter 422 generates a third resynthesized speech signal 438. As with the first and second synthesis filters 372 and 394, the third synthesis filter 420 receives the quantized LPC coefficients $A_q(z)$ 342. The third subtractor 424 subtracts the third resynthesized speech signal 438 from the modified target signal 434 to generate a third error signal 442. The third minimization module 426 receives the third error signal 442 that represents the error resulting from joint quantization of the fixed codebook gain and the adaptive codebook gain by the 2D VQ gain codebook 412. The third minimization module 426 uses the energy of the third error signal 442 to control the search and selection of vectors from the 2D VQ gain codebook 412 in order to reduce the energy of the third error signal 442.

The process repeats until the third minimization module 426 has selected the best vector from the 2D VQ gain codebook 412 for each subframe that minimizes the energy of the third error signal 442. Once the energy of the third error signal 442 has been minimized for each subframe, the index locations of the jointly quantized gains (\hat{g}_a) and (\hat{g}_c) 435 and 436 are used to generate the gain component 147, 179 for the frame. For the full -rate codec 22, the gain component 147 is the fixed and adaptive gain component 148a, 150a and for the half-rate codec 24, the gain component 179 is the adaptive and fixed gain component 180a and 182a.

The synthesis filters 372, 394 and 420, the perceptual weighting filters 374, 396 and 422, the minimization modules 378, 400 and 426, the multipliers 370, 392, 414 and 416, the adder 418, and the subtractors 376, 398 and 424 (as well as any other filter, minimization module, multiplier, adder, and subtractor described in this application) may be replaced by any other device, or modified in a manner known to those of ordinary skill in the art, that may be appropriate for the particular application.

4.2 Excitation Processing Module For Type One Frames Of The Full-Rate Codec And The Half-Rate Codec

In Fig. 11, the F1, H1 first frame processing modules 72 and 82 includes a 3D/4D open loop VQ module 454. The F1, H1 second sub-frame processing modules 74 and 84 of one embodiment include the adaptive codebook 368, the fixed codebook 390, a first multiplier 456, a second multiplier 458, a first synthesis filter 460, and a second synthesis filter 462. In addition, the F1, H1 second sub-frame processing modules 74 and 84 include a first perceptual weighting filter 464, a second perceptual weighting filter 466, a first subtractor 468, a second subtractor 470, a first minimization module 472, and an energy adjustment module 474. The F1, H1 second frame processing modules 76 and 86 include a third multiplier 476, a fourth multiplier 478, an adder 480, a third

synthesis filter 482, a third perceptual weighting filter 484, a third subtractor 486, a buffering module 488, a second minimization module 490 and a 3D/4D VQ gain codebook 492.

The processing of frames classified as Type One within the excitation-processing module 54 ptovides processing on both a frame basis and a sub-frame basis, as previously discussed. For purposes of brevity, the following discussion will refer to the modules within the full rate codec 22. The modules in the half rate codec 24 may be considered to function similarly, unless otherwise noted. Quantization of the adaptive codebook gain by the F1 first frame-processing module 72 generates the adaptive gain component 148b. The F1 second subframe processing module 74 and the F1 second frame processing module 76 operate to determine the fixed codebook vector and the corresponding fixed codebook gain, respectively as previously set forth. The F1 second subframe-processing module 74 uses the track tables, as previously discussed, to generate the fixed codebook component 146b as illustrated in FIG. 2.

The F1 second frame-processing module 76 quantizes the fixed codebook gain to generate the fixed gain component 150b. In one embodiment, the full-rate codec 22 uses 10 bits for the quantization of 4 fixed codebook gains, and the half-rate codec 24 uses 8 bits for the quantization of the 3 fixed codebook gains. The quantization may be performed using moving average prediction. In general, before the prediction and the quantization are performed, the prediction states are converted to a suitable dimension.

4.2.1 First Frame Processing Module

One embodiment of the 3D/4D open loop VQ module 454 may be the previously discussed four-dimensional pre vector quantizer (4D pre VQ) 166 and associated pre-gain quantization table for the full-rate codec 22. Another embodiment of the 3D/4D open loop VQ module 454 may be the previously discussed three-dimensional pre vector quantizer (3D pre VQ) 198 and associated pre-gain quantization table for the half-rate codec 24. The 3D/4D open loop VQ module 454 receives the unquantized pitch gains 352 from the pitch pre-processing module 322. The unquantized pitch gains 352 represent the adaptive codebook gain for the open loop pitch lag, as previously discussed.

The 3D/4D open loop VQ module 454 quantizes the unquantized pitch gains 352 to generate a quantized pitch gain (\hat{g}^k_a) 496 representing the best quantized pitch gains for each subframe where k is the number of subframes. In one embodiment, there are four subframes for the full-rate codec 22 and three subframes for the half-rate codec 24 which correspond to four quantized gains $(\hat{g}^l_a, \hat{g}^2_a, \hat{g}^3_a, \hat{g}^4_a)$ and three quantized gains $(\hat{g}^l_a, \hat{g}^2_a, \hat{g}^3_a)$ of each subframe, respectively. The index location of the quantized pitch gain (\hat{g}^k_a) 496 within the pre-gain quantization table represents the adaptive gain component 148b for the full-rate codec 22 or the adaptive gain component 180b for the half-rate codec 24. The quantized pitch gain (\hat{g}^k_a) 496 is provided to the F1 second subframe-processing module 74 or the H1 second subframe-processing module 84.

4.2.2 Second Sub-Frame Processing Module

The F1 or H1 second subframe-processing module 74 or 84 uses the pitch track 348 provided by the pitch pre-processing module 322 to identify an adaptive codebook vector (v_a^k) 498. The adaptive codebook vector (v_a^k) 498 represents the adaptive codebook contribution for each subframe where k equals the subframe number.

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In one embodiment, there are four subframes for the full-rate codec 22 and three subframes for the half-rate codec 24 which correspond to four vectors $(v_a^1, v_a^2, v_a^3, v_a^4)$ and three vectors (v_a^1, v_a^2, v_a^3) for the adaptive codebook contribution for each subframe, respectively.

The vector selected for the adaptive codebook vector (v_a^k) 498 may be derived from past vectors located in the adaptive codebook 368 and the pitch track 348. Where the pitch track 348 may be interpolated and is represented by $L_p(n)$. Accordingly, no search is required. The adaptive codebook vector (v_a^k) 498 may be obtained by interpolating the past adaptive codebook vectors (v_a^k) 498 in the adaptive codebook with a 21st order Hamming weighted Sinc window by:

$$v_{a}(n) = \sum_{i=-10}^{10} w_{s}(f(L_{p}(n)), i) \cdot e(n - i(L_{p}(n)), i)$$
 (Equation 21)

where e(n) is the past excitation, $i(L_p(n))$ and $f(L_p(n))$ are the integer and fractional part of the pitch lag, respectively, and $w_s(f,i)$ is the Hamming weighted Sinc window.

The adaptive codebook vector (v_a^k) 498 and the quantized pitch gain (\hat{g}_a^k) 496 are multiplied by the first multiplier 456. The first multiplier 456 generates a signal that is processed by the first synthesis filter 460 and the first perceptual weighting filter module 464 to provide a first resynthesized speech signal 500. The first synthesis filter 460 receives the quantized LPC coefficients $A_q(z)$ 342 from the LSF quantization module 334 as part of the processing. The first subtractor 468 subtracts the first resynthesized speech signal 500 from the modified weighted speech 350 provided by the pitch pre-processing module 322 to generate a long-term error signal 502.

The F1 or H1 second subframe-processing module 74 or 84 also performs a search for the fixed codebook contribution that is similar to that performed by the F0 or H0 first subframe-processing module 70 and 80, previously discussed. Vectors for a fixed codebook vector (v_c^k) 504 that represents the long-term residual for a subframe are selected from the fixed codebook 390 during the search. The second multiplier 458 multiplies the fixed codebook vector (v_c^k) 504 by a gain (g_c^k) 506 where k is the subframe number. The gain (g_c^k) 506 is unquantized and represents the fixed codebook gain for each subframe. The resulting signal is processed by the second synthesis filter 462 and the second perceptual weighting filter 466 to generate a second resynthesized speech signal 508. The second resynthesized speech signal 508 is subtracted from the long-term error signal 502 by the second subtractor 470 to produce a fixed codebook error signal 510.

The fixed codebook error signal 510 is received by the first minimization module 472 along with the control information 356. The first minimization module 472 operates the same as the previously discussed second minimization module 400 illustrated in FIG. 10. The search process repeats until the first minimization module 472 has selected the best vector for the fixed codebook vector (\mathbf{v}^k_c) 504 from the fixed codebook 390 for each subframe. The best vector for the fixed codebook vector (\mathbf{v}^k_c) 504 minimizes the energy of the fixed codebook error signal 510. The indices identify the best vector for the fixed codebook vector (\mathbf{v}^k_c) 504, as previously discussed, and form the fixed codebook component 146b and 178b.

4.2.2.1 Fixed Codebook Search for Full-Rate Codec

In one embodiment, the 8-pulse codebook 162, illustrated in FIG. 4, is used for each of the four subframes for frames of type 1 by the full-rate codec 22, as previously discussed. The target for the fixed codebook vector (v^k_c) 504 is the long-term error signal 502, as previously described. The long-term error signal 502, represented by t'(n), is determined based on the modified weighted speech 350, represented by t(n), with the adaptive codebook contribution from the initial frame processing module 44 removed according to:

$$t'(n) = t(n) - g_a \cdot (v_a(n) * h(n)).$$
 (Equation 22)

During the search for the best vector for the fixed codebook vector (v^k_c) 504, pitch enhancement may be applied in the forward direction. In addition, the search procedure minimizes the fixed codebook residual 508 using an iterative search procedure with controlled complexity to determine the best vector for the fixed codebook vector v^k_c 504. An initial fixed codebook gain represented by the gain (g^k_c) 506 is determined during the search. The indices identify the best vector for the fixed codebook vector (v^k_c) 504 and form the fixed codebook component 146b as previously discussed.

4.2.2.2 Fixed Codebook Search for Half-Rate Codec

In one embodiment, the long-term residual is represented with 13 bits for each of the three subframes for frames classified as Type One for the half-rate codec 24, as previously discussed. The long-term residual may be determined in a similar manner to the fixed codebook search in the full-rate codec 22. Similar to the fixed-codebook search for the half-rate codec 24 for frames of Type Zero, the high-frequency noise injection, the additional pulses that are determined by high correlation in the previous subframe, and the weak short-term spectral filter may be introduced into the impulse response of the second synthesis filter 462. In addition, forward pitch enhancement also may be introduced into the impulse response of the second synthesis filter 462.

In one embodiment, a full search is performed for the 2-pulse code book 196 and the 3-pulse codebook 197 as illustrated in FIG. 5. The pulse codebook 196, 197 and the best vector for the fixed codebook vector (v_c^k) 504 that minimizes the fixed codebook error signal 510 are selected for the representation of the long term residual for each subframe. In addition, an initial fixed codebook gain represented by the gain (g_c^k) 506 may be determined during the search similar to the full-rate codec 22. The indices identify the best vector for the fixed codebook vector (v_c^k) 504 and form the fixed codebook component 178b.

As previously discussed, the F1 or H1 second subframe-processing module 74 or 84 operates on a subframe basis. However, the F1 or H1 second frame-processing module 76 or 86 operates on a frame basis. Accordingly, parameters determined by the F1 or H1 second subframe-processing module 74 or 84 may be stored in the buffering module 488 for later use on a frame basis. In one embodiment, the parameters stored are the best vector for the adaptive codebook vector (v_a^k) 498 and the best vector for the fixed codebook vector (v_c^k) 504. In addition, a modified target signal 512 and the gains (\hat{g}_a^k) , (g_c^k) 496 and 506 representing the initial adaptive and fixed codebook gains may be stored. Generation of the modified target signal 512 will be described later.

At this time, the best vector for the adaptive codebook vector (v_a^k) 498, the best vector for the fixed codebook vector (v_c^k) 504, and the best pitch gains for the quantized pitch gain (\hat{g}_a^k) 496 have been identified. Using these best vectors and best pitch gains, the best fixed codebook gains for the gain (g_c^k) 506 will be

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determined. The best fixed codebook gains for the gain (g^k_c) 506 will replace the unquantized initial fixed codebook gains determined previously for the gain (g^k_c) 506. To determine the best fixed codebook gains, a joint delayed quantization of the fixed-codebook gains for each subframe is performed by the second frame-provessing module 76 and 86.

4.2.3 Second Frame Processing Module

The second frame processing module 76 and 86 is operable on a frame basis to generate the fixed codebook gain represented by the fixed gain component 150b and 182b. The modified target 512 is first determined in a manner similar to the gain determination and quantization of the frames classified as Type Zero. The modified target 512 is determined for each subframe and is represented by t"(n). The modified target may be derived using the best vectors for the adaptive codebook vector (v_a^k) 498 and the fixed codebook vector (v_c^k) 504, as well as the adaptive codebook gain and the initial value of the fixed codebook gain derived from Equation 18 by:

$$t''(n) = g_{o}v_{o}(n) * h(n) + g_{o}v_{o}(n) * h(n).$$
 (Equation 23)

An initial value for the fixed codebook gain for each subframe to be used in the search may be obtained by minimizing:

$${g_c} = \arg\min \left\{ \sum_{n=0}^{N-1} (t(n) - ((\hat{g}_a v_a(n) * h(n)) + (g_c v_c(n) * h(n))))^2 \right\}.$$
 (Equation 24)

Where $v_a(n)$ is the adaptive-codebook contribution for a particular subframe and $v_c(n)$ is the fixed-codebook contribution for a particular subframe. In addition, \hat{g}_a is the quantized and normalized adaptive-codebook gain for a particular subframe that is one of the elements a quantized fixed codebook gain (\hat{g}_c^k) 513. The calculated fixed codebook gain g_c is further normalized and corrected, to provide the best energy match between the third resynthesized speech signal and the modified target signal 512 that has been buffered. Unquantized fixed-codebook gains from the previous subframes may be used to generate the adaptive codebook vector (v_a^k) 498 for the processing of the next subframe according to Equation 21.

The search for vectors for the quantized fixed codebook gain (\hat{g}^k_c) 513 is performed within the 3D/4D VQ gain codebook 492. The 3D/4D VQ gain codebook 492 may be the previously discussed multi-dimensional gain quantizer and associated gain quantization table. In one embodiment, the 3D/4D VQ gain codebook 492 may be the previously discussed 4D delayed VQ gain quantizer 168 for the full-rate codec 22. As previously discussed, the 4D delayed VQ gain quantizer 168 may be operable using the associated delayed gain quantization table illustrated as Table 5. In another embodiment, the 3D/4D VQ gain codebook 492 may be the previously discussed 3D delayed VQ gain quantizer 200 for the half-rate codec 24. The 3D delayed VQ gain quantizer 200 may be operable using the delayed gain quantization table illustrated as the previously discussed Table 8.

The 3D/4D VQ gain codebook 492 may be searched for vectors for the quantized fixed codebook gain (\hat{g}^k_{c}) 513 that minimize the energy similar to the previously discussed 2D VQ gain codebook 412 of FIG. 10. The quantized vectors in the 3D/4D VQ gain codebook 492 actually represent a correction factor for the predicted fixed codebook gain as previously discussed. During the search, the third multiplier 476 multiplies the adaptive

codebook vector (v_a^k) 498 by the quantized pitch gain (\hat{g}_a^k) 496 following determination of the modified target 512. In addition, the fourth multiplier 478 multiplies the fixed codebook vector (v_c^k) 504 by the quantized fixed codebook gain (\hat{g}_c^k) 513. The adder 480 adds the resulting signals from the multipliers 476 and 478.

The resulting signal from the adder 480 is passed through the third synthesis filter 482 and the perceptual weighting filter module 484 to generate a third resynthesized speech signal 514. As with the first and second synthesis filters 460, 462, the third synthesis filter 482 receives the quantized LPC coefficients $A_q(z)$ 342 from the LSF quantization module 334 as part of the processing. The third subtractor 486 subtracts the third resynthesized speech signal 514 from the modified target signal 512 that was previously stored in the buffering module 488. The resulting signal is the weighted mean squared error referred to as a third error signal 516.

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The third minimization module 490 receives the third error signal 516 that represents the error resulting from quantization of the fixed codebook gain by the 3D/4D VQ gain codebook 492. The third minimization module 490 uses the third error signal 516 to control the search and selection of vectors from the 3D/4D VQ gain codebook 492 in order to reduce the energy of the third error signal 516. The search process repeats until the third minimization module 490 has selected the best vector from the 3D/4D VQ gain codebook 492 for each subframe that minimizes the error in the third error signal 516. Once the energy of the third error signal 516 has been minimized, the index location of the quantized fixed codebook gain (\hat{g}^k_c) 513 in the 3D/4D VQ gain codebook 492 is used to generate the fixed codebook gain component 150b for the full-rate codec 22, and the fixed codebook gain component 182b for the half-rate codec 24.

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4.2.3.1 3D/4D VQ Gain Codebook

In one embodiment, when the 3D/4D VQ gain codebook 492 is a 4-dimensional codebook, it may be searched in order to minimize

$$E = \sum_{n=0}^{39} \left(t^{1}(n) - \left(\hat{g}_{\sigma}^{1} v_{\sigma}^{1}(n) * h(n) + \hat{g}_{c}^{1} v_{c}^{1}(n) * h(n) \right) \right)^{2}$$

$$+ \sum_{n=0}^{39} \left(t^{2}(n) - \left(\hat{g}_{\sigma}^{2} v_{\sigma}^{2}(n) * h(n) + \hat{g}_{c}^{2} v_{c}^{2}(n) * h(n) \right) \right)^{2}$$

$$+ \sum_{n=0}^{39} \left(t^{3}(n) - \left(\hat{g}_{\sigma}^{3} v_{\sigma}^{3}(n) * h(n) + \hat{g}_{c}^{3} v_{c}^{3}(n) * h(n) \right) \right)^{2}$$

$$+ \sum_{n=0}^{39} \left(t^{4}(n) - \left(\hat{g}_{\sigma}^{4} v_{\sigma}^{4}(n) * h(n) + \hat{g}_{c}^{4} v_{c}^{4}(n) * h(n) \right) \right)^{2}$$

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where the quantized pitch gains $\{\hat{g}_{a}^{1}, \hat{g}_{a}^{2}, \hat{g}_{a}^{3}, \hat{g}_{a}^{4}\}$ originate from the initial frame processing module 44, and $\{t^{1}(n), t^{2}(n), t^{3}(n), t^{4}(n)\}$, $\{v^{1}_{a}(n), v^{2}_{a}(n), v^{3}_{a}(n), v^{4}_{a}(n)\}$, and $\{v^{1}_{c}(n), v^{2}_{c}(n), v^{3}_{c}(n), v^{4}_{c}(n)\}$ may be buffered during the subframe processing as previously discussed. In an example embodiment, the fixed codebook gains $\{\hat{g}_{c}^{1}, \hat{g}_{c}^{2}, \hat{g}_{c}^{3}, \hat{g}_{c}^{4}\}$ are derived from a 10-bit codebook, where the entries of the codebook contain a 4-dimensional correction factor for the predicted fixed codebook gains as previously discussed. In addition, n=40 to represent 40 samples per frame.

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In another embodiment, when the 3D/4D VQ gain codebook 492 is a 3-dimensional codebook, it may be searched in order to minimize

$$E = \sum_{n=0}^{52} \left(t^{1}(n) - \left(\hat{g}_{a}^{1} v_{a}^{1}(n) * h(n) + \hat{g}_{c}^{1} v_{c}^{1}(n) * h(n) \right) \right)^{2}$$

$$+ \sum_{n=0}^{52} \left(t^{2}(n) - \left(\hat{g}_{a}^{2} v_{a}^{2}(n) * h(n) + \hat{g}_{c}^{2} v_{c}^{2}(n) * h(n) \right) \right)^{2}$$

$$+ \sum_{n=0}^{53} \left(t^{3}(n) - \left(\hat{g}_{a}^{3} v_{a}^{3}(n) * h(n) + \hat{g}_{c}^{3} v_{c}^{3}(n) * h(n) \right) \right)^{2}$$
(Equation 26)

where the quantized pitch gains $\{\hat{g}_a^1, \hat{g}_a^2, \hat{g}_a^3\}$ originate from the initial frame processing module 44, and $\{t^{1}(n), t^{2}(n), t_{3}(n)\}, \{v_{a}^{1}(n), v_{a}^{2}(n), v_{a}^{3}(n)\}, \text{ and } \{v_{c}^{1}(n), v_{c}^{2}(n), v_{c}^{3}(n)\} \text{ may be buffered during the subframe}$ processing as previously discussed. In an example embodiment, the fixed codebook gains $\{\hat{g}_c^1, \hat{g}_c^2, \hat{g}_c^3\}$ are derived from an 8-bit codebook where the entries of the codebook contain a 3-dimensional correction factor for the predicted fixed codebook gains. The prediction of the fixed-codebook gains may be based on moving average prediction of the fixed codebook energy in the log domain.

5.0 **DECODING SYSTEM**

Referring now to FIG. 12, an expanded block diagram representing the full and half-rate decoders 90 and 92 of FIG. 3 is illustrated. The full or half-rate decoders 90 or 92 include the excitation reconstruction modules 104, 106, 114 and 116 and the linear prediction coefficient (LPC) reconstruction modules 107 and 118. One embodiment of each of the excitation reconstruction modules 104, 106, 114 and 116 includes the adaptive codebook 368, the fixed codebook 390, the 2D VQ gain codebook 412, the 3D/4D open loop VQ codebook 454, and the 3D/4D VQ gain codebook 492. The excitation reconstruction modules 104, 106, 114 and 116 also include a first multiplier 530, a second multiplier 532 and an adder 534. In one embodiment, the LPC reconstruction modules 107, 118 include an LSF decoding module 536 and an LSF conversion module 538. In addition, the half-rate codec 24 includes the predictor switch module 336, and the full-rate codec 22 includes the interpolation module 338.

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Also illustrated in FIG. 12 are the synthesis filter module 98 and the post-processing module 100. In one embodiment, the post-processing module 100 includes a short-term post filter module 540, a long-term filter module 542, a tilt compensation filter module 544, and an adaptive gain control module 546. According to the rate selection, the bit-stream may be decoded to generate the post-processed synthesized speech 20. The decoders 90 and 92 perform inverse mapping of the components of the bit-stream to algorithm parameters. The inverse mapping may be followed by a type classification dependent synthesis within the full and half-rate codecs 22 and 24.

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The decoding for the quarter-rate codec 26 and the eighth-rate codec 28 are similar to the full and halfrate codecs 22 and 24. However, the quarter and eighth-rate codecs 26 and 28 use vectors of similar yet random numbers and the energy gain, as previously discussed, instead of the adaptive and the fixed codebooks 368 and 390 and associated gains. The random numbers and the energy gain may be used to reconstruct an excitation energy that represents the short-term excitation of a frame. The LPC reconstruction modules 122 and 126 also are

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similar to the full and half-rate codec 22, 24 with the exception of the predictor switch module 336 and the interpolation module 338.

5.1 Excitation Reconstruction

Within the full and half rate decoders 90 and 92, operation of the excitation reconstruction modules 104, 106, 114 and 116 is largely dependent on the type classification provided by the type component 142 and 174. The adaptive codebook 368 receives the pitch track 348. The pitch track 348 is reconstructed by the decoding system 16 from the adaptive codebook component 144 and 176 provided in the bitstream by the encoding system 12. Depending on the type classification provided by the type component 142 and 174, the adaptive codebook 368 provides a quantized adaptive codebook vector (v^k_a) 550 to the multiplier 530. The multiplier 530 multiplies the quantized adaptive codebook vector (v^k_a) 550 with an adaptive codebook gain vector (g^k_a) 552. The selection of the adaptive codebook gain vector (g^k_a) 552 also depends on the type classification provided by the type component 142 and 174.

In an example embodiment, if the frame is classified as Type Zero in the full rate codec 22, the 2D VQ gain codebook 412 provides the adaptive codebook gain vector (g_a^k) 552 to the multiplier 530. The adaptive codebook gain vector (g_a^k) 552 is determined from the adaptive and fixed codebook gain component 148a and 150a. The adaptive codebook gain vector (g_a^k) 552 is the same as part of the best vector for the quantized gain vector (\hat{g}_{ac}) 433 determined by the gain and quantization section 366 of the F0 first sub-frame processing module 70 as previously discussed. The quantized adaptive codebook vector (v_a^k) 550 is determined from the closed loop adaptive codebook component 144b. Similarly, the quantized adaptive codebook vector (v_a^k) 550 is the same as the best vector for the adaptive codebook vector (v_a) 382 determined by the F0 first sub-frame processing module 70.

The 2D VQ gain codebook 412 is two-dimensional and provides the adaptive codebook gain vector (g^k_a) 552 to the multiplier 530 and a fixed codebook gain vector (g^k_c) 554 to the multiplier 532. The fixed codebook gain vector (g^k_c) 554 similarly is determined from the adaptive and fixed codebook gain component 148a and 150a and is part of the best vector for the quantized gain vector (\hat{g}_{ac}) 433. Also based on the type classification, the fixed codebook 390 provides a quantized fixed codebook vector (v^k_a) 556 to the multiplier 532. The quantized fixed codebook vector (v^k_a) 556 is reconstructed from the codebook identification, the pulse locations (or the gaussian codebook 195 for the half-rate codec 24), and the pulse signs provided by the fixed codebook component 146a. The quantized fixed codebook vector (v^k_a) 556 is the same as the best vector for the fixed codebook vector (v^c_c) 402 determined by the F0 first sub-frame processing module 70 as previously discussed. The multiplier 532 multiplies the quantized fixed codebook vector (v^k_a) 556 by the fixed codebook gain vector (g^k_c) 554.

If the type classification of the frame is Type One, a multi-dimensional vector quantizer provides the adaptive codebook gain vector (g_a^k) 552 to the multiplier 530. Where the number of dimensions in the multi-dimensional vector quantizer is dependent on the number of subframes. In one embodiment, the multi-dimensional vector quantizer may be the 3D/4D open loop VQ 454. Similarly, a multi-dimensional vector quantizer provides the fixed codebook gain vector (g_c^k) 554 to the multiplier 532. The adaptive codebook gain vector (g_a^k) 552 and the fixed codebook gain vector (g_c^k) 554 are provided by the gain component 147 and 179

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and are the same as the quantized pitch gain (\hat{g}_{a}^{k}) 496 and the quantized fixed codebook gain (\hat{g}_{c}^{k}) 513, respectively.

In frames classified as Type Zero or Type One, the output from the first multiplier 530 is received by the adder 534 and is added to the output from the second multiplier 532. The output from the adder 534 is the short-term excitation. The short-term excitation is provided to the synthesis filter module 98 on the short-term excitation line 128.

5.2 LPC Reconstruction

The generation of the short-term (LPC) prediction coefficients in the decoders 90 and 92 is similar to the processing in the encoding system 12. The LSF decoding module 536 reconstructs the quantized LSFs from the LSF component 140 and 172. The LSF decoding module 536 uses the same LSF prediction error quantization table and LSF predictor coefficients tables used by the encoding system 12. For the half-rate codec 24, the predictor switch module 336 selects one of the sets of predictor coefficients, to calculate the predicted LSFs as directed by the LSF component 140, 172. Interpolation of the quantized LSFs occurs using the same linear interpolation path used in the encoding system 12. For the full-rate codec 22 for frames classified as Type Zero, the interpolation module 338, selects the one of the same interpolation paths used in the encoding system 12 as directed by the LSF component 140 and 172. The weighting of the quantized LSFs is followed by conversion to the quantized LPC coefficients $A_q(z)$ 342 within the LSF conversion module 538. The quantized LPC coefficients $A_q(z)$ 342 are the short-term prediction coefficients that are supplied to the synthesis filter 98 on the short-term prediction coefficients line 130.

5.3 Synthesis Filter

The quantized LPC coefficients $A_q(z)$ 342 may be used by the synthesis filter 98 to filter the short-term prediction coefficients. The synthesis filter 98 may be a short-term inverse prediction filter that generates synthesized speech prior to post-processing. The synthesized speech may then be passed through the post-processing module 100. The short-term prediction coefficients may also be provided to the post-processing module 100.

5.4 Post-Processing

The post-processing module 100 processes the synthesized speech based on the rate selection and the short-term prediction coefficients. The short-term post filter module 540 may be first to process the synthesized speech. Filtering parameters within the short-term post filter module 540 may be adapted according to the rate selection and the long-term spectral characteristic determined by the characterization module 328 as previously discussed with reference to FIG. 9. The short-term post filter may be described by:

$$H_{st}(z) = \frac{\hat{A}\left(\frac{z}{\gamma_{1,n}}\right)}{\hat{A}\left(\frac{z}{\gamma_{2}}\right)},$$
 (Equation 27)

where in an example embodiment, $\gamma_{1,n} = 0.75 \cdot \gamma_{1,n-1} + 0.25 \cdot r_0$ and $\gamma_2 = 0.75$, and r_0 is determined based on the rate selection and the long-term spectral characteristic. Processing continues in the long term filter module 542.

The long term filter module 542 preforms a fine tuning search for the pitch period in the synthesized speech. In one embodiment, the fine tuning search is performed using pitch correlation and rate-dependent gain controlled harmonic filtering. The harmonic filtering is disabled for the quarter-rate codec 26 and the eighth-rate codec 28. The tilt compensation filter module 544, in one embodiment is a first-order finite impulse response (FIR) filter. The FIR filter may be tuned according to the spectral tilt of the perceptual weighting filter module 314 previously discussed with reference to FIG. 9. The filter may also be tuned according to the long-term spectral characteristic determined by the characterization module 328 also discussed with reference to FIG. 9.

The post filtering may be concluded with an adaptive gain control module 546. The adaptive gain control module 546 brings the energy level of the synthesized speech that has been processed within the post-processing module 100 to the level of the synthesized speech prior to the post-processing. Level smoothing and adaptations may also be performed within the adaptive gain control module 546. The result of the processing by the post-processing module 100 is the post-processed synthesized speech 20.

In one embodiment of the decoding system 16, frames received by the decoding system 16 that have been crased due to, for example, loss of the signal during radio transmission, are identified by the decoding system 16. The decoding system 16 can subsequently perform a frame erasure concealment operation. The operation involves interpolating speech parameters for the erased frame from the previous frame. The extrapolated speech parameters may be used to synthesize the erased frame. In addition, parameter smoothing may be performed to ensure continuous speech for the frames that follow the erased frame. In another embodiment, the decoding system 16 also includes bad rate determination capabilities. Identification of a bad rate selection for a frame that is received by the decoding system 16 is accomplished by identifying illegal sequences of bits in the bitstream and declaring that the particular frame is erased.

The previously discussed embodiments of the speech compression system 10 perform variable rate speech compression using the full-rate codec 22, the half-rate codec 24, the quarter-rate codec 26, and the eighth-rate codec 28. The codecs 22, 24, 26 and 28 operate with different bit allocations and bit rates using different encoding approaches to encode frames of the speech signal 18. The encoding approach of the full and half-rate codecs 22 and 24 have different perceptual matching, different waveform matching and different bit allocations depending on the type classification of a frame. The quarter and eighth-rate codecs 26 and 28 encode frames using only parametric perceptual representations. A Mode signal identifies a desired average bit rate for the speech compression system 10. The speech compression system 10 selectively activates the codecs 22, 24, 26 and 28 to balance the desired average bit rate with optimization of the perceptual quality of the post-processed synthesized speech 20.

While various embodiments of the invention have been described, it will be apparent to those of ordinary skill in the art that many more embodiments and implementations are possible that are within the scope of this invention. Accordingly, the invention is not to be restricted except in light of the attached claims and their equivalents.

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MICROFICHE APPENDIX

APPENDIX A

NSDOCID: <WO 0122402A1 1 >

```
63
  */
  /* Conexant System Inc.
                                       */
5 /* 4311 Jamborec Road
                                         */
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  */
15 /* FILE: const.h
((a)*(a))
  #define
           sqr(a)
                    ((a)^*(a)^*(a))
  #define
           cube(a)
25
           MAX(a,b)
                      ((a)>(b)?(a):(b))
  #define
           ABS(a)
                     (((a)<0)? -(a):(a))
  #define
                     (((a)<(b))?(a):(b))
  #define
           MIN(a,b)
                     (((a)>=0)?(1):(-1))
   #define
           SIGN(a)
                       (INT64)((a)>0?((a)+0.5):((a)-0.5))
30 #define
           ROUND(a)
                      (INT16)(((a)+(b)+(c))/(3.0)+0.5)
   #define
            AVG(a,b,c)
                       1.0e30
   #define
           MAXFLT
           MINFLT
                      1.0e-30
   #define
                    0.00000000000000001
   #define
           EPSI
35
                              /* length of previous speech data */
            L_PREV
                        60
   #define
                            /* length of coding frame */
   #define
            L FRM
                      160
                          /* maximun length of subframe */
            L SF
                     80
   #define
                           /* length of subframe */
   #define
            L SF3
                     54
                           /* length of subframe */
40 #define
            L_SF4
                     40
```

```
64
                        /* length of subframe */
          L_SF0
                   53
  #definc
                          /* maximun number of subframes */
          N_SF_MAX
  #define
                        /* number of subframes 8.5 k*/
  #define
          N_SF4
                   4
                        /* number of subframes 4.0 k mode 1*/
  #define
          N_SF3
                   3
                        /* number of subframes 4.0 k mode 0*/
5 #define
          N_SF2
                                   10
           NP
  #define
                                    10
  #define
             NP
                         /* length of LPC window */
  #define
          L_LPC
                   240
                          /* length of LPC lookahead */
10 #define
          L_LPCLHD
                     80
  8000.0 /* Sampling frequency */
  #define FS
                              /* 8.5 kbps */
20 #define RATE8 5K
                              /* 4.0 kbps */
  #define RATE4 0K
                              /* 2.0 kbps */
  #dcfine RATE2_0K
                             /* 0.8 kbps */
  #define RATEO_8K
         Bit-rate including overhead for the four rates
                              /* 8.5 kbit/s source coding */
                   9600
  #dcfine RATE_I_I
                              /* 4.0 kbit/s source coding */
30 #define RATE_1_2
                   4800
                              /* 2.0 kbit/s source coding */
  #define RATE_1_4
                   2400
                              /* 0.8 kbit/s source coding */
  #definc RATE_1_8
                   1200
                              /* Number of quantized Sinc interpolation
35 #define NUM_SINC_WIN
                         21
                                    /* windows
```

65 #define ENH_DELAY #define PPR FLT_ORDER 2 10 #define LP_FLT_ORDER #define SE_MEM_SIZE #define SE_HIS_SIZE 5 #definc SE_RAMP_SIZE 40 15 #define DELTA_THRLD 48.0 ______________ _______ 20 #define MEM_CLASS 140 #define N Lp 3 25 #define MAX_N_SF 8 #define MAX_L_SF 20 40 /* SHOULD NOT BE CHANGED - HARDWIRED AT PLACES */ #define L_LPC_SF #define OVERLAP 20 (L_LPC_SF+2*OVERLAP) 30 #define LPC_WIN1 #define CLA_MEM_SIZE 4 #define SLOPE_MAX_SIZE 5 #define GAMMA1 0.9

66 5 #define MAXPN 11 #define MAX_FB_PULSE_NUM 5 #define SEG_NUM_M1 16 #define SEG_NUM_M0 10 #define L_HF 20 80 #define L_CORR (L_CORR+L_SF+10) #define L_WSP 15 #define PAST_PGAIN_MAX2 0.8 20 /* Entries 1. basis vector: 13 bit codebook */ #define N_GAUSS_13b 45 #define NUM_PRESELECT 2 #define SINC_LIMIT_E 10 (2*SINC_LIMIT_E+1) 30 #define LEN_SINC_E (NUM_SINC_WIN*LEN_SINC_E) #define LEN_SINC_TAB_E (L_PREV+L_FRM+L_LPCLHD) /* length of sig_ppr = 260 */ #dcfine L_PP #define SINC_LIMIT_PP 10 40 #define LEN_SINC_PP (2*SINC_LIMIT_PP+1)

```
#define LEN SINC_TAB_PP (NUM_SINC_WIN*LEN_SINC_PP)
   #define HALFP
   #define MAX_PULS_NUM 5
 5 #define MAX_DIF_TAB 16
   #dcfine MAX_L_TG
                                          /* Maximum length for LT preprocessing
                       180
                                          /* Note: it must to be >= L_FRM and > L_SF */
10 #dcfine MAX_DELAY
        15
   #define NUM_MAX_SRCH 4
   #define SINC_LIMIT
   #define LEN SINC
                             (2*SINC_LIMIT+1)
   #dcfine LEN_SINC_TAB (NUM_SINC_WIN*LEN_SINC)
20 #define DELT_F
                             (1.0/((double)NUM SINC WIN-1.0)) /* Precision of the */
  interpolation
                                                 /* minimum pitch lag */
                      17
   #dcfine MIN_LAG
                             (L_FRM+SINC_LIMIT_E+2)
25 #define MAX_LAG
                                                       /* Maximum pitch lag
                                                              /* Note: must to be > 170 and */
                                                                   > HI LAG+11
  #define L_OLPIT
                      (L_FRM+L_LPCLHD+MAX_LAG)
                                                       /* Open loop pitch window */
30
                                                 /* maximum pitch lag */
  #define HI_LAG
                  120
  #define HI_LAG2
                                                 /* maximum pitch lag */
  #define MIN_LAG2
                                                 /* minimum pitch lag */
                    (HI_LAG2-127)
35 #define PIT F MEM
  /*======== DEFINITIONS FOR CLOSED LOOP PITCH ============
40
```

```
68
  #define LEN_PITCH_TAB_5BIT 17
  #define LEN_PITCH_TAB_7BIT 129
  #define LEN_PITCH_TAB_8BIT 257
5 #define MAX_PIT_IDX_5b
                                     16
  #define MAX_PIT_IDX_7b
                                     127
  #define MAX_PIT_IDX_8b
                              255
                                            (1.0/(FLOAT64)DELT_F)
   #define DELT_F2
10
           /* minimum value of the past quantized */
                                     0.2
15 #define PAST_PGAIN_MIN
                                                  /* pitch gain for the adaptive prefilter */
                                                  /* in the algebraic codebok search
                                                  */* maximum value of the past quantized */
                                     1.0
   #define PAST_PGAIN_MAX
                                                   /* pitch gain for the adaptive prefilter */
20
                                                   /* in the algebraic codebok search
                                     64
   #define MSMAX_1_64
                                     32
25 #define MSMAX_1_32
                                      64
   #define MSMAX_2_64
                                      128
   #define MSMAX_2_128
   #define MSMAX_3_256
                                      256
   #define MSMAX_4_1024
                               1024
 30
    #define GP_BUF_SIZE
    #define BETA_BUF_SIZE
 35
    #define GVQ_VEC_SIZE_2D
    #define GVQ_VEC_SIZE_3D
    #define TAB_SIZE_GVQ_3D
```

```
#define GVQ_VEC_SIZE_4D
  #dcfine TAB_SIZE_GVQ_4D
  #define CORR_SIZE
             10 #define DSP_BUFF_SIZE
  #define SMO_BUFF_SIZE
                             15
  #define MAX_GRP_SIZE
  #define HL_COUNT_MAX
                             15
15 #define ADAPT_THRESHOLDS 1
  #define N_MODE_SUB_START -4
  #define UPDATE_MAX
                             15
  #define CONSEC_MAX
                             31
20
                                     */
                Smooth LSF
25 #define BETA_FRM
                  0.90
  #define BETA_SUB
                  0.70
  #define dSP1
                  0.0015
  #define dSP2
                  0.0025
  #define dSP_int1
                  0.0024
30 #define Rllim
                  0.50
  35
                                  /* the maximal number of condidate per stage */
  #define MAX_CAND_LSFQ
                        10
                             /* number of LSF codebooks */
  #define N LSFVQCDBK 5
                                 /* maximal order of LPC */
  #dcfinc MAXLNp
                      /* the maximal number of codebooks */
  #define MAXLTT_08k
                      /* the maximal number of codebooks */
40 #define MAXLTT_40k
```

```
70
                              /* the maximal number of codebooks */
  #dcfine MAXLTT_85k
                                  /* number of switch predictors */
  #define LP_08k
                   1
                           /* the number of delayed predictors */
  #define LPL_08k 1
                            /* the number of entries for the 1 code */
5 #define LMS1 08k 16
                            /* the number of entries for the 2 code */
   #define LMS2_08k 16
                            /* the number of entries for the 3 code */
   #define LMS3_08k 8
   #define LMSMAX_08k 16
   #define LQMA_08k 4
10
                          /* number of switch predictors */
   #define LP_40k 2
                           /* the number of delayed predictors */
   #define LPL 40k 2
                             /* the number of entries for the 1 code */
   #define LMS1 40k 128
                             /* the number of entries for the 2 code */
   #define LMS2_40k 128
                             /* the number of entries for the 3 code */
15 #define LMS3_40k 64
   #define LMSMAX_40k 128
   #define LQMA_40k 4
                           /* number of switch predictors */
   #define LP_85k 1
                           /* the number of delayed predictors */
20 #define LPL_85k 1
                              /* the number of entries for the 1 code */
   #define LMS1_85k 128
                             /* the number of entries for the 2 code */
   #define LMS2_85k 64
                             /* the number of entries for the 3 code */
   #define LMS3_85k 64
                             /* the number of entries for the 4 code */
   #define LMS4 85k 64
25 #define LMSMAX_85k 128
    #dcfine LQMA_85k 2
                              /* the number of stages for T */
    #define LTT_08k
                              /* the number of stages for T */
30 #define LTT_40k
                              /* the number of stages for T */
    #define LTT 85k
    1
    #define VOICE
    #define NOISE
```

40 #define INIT_FRAME

```
71
                             10
  #define INIT COUNT
  #define VAD LPC_ORDER
5 #define LTP_BUFF_SIZE
                       5
  #define FLAG_VAD_MEM_SIZE 2
                             3
  #define VAD_MEM_SIZE
  #define VAD_MIN_MEM_SIZE 8
10
                             1.0e30
  #define MAX_ENERGY
                                       /* minimum pitch lag */
                                  17
  #define MIN_LAG
/*======== DEFINITIONS FOR BAD FRAME HANDLING =========
                                  /* LSF: bad frame handling overhang */
  #define BFH_OH
20 #define ERNG_MEM_SIZE
                       3
  #define MAX_BFI_COUNT
  #define PACKWDSNUM 12 /* the number of words to pack the SMV bits
              (1 for rate/erasure control, 11*16=171 for data */
  typedef struct {
         INT16 idx_lsf[N_LSFVQCDBK+1];
30
             INT16 idx_SVS_deci;
             INT16 idx_ltp_deci[N_SF_MAX];
         INT16 idx_pitch[N_SF_MAX];
             INT16 idx_Gp_VQ;
             INT16 idx_Gc_VQ;
35
             INT16 idx gainVQ[N_SF_MAX];
             INT16 idx subcpcb[N_SF_MAX][5];
             INT16 idx_cpcbsign[N_SF_MAX][20];
         INT16 idx_cpcb[N_SF_MAX][20];
             INT16 idx_center[N_SF_MAX];
40
```

	12
	INT16 fix_rate;
	INT16 idx_lpc_int;
	INT16 idx_vad;
5	INT16 idx_pitchgainQ[N_SF_MAX];
	INT16 ltp_mode;
	INT16 idx_pit_frac[N_SF_MAX];
	INT16 idx_pit_int[N_SF_MAX];
	INT16 idx_fcbpit_frac[N_SF_MAX][2];
10	INT16 idx_fcbpit_int[N_SF_MAX][2];
	} PARAMETER;
/*= -*/	*/

/*=====================================	73	
•		
/*=====================================	*/	:======*/
/* Conexant System Inc.	*/	
/* 4311 Jamborec Road	•	
/* Newport Beach, CA 92660	*/	
/*		
/* Copyright(C) 2000 Conexant System Inc.		
/* ALL RIGHTS RESERVED:	*/	
/* No part of this software may be reproduce	ed in any form or by any */	
/* means or used to make any derivative wo		
/* or adaptation) without the authorisation of		
/*====================================		======*/
/* FILE: decoder.c	*/	
/+=====================================		=======*/
	•	
/*		
/* INCLUDE	*/	
/*	*/	
	·	
#include "typedef.h"		
#include "main.h"		
#include "const.h"	·	
#include "mcutil.h"		
#include "gputil.h"		
#include "ext_var.h"		
·		
#include "lib_lpc.h"		
#include "lib_qlsf.h"		
#include "lib_geq.h"		
#include "lib_bit.h"		
#include "lib_cla.h"		
#include "lib_pit.h"		
#include "lib_ltp.h"	·	
#include "lib_ppr.h"	,	
#include "lib_fcs.h"	•	
#include "lib_gcb.h"		

```
#include "lib_flt.h"
 #ifdef DIAG_SMV
5
 #include "lib_dia.h"
  #endif
10 /*-----*/
  /*----- FUNCTIONS ------
  15 /* FUNCTION : dec_smv_frame ().
 /* PURPOSE : Performs processing on one frame.
  /* ALGORITHM:
20 /*----*/
  /* INPUT ARGUMENTS:
        _(INT16 []) scrial: intput frame bitstream. */
  /* OUTPUT ARGUMENTS :
25 /* _ (FLOAT64 []) sigout: output decoded frame.
  /* INPUT/OUTPUT ARGUMENTS:
           None.
30 /* RETURN ARGUMENTS:
           None.
   void dec_smv_frame (INT16 PackedWords [], FLOAT64 *sigout, INT16 switch_flag)
        {
 35
        INT16 i_sf,i_s, l_sf, i,j, n_sf;
        FLOAT64 x, y;
 40
```

	/*		*/
	/* Parameters for bitstream processing		*/
5	INT16 parm[3];		
	INT16 shft_count;		
	INT16 PackWdsPtr[2];		
	PARAMETER channel;		
10			
	/+		.*/
	/* Gain MA-VQ	*/	
	/*		*/ */ */ */ */
15	FLOAT64 gainQ[N_SF_MAX];		
	/*		-*/
	/* LSF quantisation	*/	
	/*		-*/
20			
	INT16 exp_flg;		
	FLOAT64 lsfq_new_dec[NP], lpcgain_q;		
	/*		
	·		
25	/* Bad Frame Concealement		•
	/*		/
	INT16 bfi;		
	INT16 update;		
30	111710 upune,		
50	FLOAT64 enrg;		
	FLOAT64 fec_refl[NP];		
	FLOAT64 temp_lagf;		
	FLOAT64 ET_buf[N_SF_MAX];		
35	,		
	/*		-*/
	FLOAT64 ForPitch_dec[L_FRM];		
	FLOAT64 ForPitch_decTEMP[L_FRM];		
40			

76 /* and a notation pananananananananananana*/DECODER /* ppacaunananananananananana 5 /* masamamamamamamamamamama bfi = 0;update = 0; 15 Initalizing bit un-packing parameters 20 PackWdsPtr[0] = 16;PackWdsPtr[1] = 1;switch (PackedWords[0]) 25 case 0: nrerror("Blank detected..."); break; case 1: channel.fix_rate = RATE0_8K; if $(bfh_oh > 0)$ 30 $N_bfi = 0$; bfh_oh--, break; 35 case 2: channel.fix_rate = RATE2_0K; if $(bfh_oh > 0)$

N bfi = 0;

```
77
                                               bfh_oh--;
                              break;
 5
                     case 3: channel.fix_rate = RATE4_0K;
                                       if (bfh_oh > 0)
                                               N_bfi = 0;
                                               bfh_oh--;
10
                                               }
                              break;
                     case 4: channel.fix_rate = RATE8_5K;
                                       if (bfh_oh > 0)
                                               {
                                               N_bfi = 0;
15
                                               bfh_oh--;
                              brcak;
20
                     case 14: bfi = 1;
                                       N_bfi++;
                                       bfh_oh = BFH_OH+1;
                              break;
                     case 15: nrerror("Full Rate Portable detected...?\n");
25
                              break;
                     default: nrerror("Illegal control word\n");
                              break;
30
                     }
            if(bfi == 1)
                     nbfi_count = 0;
35
             else if (nbfi_count < 5)
                     nbfi_count++;
            if (nbfi_count > 3)
                     bfi_caution = 0;
40
```

```
if (bfi == 1)
                     {
                     frm_erasure++;
                     channel.fix_rate = fix_rate_mem;
5
                     channel.idx_SVS_deci = SVS_deci_mem;
                         Decode bit-stream if is a good frame
10
             if (bfi == 0)
                     {
                      BIT_bits_to_cdbk_index(PackedWords, PackWdsPtr, &channel);
15
                      if (channel.fix_rate == RATE2_0K)
                              {
                               parm[0] = channel.idx_lsf[0];
                               parm[1] = channel.idx_lsf[1];
20
                               parm[2] = channel.idx_lpc_int;
                               seed_dec = 0x000000000;
                               shft_count = 15;
                               for (j = 0; j < 3; j++)
                                       {
 25
                                        shft count -= bitno0[j];
                                        secd_dec = seed_dec ^(((long) parm[j] ) << shft_count);</pre>
                               }
 30
                       clsc
                                if (channel.fix_rate == RATE0_8K)
                                         parm[0] = channel.idx_lsf[0];
 35
                                         parm[1] = channel.idx_lsf[1];
                                         parm[2] = channel.idx_lsf[2];
                                         secd_dec = 0x000000000;
                                         shft_count = 11;
                                         for (j = 0; j < 3; j++)
  40
```

```
79
                                    shft_count -= bitnol(j);
                                    seed_dcc = seed_dec ^(((long) parm[j] ) << shft_count);</pre>
5
                             }
                }
         else
10
                        Save adaptive codebook temporaly
                cpy_dvector(ext_dec, ext_dec_mem, 0, MAX_LAG-1);
15
                }
    /* DEDECORDED CONTROL LSF INVERSE QUANTIZATION AND INTERPOLATION DEDECORDED CONTROL */
                      Get the quantized Isf
25
          LSF_Q_lsf_dccodc (bfi_caution, &exp_flg, lsfq_new_dcc, channel.idx_lsf,
                             bfi, nbfi_count, channel.fix_rate);
                  */
30
              Interpolate the QUANTIZED 1sf and calculate corresponding */
                       the LPC coefficients
35
          if (channel.fix_rate == RATE4_0K)
                 if (channel.idx_SVS_deci == 1)
                       LPC_interpolate_lpc_4to3 (lsfq_new_dec, (FLOAT64 *)NULL,
```

lsfq_old_dec, pdcfq_dec,

```
1);
                    else
                            LPC_intcrpolate_lpc_4to2 (lsfq_new_dec, (FLOAT64 *)NULL,
                                                                                        lsfq_old_dec, pdcfq_dec,
5
   1);
                    }
            else
10
                     if ((channel.fix_rate == RATE2_0K) ||
                                     ((channel.fix_rate == RATE8_5K) &&
                                              (channel.idx_SVS_deci == 0)))
   #ifdef ALGO_BUG_FIX
                                                       if (bfi == 0)
15
                                                                LPC adptive_interp_dec(lsfq_new_dec,
                                                                         lsfq_old_dec, pdcfq_dec,
                                                                                 channel.idx_lpc_int);
20
                                                                }
                                                       clsc
                                                                LPC_adptive_interp_dec(lsfq_new_dec,
                                                                         lsfq_old_dec, pdcfq_dec, 0);
 25
     #else
                                                        LPC\_adptive\_interp\_dcc(lsfq\_ncw\_dec, lsfq\_old\_dec,
                                                                         pdcfq_dec, channel.idx_lpc_int);
     #cndif
 30
                       else
                               LPC_adptive_interp_dec(lsfq_new_dec, lsfq_old_dec,
                                                                                  pdcfq_dec, 0);
                      }
  35
               LPC_pred2refl (pdcfq_dec[0], fec_rcfl, NP);
                       Determination of the flatness of the input speech
                                                                           */
  40
```

```
81
            CLA_Identify_Input (channel.idx_SVS_deci, pdcfq_dec[0], lsfq_new_dec[0],
                             pgain_past_dec, &lpcgain_q, channel.fix_rate, &FlatSp_Flag);
 5
                           Interpolation of Pitch Lag
            if (channel.idx_SVS_deci == 1)
10
                    {
                    PIT_pitch_track_recons (fix_rate_mem, bfi, past_bfi, ppast_bfi,
                                                              lag_f, SVS_deci_mem, qua_gainQ, channel,
                                                                      &pitchf, pitch_f_mem,
15 ForPitch_decTEMP);
                     PIT_PitchInterpolat (pitchf, pitch_f_mem, ForPitch_dec, 0);
                    }
             if ((channel.idx_SVS_deci == 0) && (channel.fix_rate == RATE8_5K))
20
                    temp_lagf = PitLagTab8b[channel.idx_pitch[0]];
             clse if ((channel.idx_SVS_deci == 0) && (channel.fix_rate == RATE4_0K))
                    temp_lagf = PitLagTab7b[channel.idx_pitch[0]];
             LTP_adap_cbk_correction (fix_rate_mem, bfi, past_bfi, ppast_bfi,
25
                                                       qua_gainQ, gainQ, SVS_deci_mcm, temp_lagf,
                                                       &update, pitch_f_mem, lag, lag_f,
                                                       ForPitch_decTEMP, ext_dec_mem, ext_dec,
                                                       unfcod dec, channel);
30
                    Gaussian excitation for very low bit rates
             if ((channel.fix_rate == RATE2_0K) || (channel.fix_rate == RATE0_8K))
35
                      cpy_dvcctor(gp_buf+4, gp_buf, 0, 4-1);
                      ini_dvector(gp_buf+4, 0, 4-1, 0.0);
                      GCB_gauss_excit_dec (&seed_dec, channel.fix_rate, qua_unfcod[1]);
 40
```

```
if (channel.fix_rate == RATE2_0K)
                                Decode the fixed codebook gain for sub-frame
                                   # 1 and # 3 coded with 6 + 6 bits
5
                            if ((fix_ratc_mem == RATE8_5K) || (fix_rate_nicm == RATE0_8K))
                                   GEQ_update_mem_4d_to_2d();
                            else if ((fix_rate_mcm == RATE4_0K) && (SVS_deci_mcm != 0))
10
                                   GEQ_updatc_mem_3d_to_2d();
                            GEQ_dec_gains_1_6 (0, channel.idx_gainVQ[0], qua_gainQ,
                                                                    qua_unfcod[1], N_bfi);
15
                            GEQ_dec_gains_1_6 (2, channel.idx_gainVQ[1], qua_gainQ,
                                                                    qua_unfcod[1], N_bfi);
                            qua_gainQ[1][1] = qua_gainQ[1][0];
                            qua_gainQ[1][3] = qua_gainQ[1][2];
20
                    elsc
                                 Decode the fixed codebook gain for sub-frame */
25
                                         # 1 coded with 6 bits
                             if ((fix_rate_mem == RATE4_0K) || (fix_rate_mem == RATE2_0K))
                                     {
30
                                     if (SVS_deci_mcm == 0)
                                             GEQ_update_mem_2d_to_4d();
                                     elsc
                                             GEQ_update_mem_3d_to_4d();
                                     }
 35
                              GEQ_dec_gains_1_5 (channel.idx_gainVQ[0], qua_gainQ,
                                                                     qua_unfcod[1], N_bfi);
                              for (i = 0; i < N SF4; i++)
 40
```

```
qua\_gainQ[1][i] = qua\_gainQ[1][0];
                      }
               }
5
  10
      -----*/
                                          */
                  - quantized prediction coefficients
15 /*
         pdcfq_dec
         unfcod_dec[0] - adaptive codebook (ACB) vector
         unfcod_dec[1] - algebraic codebook (CDBK) vector
         gainQ[0]
                   - ACB gain
                   - CDBK gain
         gainQ[1]
                                          */
20 /*
                  - adaptive codebook
         ext_dec
                  - integral lag of the current subframe
         lag[i_sf]
                  - fractional lag of the current subframe
         lagf[i_sf]
                                          */
                     Set number of subframes
30
          if (channel.fix_rate != RATE4_0K)
                n_sf=N_SF4;
          elsc
35
                if (channel.idx_SVS_deci == 1)
                      n_sf = N_SF3;
                else
                      n_sf = N_SF2;
40
                }
```

```
/*nnonmonn First decoding loop for the decoder classification purpose nnonmon*/
5
            i_s = 0;
            for (i_sf = 0; i_sf < n_sf; i_sf++)
10
                                   Set length of subframe
                     if (channel.fix_rate != RATE4_0K)
15
                              1_sf = L_sF4;
                      else
                              if (channel.idx_SVS_deci == 1)
 20
                                       if (i_sf == N_SF3-1)
                                               l_sf = L_SF3;
                                        else
                                                1 sf = L_SF0;
  25
                                else
                                        l_sf = L_sF;
                                }
  30
                                    Adaptive Codebook Contribution
                        if ((channel.fix_rate == RATE8_5K) ||
                                         (channel.fix_rate == RATE4_0K))
   35
                                          if (channel.idx_SVS_deci == 1)
                                                  LTP_PP_pitch_ext_decod (ForPitch_dec+i_s, ext_dec,
                                                                           i_sf, unfcod_dec[0], lag, lag_f, l_sf);
    40
```

```
cpy_dvcctor (unfcod_dec[0], qua_unfcod[0]+i_s, 0,
                                                                          l_sf-1),
                                               lag[i\_sf] = (INT16)(ForPitch\_dcc[l\_sf/2+i\_s]+0.5);
5
                                               }
                                       else
                                                if (channel.fix_rate == RATE8_5K)
                                                        LTP_8_5k_pitch_decod (bfi, ext_dec, i_sf,
10
                                                                 unfcod_dec[0], lag, lag_f, l_sf,
                                                                         channel.idx_pitch);
                                                else
                                                        LTP_7b_pitch_decod (bfi, ext_dec, i_sf,
                                                                 unfcod_dec[0], lag, lag_f, l_sf,
15
                                                                         channel.idx_pitch);
                                                cpy_dvcctor(unfcod_dec[0], qua_unfcod[0]+i_s,
                                                                          0, l_sf-1);
                                               }
20
                                       }
                              clsc
                                       gainQ[0] = 0.0;
                                       qua_gainQ[0][i_sf] = 0.0;
25
                                       ini_dvector(unfcod_dec[0], 0, 1_sf-1, 0.0);
                                       ini_dvector(qua_unfcod[0]+i_s, 0, l_sf-1, 0.0);
30
                                    Fixed Codebook Contribution
                      switch (channel.fix_rate)
35
                                               RATE 8.5 kps
40
```

```
case RATE8_5K:
                                      if (channel.idx_SVS_deci == 1)
                                              {
                                               GEQ_Dec_PitchGain_4D (N_bfi, i_sf, channel.idx_Gp_VQ,
 5
                                                                                        gainQ);
                                               if (bfi == 0)
                                                       FCS_cdbk_decod_30b_sub40 (unfcod_dec[1],
                                                               l_sf, i_sf, lag[i_sf], gainQ[0], &channel);
                                               }
10
                                       clse
                                               {
                                               if (bfi == 0)
                                                        FCS\_cdbk\_decod\_22b\_sub40(unfcod\_dec[1],
                                                               l_sf, i_sf, lag[i_sf], pgain_past_dec,
- 15
                                                                        &channel);
                                               }
                                       break;
 20
                                               RATE 4.0 kps
                               casc RATE4_0K:
  25
                                        if (channel.idx_SVS_deci == 1)
                                                 GEQ_Dec_PitchGain_3D (N_bfi, i_sf, channel.idx_Gp_VQ,
                                                                                                   gainQ);
  30
                                                 if (bfi == 0)
                                                         FCS_cdbk_dccod_13b_sub54(ext_dec,
                                                                          pdcfq_dec[i_sf], unfcod_dec[1], l_sf,
                                                                                  i_sf, lag[i_sf], gainQ[0],
                                                                                           fix_rate_mem, lpcgain_q,
   35
                                                                                                   &channel);
                                                 }
                                         clsc
```

{

```
87
                                              if (bfi == 0)
                                                      FCS_cdbk_dccod_15b_sub80(ext_dec,
                                                                       pdcfq_dec[i_sf], unfcod_dec[1], l_sf,
                                                                                i_sf, lag[i_sf], pgain_past_dec,
                                                                                        fix_rate_mem, lpcgain_q,
 5
                                                                                                 &channel);
                                              }
                                     break;
10
                                             RATE 2.0 kps
                              case RATE2_0K:
15
                                      cpy_dvector (qua_unfcod[1]+i_s, unfcod_dec[1], 0,
                                                                                                 l_sf-1);
                                      gainQ[1] = qua_gainQ[1][i_sf];
                                      break;
20
                                             RATE 0.8 kps
                                                                         */
                              case RATE0_8K:
25
                                      break;
                              default: nrerror ("Invalid rate !!");
                                      break;
                              }
30
                      if ((bfi == 1) && ((channel.fix_rate == RATE8_5K) ||
                                               (channel.fix_rate == RATE4_0K)))
35
                              {
                               for (i = 0; i < l\_sf; i++)
                                      unfcod_dec[1][i] = GCB_gauss_noise(&seed_bfi_exc);
                              }
 40
```

```
88
                              Adaptive and Fixed Codebook Gains
                    if ((channel.fix_rate == RATE8_5K) ||
5
                                    (channel.fix_rate == RATE4_0K))
                                                RATE 8.5 kbps and 4.0 kbps
10
                                     cpy_dvector (unfcod_dec[1], qua_unfcod[1]+i_s, 0,
                                                                                       1_sf-1);
                                     if (channel.idx_SVS_deci == 0)
15
                                              if (channel.fix_rate == RATE8_5K)
                                                      {
                                                      if (((fix_rate_mem == RATE4_0K) ||
                                                                      (fix_rate_mem == RA^{t}TE2_0K)) \&\&
20
                                                                               (i_sf == 0))
                                                               if (SVS_deci_mem == 0)
                                                                      GEQ_update_mem_2d_to_4d();
                                                               eise
25
                                                                       GEQ_update_mem_3d_to_4d();
                                                               }
                                                       GEQ_dcc_gains_2_7 (channel.fix_rate,lag[0],
                                                                       channel.idx_gainVQ[i_sf], gainQ,
 30
                                                                               N_bfi, unfcod_dec[1], i_sf,l_sf);
                                                       if ((fec_refl[0] > 0.0) && (N_bfi > 0))
                                                                       gainQ[0] = 0.4;
                                                       }
 35
                                               else
                                                        if (((fix_rate_mem == RATE8_5K) ||
                                                                (fix_rate_mem == RATE0_8K)) \&\&
                                                                        (i_sf == 0))
  40
```

```
89
                                                                     {
                                                                     GEQ_update_mem_4d_to_2d();
                                                                     }
                                                     clse
                                                             {
 5
                                                             if ((fix_rate_mem == RATE4_0K) &&
                                                                             (SVS_deci_mem != 0) &&
                                                                                      (i_sf == 0)
10
   GEQ_update_mem_3d_to_2d();
                                                                                      }
                                                             }
                                                      GEQ dec_gains_2_7(channel.fix_rate,lag[0],
15
           channel.idx_gainVQ[i_sf],
                                                                                              gainQ, N_bfi,
    unfcod_dec[1],
                                                                                                       i_sf,
20
    l_sf);
                                                      if ((fec_refl[0] > 0.0) && (N_bfi > 0))
                                                             gainQ[0] = 0.4;
                                                     }
25
                                             }
                                    else
                                              if (channel.fix_rate == RATE8_5K)
30
                                                      if (((fix_rate_mcm == RATE4_0K) ||
                                                                      (fix_rate_mem == RATE2_0K))\&\& (i_sf
    == 0))
                                                                      {
                                                                       if (SVS_deci_mem == 0)
35
                                                                              GEQ_update_mem_2d_to_4d();
                                                                       else
                                                                              GEQ_update_mem_3d_to_4d();
                                                                      }
                                                      GEQ\_dec\_gc\_4\_10 \; (channel.idx\_Gc\_VQ, \, \& (gainQ[1]), \\
 40
```

```
90
                                                                                    N_bfi, unfcod_dec, i_sf);
                                                   }
                                           else
                                                    if (((fix_rate_mcm == RATE8_5K) \parallel
5
                                                           (fix_rate_mcm == RATE0_8K)) && (i_sf == 0))
                                                                    GEQ_update_mem_4d_to_3d();
                                                    else if (SVS_deci_mem == 0)
                                                                    GEQ_update_mem_2d_to_3d();
10
                                                    GEQ\_dec\_gc\_3\_8 (channel.idx\_Gc\_VQ, \& (gainQ[1]),
                                                                    N_bfi,unfcod_dec, i_sf);
                                                    }
                                            }
15 #ifdef PROG_BUG_FIX
                                     qua_gainQ[1][i_sf] = gainQ[1];
    #endif
                                     qua_gainQ[0][i_sf] = gainQ[0];
                                     pgain_past_dcc = gainQ[0];
 20
                                     if (pgain_past_dec < PAST_PGAIN_MIN)
                                             pgain_past_dec = PAST_PGAIN_MIN;
                                      if (pgain_past_dec > PAST_PGAIN_MAX)
                                             pgain_past_dcc = PAST_PGAIN_MAX;
 25
                                     }
                             else
                                                 RATE 2.0 kbps and 0.8 kbps
 30
                                       gainQ[0] = 0.0;
                                       pgain_past_dcc = 0.0;
                                       qua_gainQ[0][i_sf]=0.0;
  35
                                       gainQ[1]=qua_gainQ[1][i_sf];
                                       }
                                             Build the excitation
   40
```

```
91
                             if(exp_flg == 1)
                                             wad_dvector(qua_unfcod[0]+i_s, gainQ[0],
                                                                              qua_unfcod[1]+i_s, 0.7*gainQ[1],
5
                                                                          ext_dec+MAX_LAG, 0, l_sf-1);
                             else
                                             wad_dvector(qua_unfcod[0]+i_s, gainQ[0],
                                                                               qua_unfcod[1]+i_s, gainQ[1],
10
                                                                                       ext_dec+MAX_LAG, 0,
   1_sf-1);
   #ifdef PROG_BUG_FIX
15
                             GEQ_energy_extrapolation (i_s, i_sf, l_sf, gainQ,
                                              qua unfcod, bfi, lag, ext_dec, Prev_Beta_Pitch,
                                                              ET_buf, ForPitch_dec, channel);
   #else
20
                             GEQ_energy_extrapolation (i_s, i_sf, l_sf, gainQ, bfi, lag,
                                                      ext_dec, Prev_Bcta_Pitch, ET_buf, channel);
   #endif
25
                                         Temporary buffer
                             cpy_dvcctor(ext_dec+MAX_LAG, sigout+i_s, 0, l_sf-1);
30
                                       Update the adaptive codebook
                             cpy_dvcctor(ext_dec+1_sf, ext_dec, 0, MAX_LAG-1);
35
                              i_s += l_sf;
                             }
40
```

```
if ((channel.fix_rate == RATE4_0K) && (channel.idx_SVS_deci == 1))
                              cpy_dvector(gp_buf+4, gp_buf, 0, 4-1);
 5
                              gp_buf[4] = qua_gainQ[0][0];
                              gp_buf[5] = gp_buf[6] = qua_gainQ[0][1];
                              gp_buf[7] = qua_gainQ[0][2];
                     else
10
                              if ((channel.fix_rate == RATE4_0K) &&
                                     (channel.idx_SVS_dcci!=1))
                                      cpy_dvector(gp_buf+4,gp_buf, 0, 4-1);
15
                                      if ((ET_buf[0] < 40.0) && (qua_gainQ[0][0] > 0.7))
                                              gp_buf[4] = gp_buf[5] = 0.4;
                                      else
                                              gp_buf[4] = gp_buf[5] = qua_gainQ[0][0];
20
                                      if ((ET_bu[1] < 40.0) && (qua_gainQ[0][1] > 0.7))
                                              gp_buf[6] = gp_buf[7] = 0.4;
                                      else
                                              gp_buf[6] = gp_buf[7] = qua_gainQ[0][1];
25
                                     }
                              clse
                                      if (channel.fix_rate == RATE8_5K)
30
                                              cpy_dvector(gp_buf+4, gp_buf, 0, 4-1);
                                              for (i = 0; i < 4; i++)
                                                       if ((ET_buf[i] < 40.0) && (qua_gainQ[0][i] > 0.7)
                                                               && (channel.idx_SVS_deci != 1))
35
                                                                       gp_buf[i+4] = 0.4;
                                                       clsc
                                                                       gp_buf[i+4] = qua_gainQ[0][i];
                                                      }
                                             }
40
```

}

```
i_s = 0;
          for (i_sf = 0; i_sf < n_sf; i_sf++)
10
                            Set the subframe length
15
                 if (channel.fix_rate!= RATE4_0K)
                       l_sf = L_SF4;
                 clse
                        if (channel.idx_SVS_deci == 1)
20
                               if (i_sf == N_SF3-1)
                                     l_sf = L_SF3;
                               else
                                     l_sf = L_SF0;
25
                               }
                        else
                              l_sf = L_SF;
                        }
30
                 cpy_dvector(sigout+i_s, sigsyn_dec+NP, 0, 1_sf-1);
35
                              Synthesis filter
                 FLT_allsyn (sigsyn_dec+NP, l_sf, pdcfq_dec[i_sf], NP,
                                             sigsyn_dcc+NP, synth_mem_dec);
40
```

```
Post processing
5
                    switch (channel.fix_rate)
                             {
                             case RATE8_5K:
                                              if (FlatSp_Flag == 0)
                                                       {
10
                                                       x = 0.65;
                                                       y = 0.4;
                    else
15
                              x = 0.63;
                              y = 0.4;
                                       break;
20
                              case RATE4_0K: x = 0.63;
                                                                y = 0.5;
                                       break;
                               case RATE2_0K: x = 0.63;
25
                                                                y = 0.0;
                                       break;
                               case RATE0_8K: x = 0.63;
                                                                y = 0.0;
 30
                                       break;
                               default: nrerror ("Invalid fix_rate !!!");
                                        break;
 35
                               }
                       PPR post_process (sigsyn_dec, pdcfq_dec[i_sf], x, y,
                                                lag_f[i_sf], l_sf, FlatSp_Flag, channel.fix_rate);
 40
```

```
95
                             Update the synthesized speech buffer
                                -----*/
                   cpy_dvector(sigsyn_dec+NP, sigout+i_s, 0, 1_sf-1);
5
                   i_s += l_sf;
10
           dot dvector(sigout, sigout, &enrg, 0, L_FRM-1);
            cnrg_buff[0] = enrg_buff[1];
15
            enrg_buff[1] = enrg_buff[2];
            enrg_buff[2] = 10.0*log10(enrg/L_FRM+EPSI);
            if ((enrg_buff[2] < 45.0) && (bfi == 1) && (channel.fix_rate == RATE8_5K)
                   && (channel.idx_SVS_deci == 0))
20
                    ini_dvector(past_energyq_4d, 0, GVQ_VEC_SIZE_4D-1, -30.0);
                    bfi_caution = 1;
                   }
            clsc if ((cnrg_buff[2] < 45.0) && (bfi == 1) &&
25
                 (channel.fix_rate == RATE4_0K) && (channel.idx_SVS_deci == 0))
           {
                    ini_dvcctor(past_energyq_2d, 0, GVQ_VEC_SIZE_2D-1, -30.0);
                    bfi_caution = 1;
30
            ppast_bfi = past_bfi;
            past_bfi = bfi;
35
            SVS_deci_mem = channel.idx_SVS_deci;
            fix_rate_mem = channel.fix_rate;
```

40 #ifdef MEM_TEST

	97 ====================================
/*=====================================	=======================================
/* Conexant System Inc.	*/
/* 4311 Jamborec Road	*/
/* Newport Beach, CA 92660	*/
/+	*/
/* Copyright(C) 2000 Conexant System Inc.	*/
/+	*/
/* ALL RIGHTS RESERVED:	*/
/* No part of this software may be reproduced in	any form or by any */
/* means or used to make any derivative work (su	ich as transformation */
/* or adaptation) without the authorisation of Cor	nexant System Inc. */
/*=====================================	±========*/
/* FILE : decoder.h	*/
/*	=======================================
/*	*/
/* FUNCTIONS	*/
/*	
void dec_smv_framc (INT16 [], FLOAT64 [], IN	TT16);
· · · · · · · · · · · · · · · · ·	
/*=====================================	
/* END	*/

98 */ /* Conexant System Inc. 5 /* 4311 Jamborec Road /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ 15 /* FILE: encoder.c /*-----INCLUDE -----#include "typedef.h" #include "main.h" #include "const.h" 25 #include "gputil.h" #include "mcutil.h" #include "ext_var.h" #include "lib_qlsf.h" 30 #include "lib_gcq.h" #include "lib_vad.h" #include "lib_lpc.h" #include "lib_cla.h" #include "lib_snr.h" 35 #include "lib_pwf.h" #include "lib_bit.h" #include "lib_pit.h" #include "lib_smo.h" #include "lib_prc.h" 40 #include "lib_flt.h"

```
#include "lib fcs.h"
  #include "lib_ltp.h"
  #include "lib_ppr.h"
  #include "lib_ppp.h"
5 #include "lib_gcb.h"
  #ifdef DIAG SMV
10 #include "lib_dia.h"
  #endif
15 /*----*/
   /* FUNCTION : enc_smv_frame ().
   /* PURPOSE : Performs processing on one frame.
   /* ALGORITHM:
25 /* INPUT ARGUMENTS:
          _(FLOAT64 []) sigpp : input frame.
          _(INT16 ) smv_mode : selected SMV mode.
          (INT16 ) switch_flag: switching mode flag.
          (char ) signaling : signal information.
30 /*----
   /* OUTPUT ARGUMENTS:
          _(INT16 []) PackedWords: output frame bitstream.
          _(FLOAT64 []) enc_sigout : output processed frame.
          _(INT16 *) FlatSpFlag : flat input speech flag.
          _(FLOAT64 *) avg_rate : average bit rate.
35 /*
          _(INT16 *) VoicingClass: Frame classification.
                                                     */
   /* INPUT/OUTPUT ARGUMENTS:
                                         */
            _ None.
```

	100	*/
•	TURN ARGUMENTS : None */	,
/* /*	_ None. */ 	*/
,		·
5 void c	enc_smv_frame (FLOAT64 sigpp [], INT16 PackedWor	rds [], FLOAT64 enc sigout [],
5 Void C	•	6 switch_flag, INT16 *FlatSpFlag,
		FLOAT64 *avg_rate, INT16 *VoicingClass)
	{	
	/*	*/
10	/* Misc. parameters	*/
	/+	*/
	·	
	FLOAT64 *px, *buf,	
	FLOAT64 gl, g2, vall, val2;	
15		
	INT16 i_sf, i, j, k, l, i_s, l_sf, n_sf;	
	FLOAT64 x;	
	fDIAG_SMV	
20	FLOATEA	
#endi	FLOAT64 y;	
#Clidi		
	/*	*******/
25	/* Variables for Voice Activity Detection	*/
	/*	*/
	INT16 avg_lag;	
30	/*	*/
	/* Variables for classification	*/
	/*	
	INT16 NoisyV_flag, OnSetFlag;	
35	FLOAT64 Francis a:	•
	FLOAT64 lpcgain_q;	
	/*	*/
	/* Variables for LPC smoothing	*/
40	/*	+/

	FLOA 164 beta_irm, beta_sub;	
	FLOAT64 refl0[N_SF_MAX];	
_	/*	* /
5		*/
	/* Parameters for LPC smoothing	•
	/ *	•
	FLOAT64 LP_CF[N_SF_MAX+1];	
0	FLOAT64 lsf_int [NP];	
	/*	. */
	·	*/
	/* Variables for Smoothing	•
_	/*	
15	INT16 exc_mode, speech_mode;	
	FLOAT64 pitch_corr[2];	
	/*	*/
20	/* Parameters for LPC analysis and quantiza	
.0	/*	
	FLOAT64 lsf_new2[NP], lsf_new3[NP], lsfq_new[NP];	•
-		
25	/*	+/
	/* Variables for Pitch Interpolation	*/
	/*	+/
	•	
	FLOAT64 ForPitch[L_FRM+L_FRM/2];	
30		
	/+	*/
	/* Parameters for pitch pre-processing	*/
	/+	*/
	,	
35	FLOAT64 gp_pp[N_SF_MAX];	
	2.2 2	
	/*	*/
	/* Parameters for pitch open loop	*/
	/*	*/
40		

	IN 1 to pre_tag;	
	/*	*/
	/* Parameters for LTP */	
5	/*	*/
	FLOAT64 Rp;	
	/*	*/
10	/* Parameters for Closed Loop processing	*,
	/*	*/
	FLOAT64 gainQ [N_SF_MAX];	
	FLOAT64 hh[L_SF], hh_w[L_SF], hh_m[L_FRM];	
15	FLOAT64 Tg[L_SF], Tgs[L_SF], Tgs_m[L_SF];	
	FLOAT64 rcs2[L_SF],	
	FLOAT64 nsr = 0.0 ;	
20	FLOAT64 Rp_Wn, ac_sharp, frm_sharp;	
	FLOAT64 qua_Tg [L_FRM];	
	/*	*/
25	, underes ion or British or annual o	*/
	/*	*/
	FLOAT64 sigsyn[L_SF+NP];	
	FLOAT64 qua_sigsyn[L_SF+NP];	
30		
	/*	*/
	/* Parameters for bitstream processing	*/
	/*	* /
35		
	INT64 seed_enc;	
	INT16 parm[3];	
	INT16 shft_count;	
40	INT16 PackWdsPtr[2];	

103

PARAMETER channel;

	/*=====================================
	$/*$ $_{ m constraint and an $
5	
	/* noncommonomental encoder approximation */
	$/*$ \sim
	/ * ====================================
10	
	/ * ====================================
	/* прирадинивинивинивинивинивини LPC ANALYSIS инпринивинивинивинивинивинивинивинивинивини
	/*====================================
15	
	/**/
	/* Second subframe LPC */
	/**/
20	
	LPC_analysis(L_LPC, sigpp, lpc_window, rxx, bwe_factor,
	refi[1], &pderr, pdcf[1], lsf_new2, NP, &x,
	(*FlatSpFlag));
	/**/
25	·
	/* First subframe LPC */
	/*
	$f_{om}(i=0)$ $i < NID$, $i \neq i$
30	for $(i = 0; i < NP; i++)$ reft[0][i] = 0.5*(reft[1][i] + reft[N_SF4-1][i]);
30	teti[0][i] = 0.5 (teti[1][i] + teti[ii_5] + 1][i]);
	for $(i = 0; i < NP; i++)$
	lsf_int [i] = 0.5*(lsf_new[i] + lsf_new2[i]);
	131_III [1] 0.5 (131_1104/[1] 131_1104/2[1]),
35	LPC lsftop(lsf_int, pdcf[0], NP);
	en ="
	/**/
	/* Fourth subframe LPC analysis, used for LSF quantization */
	/**/

```
104
            LPC\_analysis(L\_LPC, sigpp+L\_PP-L\_LPC, lpc\_windowl, rxx, bwe\_factor,
                                     refi[N SF4-1], &pdcrr, pdcf[N_SF4-1], lsf_new, NP,
                                             &lpcgain, (*FlatSpFlag));
5
                      VAD based on the fourth subframe LPC LPC
            flag_vad_mem [1] = flag_vad_mem [0];
            flag_vad_mcm [0] = Vad;
10
            px = sigpp + L PP-L_FRM-L_LPCLHD;
            VAD_voice_detection (px, refl[N_SF4-1], pderr, lag_buf, pgain_buf,
15
                                                     lsf_new, rxx, frm_count, &Vad, flag_vad_mcm);
                            Third subframe LPC
20
           for (i = 0; i < NP; i++)
                    refl[2][i] = 0.5*(refl[1][i] + refl[N_SF4-1][i]);
            for (i = 0; i < NP; i++)
25
                    lsf_int[i] = 0.5*(lsf_new[i]+lsf_new2[i]);
            LPC_lsftop(lsf_int, pdcf[2], NP);
30
                            LPC analysis for Look-Ahead
           energy_m = energy;
35
             LPC_analysis (L_LPC, sigpp+L_PP-L_LPC, lpc_window2, rxx, bwe_factor,
                                     refl[N_SF4], &pderr, pdcf[N_SF4], lsf_new3, NP, &x,
                                                      (*FlatSpFlag));
             energy = rxx[0];
40
```

	#/
rees	=======================================
ifndef	f MEM_TEST
	/** /* */ */ */
	/* Update the weighted speech buffer */ /**/
	• • • • • • • • • • • • • • • • • • •
	cpy_dvcctor(wspecch+L_FRM, wspeech, 0, L_OLPIT-L_FRM-L_LPCLHD-1);
endif	
,	/**/ Generate the residual */
,	/* Generate the residual */
	PWF_speech_to_residu (sigpp, wspeech, pdcf, wpdcf_zero, GAMMA1);
	/* Temporal poise-like unvoied speech detector */
	/* Temporal noise-like unvoied speech detector */
	,
	VUV = 3;
	I = L_FRM + L_LPCLHD;
	The second secon
	CLA_NoisUnvoiceDetect (wspeech+ L_OLPIT-I, sigpp+L_PP-I, &VUV, ResEng, &frm_sharp, L_FRM);
	FrmResEng = ResEng[0] + ResEng[1];
	/* * /
	/* Generate the weighted speech */
	/**/
	PWF residu to wspeech((*FlatSpFlag), wspeech, wspeech, refl, pdcf,
	T 11 Tenant to makeepuil(T match: 100), makeepu, makeepu, ten, beer,

```
106
             /* population of the part of t
                                                                pre_lag = ol_lag[1];
                                                                PIT_ol_2pitchs(wspeech, L_OLPIT, ol_lag, Rp_sub, frame_class, HI_LAG2);
  5
                                                                  for (i = 0; i < 3; i++)
                                                                                                            lag_buf[i] = lag_buf[i+2];
10 #ifndef COMPLEXITY_REDUC
                                                                   if( abs((short) (ol_lag[0]/2.0) - avg_lag)<=2)
                                                          lag_buf[3] = (INT16) (ol_lag[0]/2.0);
15
                                                                      else
                                                                                                                  {
                                                                                                                   if (abs((INT16) (ol_lag[0]/3.0) - avg_lag) \le 2)
                                                                                                                                                             lag_buf[3] = (INT16) (ol_lag[0]/3.0);
                                                                                                                     else
  20
                                                                                                                                                               lag buf[3] = ol_lag[0];
                                                                                                                    ì
                                                                          if (abs((INT16) (ol_lag[1]/2.0) - avg_lag)<=2)
                                                                                                                     lag_buf[4] = (short) (ol_lag[1]/2.0);
     25
                                                                           else
                                                                                                                        if( abs((INT16) (ol_lag[1]/3.0) - avg_lag) \leq 2)
                                                                                                                                                                   lag_buf[4] = (short) (ol_lag[1]/3.0);
                                                                                                                         elsc
        30
                                                                                                                                                                     lag_buf[4] = ol_lag[1];
                                                                                                                        }
                               #else
                                                                               lag_buf[3] = ol_lag[0];
                                                                               lag_buf[4] = ol_lag[1];
          35
                               #endif
             40 /* producione de la companie de l
```

```
107
        frame class m = frame_class;
        frame_class = CLA_signal_classificr(sigpp + L_PP-L_FRM - L_LPCLHD,
                                 Rp_sub[1], (double)(ol_lag[1]), Vad);
5
                    Background Noise Level
10
        SNR_Calc_NSR_enc (sigpp+L_PP-L_FRM, lsf_new3, Vad, frame_class,
                                &nsr, L_FRM);
                     Class correction
15
        CLA_Class_Correct(&frame_class, &frame_class_pp, &Vad, &VUV,
                                 &OnSctFlag, frame_class_m, Rp_sub, nsr, refl,
                                 frm_sharp, smv_mode, energy_m, energy,
20
                                      FrmResEng, lpcgain, &NoisyV_flag);
Fixed rate selection
30
        fix_rate_mem = fix_rate;
        if (smv_mode <= 1)
35
               fix_rate = RATE8_5K;
        else
               fix_rate = RATE4_0K;
         if (switch flag)
               {
40
```

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```
108
                                                                  CLA_Rate_Select (smv_mode, frame_class, frame_class_m,
                                                                                                                                                                                                        OnSetFlag, Rp_sub, nsr, frm_sharp,
                                                                                                                                                                                                          refl[2][0], FrmResEng, &fix_rate);
                                                                  if ((signaling == (char)1) && (fix_rate == RATE8_5K))
  5
                                                                                                                      fix_rate = RATE4_0K;
10 /* DEDUCACION DEDUCACIONED FINE PITCH AND INTERPOLATION DEDUCACIONED DE DESCRIPCION DE PROPERTIE PITCH AND INTERPOLATION DE PROPERTIE PITCH AND PROPERTIE PITCH AND PROPERTIE PITCH PITC
                                        if ((fix_rate == RATE4_0K) && (ol_lag[1] > HI_LAG) && (frame_class_pp>0))
15
                                                                  frame_class_pp = 0;
                                                                                            Forward fine pitch
 20
                                          if (frame_class_pp > 0)
                                                                      low_pit = MAX(min_pit, ol_lag[1] - 5);
                                                                      if (fix rate == RATE8_5K)
                                                                                               high_pit = MIN(HI_LAG2, ol_lag[1] + 5);
  25
                                                                      clse
                                                                                               high pit = MIN(HI\_LAG, ol\_lag[1] + 5);
                                                                      pre_pitch_index = pitch_index;
   30
                                                                       pitch_f_mem [1] = pitch_f_mem [0];
                                                                       pitch_f_mem [0] = pitchf;
                                                                        I = L_OLPIT-L_LPCLHD;
   35
                                                                        lagl = PIT FinePitch (fix_rate, low_pit, high_pit, wspeech,
                                                                                                                                                                                                              I, Delay_pp, &Rp, &pitchf, &pitch_index);
                                                                        channel.idx_pitch[0] = pitch_index;
```

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```
109
                                                                        /* Pitch interpolation and modify pitch index to avoid too */
                                                                                                                                  large delay
. 5
                                                                        if ((fix_rate_mem == RATE2_0K) || (fix_rate_mem == RATE0_8K))
                                                                                                    {
                                                                                                      pitch_f_mem [1] = pitchf;
                                                                                                      pitch_f_mem [0] = pitchf;
10
                                                                                                    }
                                                                        clsc if (frame_class_m < 6)
                                                                                                       if (fix_rate_mem != RATE8_5K)
15
                                                                                                                                    pitch_f_mem [1] = lag_f[N_SF2-1];
                                                                                                                                    pitch f mem [0] = lag_{N_SF2-1};
                                                                                                                                   }
20
                                                                                                        else
                                                                                                                                     pitch f mem [1] = lag_f[N_SF4-1];
                                                                                                                                   pitch f_mem [0] = lag_f[N_SF4-1];
 25 .
                                                                                                      }
                                                                          PIT_PitchInterpolat (pitchf, pitch_f_mem, ForPitch, 1);
                                                                        }
               /* production of the processing of the processin
                                              cpy_dvector (NewTg+L_FRM, NewTg, 0, NP-1);
 35
                                              PPP_pitch_preproc (smv_mode, fix_rate, (*FlatSpFlag), nsr, wspeech,
                                                                                                                                                                                                NewTg+NP, ForPitch, &Dclay_pp, frame_class_pp,
                                                                                                                                                                                                                              &frame_class, &VUV, gp_pp);
   40
```

```
110
                             frame_class correction
           if ((VUV == 2) && (framc_class > 1) && (OnSetFlag != 1))
                   frame_class=1;
5
           if ((frame_class == 6) && ((fix_rate_mem == RATE2_0K) \parallel
                    (fix_rate_mem == RATE0_8K) || (fix_rate == RATE2_0K) ||
                            (fix_ratc == RATE0_8K))
                                    frame_class = 5;
10
            if ((framc_class == 6) && (frame_class_m < 3) && (switch_flag != 0))
                    frame_class = 5;
15
                     Rate modification because of the complexity
             if (switch_flag)
20
                     if ((smv_mode >= 1) && (fix_rate == RATE8_5K) &&
                                      (framc_class == 1))
                                              fix_rate = RATE4_0K;
                     if ((frame_class_pp > 0) && (frame_class < 6) &&
                              (fix_rate == RATE4_0K) \&\&
 25
                                      ((ol_lag[0] < 30) || (ol_lag[1] < 30)))
                                              fix_rate = RATE8_5K;
 30 #ifdef PROG_BUG_FIX
                      if ((signaling == (char)1) && fix_rate == RATE8_5K)
                               fix_rate = RATE4_0K;
                               if (frame_class == 6)
 35
                                       frame_class = 5;
                                if (frame_class_pp > 0)
                                        if (ol_lag[0] < 30)
  40
```

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```
111
                                                ol_lag[0] *=2;
                                       if (ol_lag[1] < 30)
                                                ol_lag[1] *=2;
 5
                                       }
                              }
   #else
                     if ((signaling == (char)1) && (fix_rate == RATE8_5K))
10
                              fix_rate = RATE4_0K;
                              if (frame_class == 6)
                                       framc_class = 5;
15
                              if ((frame_class_pp > 0) &&
                                       ((ol_lag[0] < 30) || (ol_lag[1] < 30)))
                                               ol_lag[0] *= 2;
                              }
   #endif
20
                     PRC_average_rate (fix_ratc, avg_rate);
                     }
             channel.fix_rate = fix_rate;
25
                                                                  */
                             Reset open-loop pitch
             if (fix_rate == RATE8_5K)
30
                     {
                      for (i = 0; i < 2; i++)
                              lag[i] = ol_lag[0];
                      for (i = 2; i < N_SF4; i++)
35
                              lag[i] = ol_lag[1];
                     }
             else
                      lag[0] = ol_lag[0];
40
```

```
112
                    lag[1] = ol_lag[1];
                    }
            if (frame_class_pp > 0)
5
                     if (fix_rate == RATE8_5K)
                             lag[N_SF4-1] = lagl;
                     else if (lagl >= 30)
                             lag[N_SF2-1] = lagl;
10
                    }
                       Quantize 3 stable LTP gains with 4bits
15
             if ((frame_class == 6) && (fix_rate != RATE2_0K) &&
                     (fix_rate != RATE0_8K))
                     channel.idx_SVS_deci = 1;
                     if (NoisyV_flag == 1)
20
                             for (i = 0; i < N_SF4; i++)
                                      gp_p[i] *= 0.95;
                     clse if (NoisyV_flag == 2)
                             for (i = 0; i < N_SF4; i++)
                                      gp_p[i] = 0.92;
25
                     if ((*FlatSpFlag) == 0)
                              if (smv_mode == 0)
                                       for (i = 0; i < N_SF4; i++)
30
                                              gp_p[i] = 0.95;
                                      }
                               else
                                       for (i = 0; i < N_SF4; i++)
35
                                               gp_p[i] = 0.97;
                                       }
                              }
                      if (fix_rate != RATE8_5K)
 40
```

```
113
                                           */
                       4 bits to quantize 3 gp
5
                 GEQ_Quant_PitchGain_3D(gp_pp, &(channel.idx_Gp_VQ));
                 }
            else
10
                        6 bits to quantize 4 gp
                 GEQ_Quant_PitchGain_4D(gp_pp, &(channel.idx_Gp_VQ));
                 }
15
       else
            channel.idx_SVS_deci = 0;
20
               Back to speech domain
     PWF_wspecch_to_speech(NewTg+NP, wpdcf_zero, wpdcf_pole, LP_CF, &Z1_ws_2);
25
    30
         Estimate frame based and initial subframe based smoothing */
        SMO_lsf_smooth_est (Vad, flag_vad_mem [0], refl[1][0], lsf_new,
35
                                      &beta_frm);
            Smooth LSF, smoothing not active for coder_mode >= 3
40
```

```
for (i = 0; i < NP; i++)
                    lsf smooth[i] = beta_frm*lsf_smooth[i] +
                                                             (1.0-beta_frm)*lsf_new[i];
5
                             LSF quantization
            LSF_Q_lsfqnt(lsf_smooth, lsfq_new, channel.idx_lsf, fix_rate);
10
            /* Interpolation of LSF and weighting filter coefficients extraction */
15
            if ((*FlatSpFlag) == 0)
                     if (NoisyV_flag > 0)
                             x = 0.55;
                     else
20
                             x = 0.6;
                     }
             else
                     x = 0.5;
25
             r_pole_ws = 0.25*r_pole_ws + 0.75*x;
             if ((fix rate == RATE8_5K) \parallel (fix_rate == RATE2_0K) \parallel
 30
                     (fix_rate == RATE0_8K))
                                        */
                     /* Interpolate the QUANTIZED Isf and get the LPC coefficients */
 35
                     if ((fix_rate == RATE2_0K) ||
                              ((fix_rate == RATE8_5K) && (frame_class != 6)))
                                      LPC_adptive_interp(lsfq_ncw, lsf_ncw2, lsfq_old, pdcfq,
 40
```

```
115
                                                                        &(channel.idx_lpc_int));
                    else
                             LPC_adptivc_interp_dec(lsfq_new, lsfq_old, pdcfq, 0);
5
                     for (i = 0; i < N_SF4; i++)
                             refl0[i] = refl[i][0];
10
                     /* Perceptual weighting filter coefficients calculation */
                     for (i = 0; i < N_SF4; i++)
15
                             val1 = 1.0;
                              for (j = 0; j < NP; j++)
20
                          vall *= r_pole_ws;
                                      wpdcf_pole[i][j] = pdcf[i][j]*vall;
                             }
                     }
25
             else
                     if (frame_class==6)
30
                              /* Interpolate the QUANTIZED Isf and get the LPC */
                                            coefficients
                              LPC_interpolate_lpc_4to3 (lsfq_new, (FLOAT64 *)NULL,
35
                                                                                                  lsfq_old, pdcfq,
    1);
```

Interpolate the UNQUANTIZED Isf and get the LPC */

```
116
                                                                      */
                                            coefficients
                             LPC_interpolate_lpc_4to3 (lsf_new, lsf_new2, lsf_old,
                                                                                                    pdcf, 0);
5
                              x = 6.5 / 40;
                              refl0[0] = (1-x)*refl[0][0] + x*refl[1][0];
10
                              refl0[1] = 0.5*(refl[1][0] + refl[2][0]);
                               ref[0] = (1-x) ref[3][0] + x ref[2][0];
                               /* Perceptual weighting filter coefficients calculation */
15
                                for (i = 0; i < N_SF3; i++)
                                        {
                                         vall = 1.0;
 20
                                         val2 = 1.0;
                                         for (j = 0; j < NP; j++)
                                                  vall *= GAMMA1;
  25
                                                  val2 *= r_pole_ws;
                                                  wpdcf_zero[i][j] = pdcf[i][j]*val1;
                                                  wpdcf\_pole[i][j] = pdcf[i][j]*val2;
                                          }
   30
                                 }
                         else
   35
                                       Interpolate the QUANTIZED Isf and get the LPC */
                                                   coefficients
                                   LPC_interpolate_lpc_4to2 (lsfq_new, (FLOAT64 *)NULL,
    40
```

```
lsfq_old, pdcfq, 1);
                            /*____*/
                                 Interpolate the UNQUANTIZED Isf and get the LPC */
                                                                  */
                                           coefficients
5
                            LPC interpolate_lpc_4to2 (lsf_new, lsf_new2, lsf_old,
                                                                                     pdcf, 0);
10
                             ref[0][0] = 0.5*(ref[0][0] + ref[1][0]);
                             refl0[1] = 0.5*(refl[2][0] + refl[3][0]);
15
                             /* Perceptual weighting filter coefficients calculation */
                             for (i = 0; i < N_SF2; i++)
20
                                     val1 = 1.0;
                                     val2 = 1.0;
                                     for (j = 0; j < NP; j++)
25
                                             vall *= GAMMA1;
                                             val2 *= r_pole_ws;
                                             wpdcf_zero[i][j] = pdcf[i][j]*val1;
                                             wpdcf_polc[i][j] = pdcf[i][j]*val2;
30
                                             }
                                     }
                             }
                    }
35
                           Flat input speech identifier
             CLA_Identify_Input (channel.idx_SVS_deci, pdcfq[0], lsfq_new[0],
40
```

118 pgain_past, &lpcgain_q, fix_rate, FlatSpFlag);

```
if (fix_rate == RATE2_0K)
                    {
                     parm[0] = channel.idx_lsf[0];
5
                     parm[1] = channel.idx_lsf[1];
                     parm[2] = channel.idx_lpc_int;
                     secd_enc = 0x000000000;
                      shft_count = 15;
                      for (j = 0; j < 3; j++)
10
                               shft_count -= bitno0[j];
                               secd_enc = seed_enc ^(((long) parm[j] ) << shft_count);</pre>
15
              clsc if (fix_rate == RATE0_8K)
                       parm[0] = channel.idx_lsf[0];
                       parm[1] = channel.idx_lsf[1];
 20
                       parm[2] = channel.idx_lsf[2];
                       sced_enc = 0x000000000;
                        shft_count = 11;
                        for (j = 0; j < 3; j++)
  25
                                 shft_count -= bitno1[j];
                                 seed_enc = seed_enc ^(((long) parm[j] ) << shft_count);</pre>
                                 }
                        }
  30
                          Gaussian excitation for very low bit rates
                 if (nsr > 0.125)
   35
                         x = 1.0 - 0.6*nsr;
                 else
                         x = 1.0;
                 if ((fix_rate == RATE0_8K) && (nsr > 0.125))
    40
```

```
119
```

```
x = 0.4;
           if (frame_class == 6)
                   x = 1.0;
            NoiseGainFactor = 0.5*NoiseGainFactor + 0.5*x;
5
            if ((fix\_ratc == RATE2\_0K) \parallel (fix\_rate == RATE0\_8K))
                    GCB_gauss_excit (&seed_enc, fix_rate, fix_rate_mem, nsr, ResEng,
                                                    qua_unfcod[1], qua_gainQ[1]);
10
                    qua_gainQ[1][0] *= NoiseGainFactor;
                    if (fix_rate == RATE2_0K)
15
                            /* Quantize 'qua_gainQ[1][0]' and 'qua_gainQ[1][2]' */
                                               with 6 + 6 bits
                            if ((fix_rate_mem == RATE8_5K) \parallel (fix_rate_mem == RATE0_8K))
20
                                    GEQ_updatc_mem_4d_to_2d();
                            else if ((fix_rate_mem == RATE4_0K) && (frame_class_m == 6))
                                    GEQ update_mem_3d_to_2d();
                             qua gainQ[1][2] *= NoiseGainFactor;
25
                             GEQ_gainNSQMA_1_6 (0, qua_unfcod[1], qua_gainQ,
                                                                     &(channel.idx_gainVQ[0]));
                             GEQ_gainNSQMA_1_6 (2, qua_unfcod[1], qua_gainQ,
                                                                     &(channel.idx_gainVQ[1]));
30
                             qua_gainQ[1][1] = qua_gainQ[1][0];
                             qua gainQ[1][3] = qua_gainQ[1][2];
                     else
35
                                    Quantize 'qua_gainQ[1][0]' 5 bits
 40
```

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```
120
                 if ((fix_rate_mem == RATE4_0K) || (fix_rate_mem == RATE2_0K))
                       if (frame_class_m != 6)
                            GEQ_update_mcm_2d_to_4d();
                       else
                            GEQ_update_mcm_3d_to_4d();
5
                       }
                  GEQ_gainNSQMA_1_5(qua_unfcod[1], qua_gainQ,
                                                  &(channel.idx_gainVQ[0]));
10
                   for (i = 1; i < N_SF4; i++)
                        qua_gainQ[1][i] = qua_gainQ[1][0];
                   }
          }
15
        (*VoicingClass) = frame_class;
 20 #ifdef MEM_TEST
               Update the weighted speech buffer
         cpy_dvector (wspeech+L_FRM, wspeech, 0, L_OLPIT-L_FRM-L_LPCLHD-1);
  25
    #endif
  30
     /* nanaaanaanaaanaaanaaa SUB-FRAME PROCESSING nanaaanaanaaanaaanaanaa */
     ----*/
                                       */
    40 /*
```

```
121
   /*
          pdcfq
                   - quantized prediction coefficients
          wpdcf_pole - poles of the perceptual weighting filter
          wpdcf_zero - zeros of the perceptual weighting filter
                                                   */
                                                        */
                  - target signal
5 /*
          Tgs
           Tg
                  - target signal for codebook search
                   (no pitch contribution)
                  - impulse response of the combined synthesis and
           hh
                   perceptual weighting filter
           unfcod[0] - adaptive codebook (ACB) vector
10 /*
           unfcod[1] - algebraic codebook (CDBK) vector
           fcod[0] - filtered ACB vector
           fcod[1] - filtered CDBK vector
   /*
           gainQ[0] - ACB gain
   /*
           gainQ[1] - CDBK gain
15 /*
           gainQ[2] - CDBK gain
   /*
                                                   */
                  - adaptive codebook
           ext
           lag[i_sf] - integral lag of the current subframe
   /*
           lagf[i_sf] - fractional lag of the current subframe
20 /*
if (fix rate != RATE4_0K)
                   n_sf = N_SF4;
30
            clse
                    if (framc_class == 6)
                           n_sf = N_SF3;
                    clse
35
                           n sf = N SF2;
                    }
            i_s = 0;
            for (i_sf = 0; i_sf < n_sf; i_sf++)
40
```

```
122
                               Set-up of the sub-frame parameters
5
                    if (fix_rate != RATE4_0K)
                            1_sf = L_sF4;
                     else
                              if (frame_class == 6)
10
                                      {
                                      if (i_sf == N_SF3-1)
                                              l_sf = L_SF3;
                                       else
                                              l_sf = L_SF0;
15
                                       }
                               clsc
                                       l_sf = L_SF;
                               }
 20
                       /* The impulse response with the all pole synthesis filter */
                                and the percerptual weighting filter*
                        LPC_ImpulseResponse (hh, wpdcf_zero [i_sf], wpdcf_pole[i_sf],
  25
                                                                           pdcfq [i_sf], l_sf);
                        cpy_dvector(hh, hh_m+i_s, 0, l_sf-1);
   30
                                         Target signal
                          PRC_TargetSignal (wpdcf_zero[i_sf], wpdcf_pole[i_sf],
                                                                    pdcfq[i_sf], NewTg+i_s, ext+MAX_LAG, Tgs,
    35
                                                                            dif_mem, target_mem, l_sf);
                           if (i_sf == 0)
                                  cpy_dvcctor (Tgs, Tgs_m, 0, 1_sf-1);
     40
```

```
Ideal excitation
5
                   PRC_Ideal_Excit (Tgs, pdcfq[i_sf], wpdcf_pole[i_sf],
                                                         wpdcf_zero[i_sf], res2, l_sf);
10
                                                            */
                                LTP Contribution
                   if ((fix_rate == RATE8_5K) || (fix_rate == RATE4_0K))
15
                           if (frame_class == 6)
                                  /*-----*/
                                          Pitch lag interpolation
20
                                  LTP PP pitch ext (ForPitch+i_s, Tgs, ext, hh, i_sf,
                                                                        unfcod[0], fcod[0], lag, lag_f,
                                                                         &Rp_Wn, l_sf);
25
                                              Pitch gain
                                  gainQ[0] = gp_pp[i_sf];
30
                                  if (NoisyV_flag < 2)
                                          gainQ[0] *= 0.25*Rp_Wn + 0.75;
                                  }
                           elsc
35
                                              Close-loop search
                                   if (fix_rate == RATE8_5K)
40
```

```
124
                                              LTP_close_8_5k_pitch (Tgs, ext, hh, i_sf, unfcod[0],
                                               fcod[0], lag, lag_f, &(gainQ[0]), &Rp_Wn, l_sf,
                                                framc_class_pp, channel.idx_pitch);
                                      else
                                              LTP_close_7b_pitch (Tgs, ext, hh, i_sf, unfcod[0],
5
                                                                         fcod[0], lag, lag_f, &(gainQ[0]),
                                                                                 &Rp_Wn, l_sf, frame_class_pp,
                                                                                           channel.idx_pitch);
10
                                          Modify the gainQ[0] to de-emphasize the pitch */
                                                contribution in the cdbk search
                                       gp_p[i_sf] = gainQ[0];
15
                                       ac_sharp = PPP_sharpness(l_sf, unfcod[0]);
                                       if ((framc_class > 1) && (ac_sharp < 0.25))
20
                                                x = MAX(MIN(5*ac_sharp-0.25, 1.0), 0.0);
                                                gainQ[0] *= (1 - x)*Rp_Wn + x;
25
                                       Save the results for the second loop
                               cpy_dvector (unfcod[0], qua_unfcod[0]+i_s, 0, 1_sf-1);
                               cpy_dvector (fcod[0], qua_fcod[0]+i_s, 0, l_sf-1);
 30
                               }
                      else
                                gainQ[0] = 0.0;
                                gp_p[i_sf] = 0.0;
 35
                                Rp_Wn = 0.0;
                                lag_f[i_sf] = lag[i_sf];
                                ini_dvector(unfcod[0], 0, 1_sf-1, 0.0);
                                ini_dvector(qua_unfcod[0]+i_s, 0, 1_sf-1, 0.0);
                                ini dvector(fcod[0], 0, 1_sf-1, 0.0);
 40
```

```
125
                            ini dvector(qua_fcod[0], 0, 1_sf-1, 0.0);
                    /*-----
                         Classify the speech subframe, used for excitation */
 5
                                   presclection
                    SMO_initial_analysis (NewTg+i_s+NP, Vad, lag_f[i_sf],
                                                                     refl0[i_sf], &speech_mode,
10
                                                                                      pitch_corr, l_sf);
                          Calculate the Target for codebook search
15
                    for (i = 0; i < l_sf; i++)
                            Tg[i] = Tgs[i] - gainQ[0]*fcod[0][i];
                            res2[i] = gainQ[0]*unfcod[0][i];
20
                            }
                               Mode selection of fixed excitation:
                                                                     */
                                mixed search vs pure Gaussian
25
                     SMO_refined_analysis (res2, speech_mode, pitch_corr, &exc_mode,
                                                                      &beta_sub, l_sf);
30
                                  Fixed Codebook scarch
                     k = cxc_mode*frame_class;
35
                     if (NoisyV_flag == 1)
                            k = 2;
                     if (NoisyV_flag == 2)
                            k = 1;
40
```

```
126
                     switch (fix_rate)
                             case RATE8_5K:
                                              if (framc_class == 6)
5
                                                       FCS_cdbk_search_30b_sub40 (ext, pdcfq[i_sf],
                                                                       Tg, res2, hh, unfcod[1], l_sf, i_sf,
                                                                                lag[i_sf], k, gp_pp[i_sf],
                                                                                        nsr, Rp_Wn, &channel);
                                                      }
10
                                              clse
                                                       FCS_cdbk_scarch_22b_sub40 (ext, pdcfq[i_sf],
                                                                       Tg, res2, hh, unfcod[1], l_sf, i_sf,
                                                                                lag[i_sf], k, pgain_past,
15
                                                               nsr, Rp_Wn, &channel);
                                                       }
                                     break;
                              case RATE4_0K:
20
                                              if (frame_class == 6)
                                                       {
                                                       FCS_cdbk_scarch_13b_sub54 (ext, pdcfq[i_sf],
                                                                        Tg, res2, hh, unfcod[1],
25
                                                                                 l_sf, i_sf, lag[i_sf], k,
                                                                                         gp_pp[i_sf], nsr, Rp_Wn,
                                                                                                fix_rate_mem,
                                                                                                   lpcgain_q,
30 &channel);
                                                       }
                                               else
                                                        {
    #ifdef MEM_TEST
                                                        ini_dvector(&sigsyn[NP], 0, L_SF-1, -1.0);
 35
                                                        ini\_dvector(\&qua\_sigsyn[NP], 0, L\_SF-1, -1.0);
                                                        ini dvector(dif_mem, 0, L_SF+NP-1, -1.0);
                                                        ini_dvector(qua_dif_mem, 0, L_SF+NP-1, -1.0);
                                                         px = &wspeech[L_OLPIT-L_FRM-L_LPCLHD];
 40
```

```
127
                                                       ini_dvector(px, 0, L_FRM+L_LPCLHD-1, -1.0);
                                                       ini_dvcctor(hh_w, 0, L_SF-1, -1.0);
                                                       ini_dvector(ForPitch, 0, L_FRM+L_FRM/2-1, -1.0);
 5
                                                       px = \&qua\_unfcod[0][0];
                                                       ini_dvector(px, i_s, L_FRM-1, -1.0);
                                                       px = &qua\_unfcod[1][0];
                                                       ini_dvector(px, i_s, L_FRM-1, -1.0);
                                                       px = &qua_fcod[0][0];
10
                                                       ini_dvector(px, i_s, L_FRM-1, -1.0);
                                                       px = &qua_fcod[1][0];
                                                       ini_dvector(px, i_s, L_FRM-1, -1.0);
15
                                                       ini_dvector(qua_ext, 0, MAX_LAG+L_SF-1, -1.0);
                                                       ini_dvector(qua_Tg, 0, L_SF-1, -1.0);
                                                       ini_dvector(rxx, 0, NP, -1.0);
20
                                                       ini_dvector(refl0, 0, N_SF_MAX-1, -1.0);
                                                       for (i=0;i<N_SF_MAX+1;i++)
                                                               {
25
                                                               px = &pdcf[i][0];
                                                                ini_dvector(px, 0, NP-1, -1.0);
                                                               }
                                                       ini_dvector(lsfq_new, 0, NP-1, -1.0);
                                                       ini_dvector(lsf_new2, 0, NP-1, -1.0);
30
                                                       ini_dvector(lsf_new3, 0, NP-1, -1.0);
                                                       ini_dvector(lsf_int, 0, NP-1, -1.0);
                                                       ini_dvector(sigpp, i_s, L_FRM-1, -1.0);
35
   #endif
                                                       FCS_cdbk_scarch_15b_sub80 (ext, pdcfq[i_sf],
                                                                        Tg, res2, hh, unfcod[1], I_sf, i_sf,
40
```

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```
fix_rate_mem,
                                                                                                    lpcgain_q,
5 &channel);
                                                        }
                                       break;
                               case RATE2_0K: case RATE0_8K:
                                                cpy\_dvector(qua\_unfcod[1]+i\_s,\,unfcod[1],\,0,\,l\_sf-1);
10
                                                gainQ[1] = qua_gainQ[1][i_sf];
                                       break;
                               default: nrerror ("Invalid fix_rate !!\n");
                                       break;
15
                               }
                                Filter the selected excitation vector
20
                       buf = dvector (0, 1_sf-1);
                       filter AZ \ (hh, unfcod[1], fcod[1], buf, (INT16)(l\_sf-1), l\_sf);
 25
                        free_dvcctor (buf, 0, 1_sf-1);
  30
                                            Gains
                         if ((fix_rate == RATE8_5K) \parallel (fix_rate == RATE4_0K))
   35
                                               Unquantized gains
                                   GainModiDeci = 1;
   40
```

```
129
                             if ((frame_class < 6) && (frame_class > 1))
                                     GEQ_gain_reopt_2 (fcod[0], fcod[1], Tgs, gainQ,
                                                                                gainQ+1, l_sf);
                             elsc
5
                                      gainQ[0] = gp_pp[i_sf];
                                      for(i = 0; i < l_sf; i++)
                                              Tg[i] = Tgs[i] - gainQ[0]*fcod[0][i];
                                      dot_dvector (Tg, fcod[1], &gainQ[1], 0, l_sf-1);
10
                                      dot dvcctor (fcod[1], fcod[1], &val1, 0, 1_sf-1);
                                      gainQ[1] /= MAX(vall, 0.01);
                                      }
15
                                           Gain normalization
20
                              if (frame_class < 6)
                                      {
                                       if (GainNormDeci == 0)
                                              beta_sub = 0.0;
25
                                       GainNormDeci = 0;
                                       if ((nsr > 0.125) || (frame_class <= 1) ||
                                                       ((fix_rate == RATE4_0K) && (Rp_Wn < 0.5)))
                                                        GainNormDeci = 1;
30
                                                        if (fix_rate == RATE8_5K)
                                                                x = ResEng[(short)(i_sf/2)];
                                                        else
                                                                x = ResEng[i_sf];
35
                                                        PRC_GainsNorm_Gc_Gp (nsr, x, ext+MAX_LAG, gainQ,
                                                                                 unfcod, fcod, Tgs, Vad*exc_mode,
                                                                                         beta_sub, l_sf);
                                                        }
 40
```

```
gainQ[0] *= NoiseGainFactor,
                                     gainQ[1] *= NoiscGainFactor;
                                    }
                             elsc
5
                                     if (nsr > 0.25)
                                            PRC_GainNorm_Gc (ext+MAX_LAG, gainQ, unfcod,
           fcod, Tg, 1_sf);
10 #ifdef PROG_BUG_FIX
                                GainNormDeci = 0;
    #endif
                                    •}
15
                                           Gain Quantization
20
                                            Limit pitch gain
                              if (gainQ[0] < 0.0)
 25
                                      gainQ[0] = 0.0;
                               if (gainQ[0] > 1.2)
                                      gainQ[0] = 1.2;
 30
                               for (i = 0; i < LTP\_BUFF\_SIZE-1; i++)
                                      pgain_buf[i] = pgain_buf[i+1];
                               pgain_buf[LTP_BUFF_SIZE-1] = gainQ[0];
  35
                               if (gainQ[1] < 0.0)
                                       FCS_ChangeSign (&channel, i_sf, l_sf, unfcod[1], fcod[1],
                                                                        gainQ+1);
  40
```

```
#ifdef MEM_TEST
                             cpy_dvcctor (unfcod[0], qua_unfcod[0]+i_s, 0, l_sf-1);
                             cpy_dvector (fcod[0], qua_fcod[0]+i_s, 0, l_sf-1);
 5 #endif
                             cpy_dvector (unfcod[1], qua_unfcod[1]+i_s, 0, l_sf-1);
                             cpy_dvector (fcod[1], qua_fcod[1]+i_s, 0, l_sf-1);
10
                             if (GainModiDeci == 0)
                                     cpy dvector (Tgs, Tg, 0, 1_sf-1);
                             else
                                     wad dvector (fcod[0], gainQ[0], fcod[1], gainQ[1],
                                                                      Tg, 0, 1_sf-1);
15
                             cpy dvector (Tg, &qua_Tg[i_s], 0, l_sf-1);
20
                             if (frame_class != 6)
                                      if (fix_rate != RATE8_5K)
                                              if (((fix_rate_mem == RATE8_5K) ||
25
                                                      (fix_rate_mcm == RATE0_8K)) && (i_sf == 0))
                                                              GEQ_update_mem_4d_to_2d();
                                              clsc if ((fix_rate_mem == RATE4_0K) &&
                                                              (framc_class_m == 6) && (i_sf == 0))
                                                                       GEQ_update_mcm_3d_to_2d();
30
                                              GEQ_gainVQMA_2 (Tg, unfcod, fcod, gainQ,
                                                                       &(channel.idx_gainVQ[i_sf]),
                                                                               l_sf, fix_ratc);
                                              }
35
                                      clse
                                              if (((fix_ratc_mem == RATE4_0K) ||
                                                      (fix_rate_mem == RATE2_0K)) \&\& (i_sf == 0))
40
```

```
132
                                                  if (frame_class_m != 6)
                                                         GEQ_update_mem_2d_to_4d();
                                                  else
                                                         GEQ_update_mem_3d_to_4d();
                                                  }
5
                                          GEQ_gainVQMA_2 (Tg, unfcod, fcod, gainQ,
                                                                          &(channel.idx_gainVQ[i_sf]),
                                                                                 1 sf, fix_rate);
                                          }
10
                                  }
                           /* Update the past quantized pitch gain for cdbk search */
15
                           pgain_past = gainQ[0];
                           if (pgain_past < PAST_PGAIN_MIN)
                                          pgain_past = PAST_PGAIN_MIN;
                           if (pgain_past > PAST_PGAIN_MAX)
20
                                          pgain_past = PAST_PGAIN_MAX;
                           qua_gainQ[0][i_sf] = gainQ[0];
                           qua_gainQ[1][i_sf] = gainQ[1];
25
                           }
                   else
                                   fix_rate==RATE0_8K */
30
                            GainNormDeci = 0;
                            gainQ[0] = 0.0;
                            pgain_past = gainQ[0];
                            qua_gainQ[0][i_sf]=0;
35
                            gainQ[1]=qua_gainQ[1](i_sf];
                            cpy_dvector(fcod[1], qua_fcod[1]+i_s, 0, l_sf-1);
                           }
 40
```

```
Build the excitation and synthesized signal
               wad_dvector (unfcod[0], gainQ[0], unfcod[1], gainQ[1],
                                              ext+MAX_LAG, 0, 1_sf-1);
5
                           Update memory
10
               FLT_allsyn (ext+MAX_LAG, l_sf, pdcfq[i_sf], NP, sigsyn+NP,
                     synth_mem);
               for (i = 1 \text{ sf-NP}, j=0; i < l_sf; i++,j++)
15
                      dif_mem[j] = NewTg[i_s+NP+i] - sigsyn[i+NP];
                      target_mem[j] = Tgs[i] - gainQ[0]*fcod[0][i]
                                                - gainQ[1]*fcod[1][i];
                     }
20
                        Update the adaptive codebook
25
                cpy_dvector (ext+l_sf, ext, 0, MAX_LAG-1);
                i_s += l_sf;
30
/*concommonomed Second Loop With Quantized Fixed CB Gains monomonomed*/
   if ((frame_class == 6) && (fix_rate != RATE2_0K) && (fix_rate != RATE0_8K))
40
```

```
134
                           Quantize 3 fixed CB gains
                 if (fix_rate != RATE8_5K)
5
                         {
                         if(\ ((fix\_rate\_mem == RATE8\_5K) \ \|\ (fix\_rate\_mem == RATE0\_8K)))
                                       GEQ_update_mem_4d_to_3d();
                         else if (frame_class_m != 6)
                                       GEQ_update_mem_2d_to_3d();
10
                         GEQ_gainVQMA_3(qua_Tg, qua_unfcod, qua_fcod, qua_gainQ,
                                                       &(channel.idx_Gc_VQ));
                         }
15
                  else
                          if( ((fix_ratc_mem == RATE4_0K) || (fix_rate_mem == RATE2_0K)))
                                {
                                 if (frame_class_m != 6)
20
                                        GEQ_update_mcm_2d_to_4d();
                                 else
                                        GEQ_update_mem_3d_to_4d();
                                 }
25
                          GEQ_gainVQMA_4(qua_Tg, qua_unfcod, qua_fcod, qua_gainQ,
                                                              &(channel.idx_Gc_VQ));
                          }
 30
                   i_s = 0;
                   for (i_sf = 0; i_sf < n_sf; i_sf++)
                           35
                           if (fix_rate == RATE8_5K)
                                 l_sf = L_SF4;
                           else
                                  if (i_sf == N_SF3-1)
 40
```

```
135
                                               l sf = L_SF3;
                                      clsc
                                               l_sf = L_SF0;
                                      }
 5
                                              Target signal
                              if (i_sf > 0)
10
                                      {
                                      PRC_TargetSignal (wpdcf_zero[i_sf], wpdcf_pole[i_sf],
                                                                pdcfq[i_sf], NewTg+i_s, ext+MAX_LAG, Tgs,
                                                                         qua_dif_mem, qua_target_mem, l_sf);
                                      }
15
                              elsc
                                      cpy_dvector (Tgs_m, Tgs, 0, 1_sf-1);
                                         Re-build adaptive CB excitation
20
                              if (i_sf > 0)
                                    LTP PP pitch ext_decod (ForPitch+i_s, qua_ext, i_sf,
25
                                                                          qua_unfcod[0]+i_s, lag, lag_f, l_sf);
                                       buf = dvector(0, 1_sf-1);
30
                                       filter AZ \ (hh\_m+i\_s, qua\_unfcod[0]+i\_s, qua\_fcod[0]+i\_s,
                                                                                                   buſ,
    (INT16)(l_sf-1), l_sf);
35
                                       free_dvector (buf, 0, 1_sf-1);
40
```

```
Build the excitation and synthesized signal
5
                             wad_dvcctor (qua_unfcod[0]+i_s, qua_gainQ[0][i_sf],
                                                              qua_unfcod[1]+i_s, qua_gainQ[1][i_sf],
                                                                       qua_cxt+MAX_LAG, 0, 1_sf-1);
10
                                            Update memory
                             FLT allsyn (qua_ext+MAX_LAG, I_sf, pdcfq[i_sf], NP,
                                                               qua_sigsyn+NP, qua_synth_mem);
15
                              gl = qua\_gainQ[0][i\_sf];
                              g2 = qua_gainQ[1][i_sf];
20
                              for (i = l\_sf-NP, j = 0; i < l\_sf; i++, j++)
                                      qua_dif_mem[j] = NewTg[i_s+NP+i] - qua_sigsyn[i+NP];
                                      qua_target_mem[j] = Tgs[i] - gl * qua_fcod[0][i+i_s]
25
                                              - g2 * qua_fcod[1][i+i_s];
                                      }
                                        Update the adaptive codebook
30
                              cpy_dvector (qua_ext+l_sf, qua_ext, 0, MAX_LAG-1);
                              i_s += l_sf;
35
40
```

```
137
                                                                           Modify the memorys for next frame
                                                                                                                                synth_mem,
                                                                                                                                                                         0, NP-1);
                                                 cpy dvector (qua_synth_mem,
                                                                                                                                                                                            0, NP-1);
                                                                                                                                                     dif mem,
                                                  cpy_dvector (qua_dif_mem,
  5
                                                                                                                                target_mem, 0, NP-1);
                                                  cpy_dvector (qua_target_mem,
                                                                                                                                                                                             0, MAX_LAG-1);
                                                  cpy_dvector (qua_ext,
10
                              else
                                                                            Modify the memorys for next frame
15
                                                  cpy dvector (synth_mem, qua_synth_mem, 0, NP-1);
                                                                                                                                qua_dif_mem,
                                                                                                                                                                     0, NP-1);
                                                  cpy dvector (dif_mem,
                                                                                                                                qua_target_mcm, 0, NP-1);
                                                  cpy dvector (target_mem,
                                                                                                                                                                                                                 0, MAX_LAG-1);
                                                                                                                                                     qua_cxt,
                                                   cpy_dvector (ext,
20
                                                  }
                                                              Index to bitstream convertion
25
                               BIT_cdbk_index_to_bits(&channel, PackedWords, PackWdsPtr);
 30
         #ifdef DIAG_SMV
          35 /* ממממממממממממממממממממממממ
                                                                                                                             */
          DECODER
                                                                                                                             /* production and a contraction and a contrac
 40
```

```
138
            i_s = 0;
            for (i_sf = 0; i_sf < n_sf; i_sf++)
                     if (fix_rate != RATE4_0K)
                              l_sf = L_SF4;
5
                      elsc
                               if (frame_class == 6)
                                        if (i_sf == N_SF3-1)
10
                                                l_sf = L_SF3;
                                        else
                                                l_sf = L_SF0;
                                        }
                               else
15
                                        l_sf = L_SF;
                               }
20
                       cpy\_dvector\ (qua\_unfcod[0]+i\_s,\,unfcod[0],\,0,\,l\_sf-I);
                       cpy\_dvector\ (qua\_unfcod[1]+i\_s,\,unfcod[1],\,0,\,l\_sf-1);
                       wad_dvcctor (unfcod[0], qua_gainQ[0][i_sf], unfcod[1],
                                                           \label{eq:qua_gainQ[1][i_sf], qua_sigsyn+NP, 0, i_sf-1);} \\
 25
                       FLT_allsyn (qua_sigsyn+NP, l_sf, pdcfq[i_sf], NP,
                                                           qua_sigsyn+NP, synth_mem_dec);
 30
                                        Post processing
                        switch (fix_rate)
  35
                                 case RATE8_5K: if ((*FlatSpFlag) == 0)
                                                                              x = 0.65;
                                                                              y = 0.4;
```

```
139
                                                                      }
                                                              else
                                                                       x = 0.63;
                                                                       y = 0.4;
                                     break;
                                                     x = 0.63;
                             case RATE4_0K:
                                                              y = 0.5;
10
                                     break;
                             case RATE2_0K: x = 0.63;
                                                              y = 0.0;
                                     break;
15
                             case RATE0_8K:
                                                  x = 0.63;
                                                              y = 0.0;
                                     break;
20
                             default: nrerror("Invalid fix_rate !!");
                                     break;
                             }
                     PPR_post_process(qua_sigsyn, pdcfq[i_sf], x, y, lag_f[i_sf],
                                                                       l_sf, (*FlatSpFlag), fix_rate);
25
                               Update the synthesized speech buffer
30
                     cpy_dvector (qua_sigsyn+NP, enc_sigout+i_s, 0, l_sf-1),
35
    #endif
40
```

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PCT/US00/25182

#ifdef	DIAG	SMV

	DIA_trace_data ((FLOAT64)Vad, L_FRM, 10000.0, fdia_vad);
5 #end	lif
	/**/
10	return;
10	/**/ }
/ * =:	
	*/ */

	141
/*=	
	Conexam System nic.
/*	4311 Jamboree Road */
	Newport Beach, CA 92660 */
/*-	*/
	Copyright(C) 2000 Conexant System Inc. */
/ *.	*/
/*	ALL RIGHTS RESERVED: */
/+	No part of this software may be reproduced in any form or by any */
	means or used to make any derivative work (such as transformation */
	or adaptation) without the authorisation of Conexant System Inc. */
/*	=======================================
	PROTOTYPE : encoder.h */
	======================================
/-	
	*/
•	
	*/
/*	*/
VC	oid enc_smv_frame (FLOAT64 sigpp [], INT16 PackedWords [], FLOAT64 enc_sigout [],
	INT16 smv_mode, INT16 switch_flag, INT16 *FlatSpFlag,
	char signaling, FLOAT64 *av_rat, INT16 *VoicingClass);
/*	
/+	**_*/

=====*,
•

	/*	*/	
	/* Decoder	*/	•
	/*	*/	
5	#endif		
	/*====================================		
	/*	=== FRAME COUNTERS ======	*/
10	/*=====================================		
	extern INT64 frm_count;		
	extern INT64 frm_crasure;		•
15	/*=====================================	======================================	*
	/*=====================================	==== SMV CONTROL =======	=======================================
	/*=====================================		
	extern INT16 smv_mode;		
20	extern INT16 fix_rate, fix_rate_mem;		
	/*=====================================	w=====================================	====================================
	/*=====================================	== NOISE SUPRESSION ======	*/
	/*=====================================		
25			
	extern FLOAT64 MIN_GAIN;		
	/*====================================		
30) /*====================================	PRE AND POST PROCESSING =====	=======================================
	/*=====================================		:========*/
	TO ATT CLASSIC SALES		
	extern FLOAT64 *pre_fit_num; extern FLOAT64 *pre_fit_den;		
35	5 extern FLOAT64 *pre_fit_buf_z;		
	extern FLOAT64 *pre_flt_buf_p;		
		•	•
	extern FLOAT64 pre_gain;		
	TO ATTCAME OF		
40	0 extern FLOAT64 *lp_flt_num;		

extern FLOAT64 *lp_flt_buf;
/ * */
5 extern INT16 *zeroed;
extern FLOAT64 *zero_rate;
extern FLOAT64 *low_rate;
extern FLOAT64 *high_rate;
10 extern FLOAT64 *low_neg;
extern FLOAT64 *low_pos;
extern INT32 min_delta;
15 extern FLOAT64 zero_level;
extern FLOAT6412_neg, 11_neg, 11_pos, 12_pos;
/**/
20 extern FLOAT64 tc_mem_dec;
extern FLOAT64 tc_coeff;
extern FLOAT64 tc_gain;
extern FLOAT64 *tc_buff_exc;
25 /**/
extern FLOAT64 *buff_LTpost;
extern FLOAT64 *PF_mem_syn;
extern FLOAT64 r_zcro, r_pole;
30 extern FLOAT64 pst_scale;
extern FLOAT64 pst_hp_mem;
/ * *
35 extern FLOAT64 *pst_flt_num;
extern FLOAT64 *pst_flt_den;
extern FLOAT64 *pst_flt_buf_z,
extern FLOAT64 *pst_fit_buf_p;
40, extern FLOAT64 pst gain;

•	*****************************			
/*======	======================================	NALYSIS ======		w=======
/*======				
	2			•
extern FLO.	AT64 *siglpc;		•	
		*/		
	' LPC analysis window	*/		
	Li C analysis william	•		
extern FLO	AT64 *lpc_window;			•
extern FLO	AT64 *lpc_window1;			
extern FLO	AT64 *lpc_window2;			
/*				
/*	Tilt analysis window	*/		
/*		······································		
= 0	A TOTAL COLUMN			
extern FLO	OAT64 *tilt_window;			
/*		*/		
-	Bandwidth expansion factor	*/		• •
		+/		
,				
extern FLO	AT64 *bwc_factor;			
•				
/*	LPC parameters	*/		
/*				
autam El C	OAT64 pderr;			
)AT64 *rxx, **refl, **pdcf, **pdcfq, **pdc	efa dec:		
	OAT64 *Isf_new, *Isf_old, *Isf_mid;			
J				
/*		*/		
extern FLC	OAT64 erg, Ipcgain;			

	ktern FLOA 164 sub_ipcg;
/*	**/
5 e:	xtern FLOAT64 *IntLSF_C;
/-	*=====================================
/-	*=====================================
· /	*=====±===============================
.0	
e	extern FLOAT64 r_polc_ws;
e	extern FLOAT64 **wpdcf_zero, **wpdcf_pole;
e	extern FLOAT64 *wspeech, *wspeech_mem;
6	extern FLOAT64 Z1_ws_1, Z1_ws_2;
15 6	extern FLOAT64 *ModiSig_m, *tmp_ws_m;
	/*====================================
1	/*====================================
1	/*====================================
	/*====================================
20	extern FLOAT64 **fcod, **unfcod;
	extern FLOAT64 **qua_fcod, **qua_unfcod;
	extern FLOA164 Figura_1cod, qua_umeou,
	/*=====================================
25	/*====================================
23	/*=====================================
	/*=====================================
30	/*====================================
	/*=====================================
	extern INT16 GainNormDeci, GainModiDeci;
	TI OATKA nanin parti
35	extern FLOAT64 pgain_past;
	extern FLOAT64 pgain_past_dcc;
	extern FLOAT64 **qua_gainQ;
40	extern FLOAT64 *past_energyq_2d;

```
extern FLOAT64 *past_energyq_3d;
  extern FLOAT64 *past_energyq_4d;
  extern FLOAT64 *gp_buf;
  extern FLOAT64 past_fixed_energy;
  extern FLOAT64 *Prev_Bcta_Pitch;
10
  extern FLOAT64 *energy_pred_coeff_1;
  extern FLOAT64 *energy_pred_coeff_2;
  extern FLOAT64 *cncrgy_pred_coeff_3;
15 extern FLOAT64 *energy_pred_coeff4d_l;
  cxtern FLOAT64 *cnergy_pred_coeff4d_2;
  extern FLOAT64 *cnergy_pred_coeff4d_3;
  extern FLOAT64 *energy_pred_coeff4d_4;
20 extern FLOAT64 *pred_energy_d38;
   extern FLOAT64 *prcd_energy_d410;
   extern FLOAT64 gp_mean;
   extern FLOAT64 gp_fec;
25
   extern FLOAT64 **gain_cb_2_128;
   extern FLOAT64 **gain_cb_2_128_8_5;
   extern FLOAT64 gp3_tab[TAB_SIZE_GVQ_3D][GVQ_VEC_SIZE_3D];
30 cxtem FLOAT64 gp4_tab[TAB_SIZE_GVQ_4D][GVQ_VEC_SIZE_4D];
   extern FLOAT64 gainVQ_2_128[MSMAX_2_128][GVQ_VEC_SIZE_2D];
   extern FLOAT64 gainVQ_2_128_8_5[MSMAX_2_128][GVQ_VEC_SIZE_2D];
   extern FLOAT64 gainVQ_3_256[MSMAX_3_256][GVQ_VEC_SIZE_3D];
   extern FLOAT64 gainVQ_4_1024[MSMAX_4_1024][GVQ_VEC_SIZE_4D];
35 extern FLOAT64 gainSQ_1_32[MSMAX_1_32];
   extern FLOAT64 gainSQ_1_64[MSMAX_1_64];
```

```
extern FLOAT64 *** lsf_cb_08k;
 extern FLOAT64 ***isf_cb_40k;
 extern FLOAT64 ***isf_cb_85k;
  extern INT16 _*stage_cand_08k;
  extern INT16 *stage_cand_40k;
  extern INT16 *stage_cand_85k;
10
  extern FLOAT64 B_08k[LP_08k][LQMA_08k][MAXLNp];
  extern FLOAT64 B_40k[LP_40k][LQMA_40k][MAXLNp];
  extern FLOAT64 B_85k[LP_85k][LQMA_85k][MAXLNp];
15 cxtcm FLOAT64 CBes_08k[MAXLTT_08k][LMSMAX_08k][MAXLNp];
  extern FLOAT64 CBes_40k[MAXLTT_40k][LMSMAX_40k][MAXLNp];
  extern FLOAT64 CBcs_85k[MAXLTT_85k][LMSMAX_85k][MAXLNp];
  extern INT16 *MS_08k;
20 extern INT16 *MS_40k;
  extern INT16 *MS_85k;
   extern FLOAT64 *last_qlsf;
   extern FLOAT64 *lsfq_old, *lsfq_old_dec;
25 extern FLOAT64 *Mean;
   extern FLOAT64 **qes;
   extern FLOAT64 **qes_dec;
30
   extern FLOAT64 *lsfq_mcm_dec;
   extern FLOAT64 *lsfq_mem_enc;
 extern INT16 *ol_lag;
 40 extern INT16 *lag;
```

```
extern INT16 lagl, low_pit, high_pit;
 extern FLOAT64 *Rp_sub;
 extern FLOAT64 pitchf;
10
  extern FLOAT64 *PitLagTab5b;
  extern FLOAT64 *PitLagTab7b;
  extern FLOAT64 *PitLagTab8b;
15 extern FLOAT64 *SincWindows;
           VUVmm, pitch_m, pitch_mm, Iopt0;
  extern INT16
20
           *PITmax0;
  extern INT16
           LagCount, VadCount, Av_lag;
  extern INT16
  extern FLOAT64 Rp_m;
25 extern FLOAT64 *Rmax0;
           extern FLOAT64 *pitch_f_mem;
30
  35 extern FLOAT64 *cxt;
  extern FLOAT64 *SincWindows_E;
  extern FLOAT64 *NewTg;
  extern FLOAT64 *lag_f;
  extern INT16 pitch_index, pre_pitch_index;
40
```

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	150		
,	*=====================================		
,	/*================================		
5	extern FLOAT64 *SincWindows_PP;		
•	extern FLOAT64 Delay_pp, Last_Rp;		
	extern INT16 frame_class_pp_m;		
	extern FLOAT64 *targ_mem,		
10			
		,	
	/*=====================================		
	/*====================================	,	
	/*=====================================		
15			
	extern FLOAT64 cl_Eg, ol_Eg;		
	extern FLOAT64 m_lpc_gain;		
		,	
	/*=======*	,	
20	/*====================================	1	
	/*=====================================	•	
	extern INT64 seed_exc;		
	extern FLOAT64 Z1_gcb;		
25	extern FLOAT64 ref_eng_gcb;		
23	extern INT16 rate_mem;		
	EXICITIVITO Idio_moni,		
	extern INT64 seed_dec;		
	CALCIN INVIOLOGICAL CONTRACTOR CO		
30) / *====================================	*/	
30	/*====================================	/	
	/*====================================	*/	
	extern INT16 VUV, VUVm;		
34	5 extern INT16 frame_class, frame_class_m, frame_class_pp;		
<i>J</i> =			
	extern FLOAT64 energy, energy_m;		
	extern INT16 frame_onset;		
4	0 extern INT16 frame_pit_tab, min_pit, max_pit;		

```
extern INT16 *frame_class_mem, *onstplsv_mem, *voiced_mem;
  extern FLOAT64 *window1;
5 extern FLOAT64 *buffer_cla;
  extern FLOAT64 *Lp_buffer;
  extern FLOAT64 *buffer_refl0;
  extern FLOAT64 *buffer_max_cla;
  extern FLOAT64 *buffer_wtilt, *buffer_wmax, *buffer_wRp;
10 extern FLOAT64 E_noi, T_noi, M_noi, R_noi;
  extern FLOAT64 **P_w, MA_avg_wRp, MA_min_wtilt;
  extern INT16 SVS_Count, Vad_0_Count, FlatSp_Flag;
  extern FLOAT64 Av_value;
15
  extern FLOAT64 Rp_m_cla, lpcg_m_cla;
20 /*-----*/
  extern INT16 OnSct;
  extern INT16 frame_class_mm;
  INT16 SVS_deci_mem;
  extern INT16 *lag_buf;
  extern FLOAT64 *pgain_buf;
35
  extern INT16 Vad, *flag_vad_mem;
  extern INT16 flag;
  extern INT16 count_sil;
40 extern INT16 count_ext;
```

```
extern INT16 count_noise;
 extern INT16 dec3_flg_mem;
 extern INT16 onset_fig, count_onset;
5 extern FLOAT64 pitch_gain_mean;
 extern FLOAT64 **vad_lsf_mem;
 extern FLOAT64 min_energy;
 extern FLOAT64 mean_energy;
 extern FLOAT64 *prev_cml_lsf_diff;
10 extern FLOAT64 *prev_energy;
 extern FLOAT64 snr,
 extern FLOAT64 mcan_max;
 extern FLOAT64 mean_s_energy;
 extern FLOAT64 prev_min_encrgy;
15 extern FLOAT64 *min_energy_mem;
  extern FLOAT64 next_min_energy;
  extern FLOAT64 *mcan_lsf;
  extern FLOAT64 *norm_mean_lsf;
  extern FLOAT64 cml_lsf_diff_filt;
20 extern FLOAT64 onset_trhsd;
 25
  extern FLOAT64 NoiseGainFactor;
  extern INT16 n_nois_ext_enc;
30 extern FLOAT64 NoisEng_enc, eng_old_enc, diff_lsf_m_enc, diff_eng_m_enc;
  extern INT16 snr_frm_count, snr_count_vad;
  extern FLOAT64 eng_m_enc;
extern FLOAT64 *lsf_smooth;
40
```

```
153
                  N_mode_sub_est, N_mode_frm;
  extern INT16
   extern FLOAT64 *Isf_old_smo, *ma_lsf, *dSP_buf;
                  *N_sub;
  extern INT16
                 conscc_low, consec_high, conscc_vad_0;
5 extern INT16
                 updates_noise, updates_speech, calls, lev_reset;
   extern INT16
   extern FLOAT64 ma_max_noise, ma_max_speech;
   extern FLOAT64 *buffer_smo, *buffer_p, ma_cp;
   extern FLOAT64 *buffer_sum_smo, *buffer_max_smo;
10
                 N_mode_sub_ref, consec;
   extern INT16
   extern INT16
                 updates;
   extern FLOAT64 ma_max;
                     extern FLOAT64 lpcg_m, alpha;
20 extern INT16 lag_m, SVS_flag_m;
   extern FLOAT64 *wsp_m;
   extern FLOAT64 *hh_hf;
25
   extern FLOAT64 **PHI;
   extern INT16
                  *MaxIdx;
   extern INT16
                  **track;
30
   extern INT16 **p_track_2_5_0, **p_track_2_7_1;
   extern INT16 ***p_track_3_2_80, ***p_track_3_2_54;
   extern INT16 **p_track_5_4_0, **p_track_5_3_1, **p_track_5_3_2, **p_track_8_4_0;
35 extern FLOAT64 **unfcod_dcc;
                                               */
                        13 bits
```

154 extern FLOAT64 Stab_13b_enc; extern FLOAT64 Stab_13b_dec; 15 bits seed_bfi_exc; extern INT64 extern FLOAT64 Stab_15b_enc; 10 extern FLOAT64 Stab_15b_dec; 15 N_bfi, bfh_oh; extern INT16 past bfi, ppast_bfi; extern INT16 bfi_caution, nbfi_count; extern INT16 extern FLOAT64 *enrg_buff; 20 extern FLOAT64 *ext_dcc_mcm; . 25 extern FLOAT64 *synth_mcm, *synth_mcm_dcc, *dif_mem, *target_mem; extern FLOAT64 *qua_synth_mem, *qua_dif_mem, *qua_target_mem, *qua_ext; extern FLOAT64 *sigsyn_dec, *ext_dec; extern INT16 *bitno0, *bitno1; 35

----*/

	<u>.</u>	155				=====*/
/*===:	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,					,
/*====	=======================================	======			====*/	
/* Con	exant System Inc.	*/				
/ * 431	l Jamborec Road	*/				
/* New	vport Beach, CA 92660	*/				
/*		*/				
/* Cop	yright(C) 2000 Conexant System Inc.		*/			
/*		*/			•	
/* ALI	RIGHTS RESERVED:		*/			
/* No	part of this software may be reproduced in	any form o	r by any */			
/* mea	uns or used to make any derivative work (su	ich as trans	sformation */			
/* or a	daptation) without the authorisation of Cor	nexant Syst	em Inc. */			
/*===	=======================================		.=====================================		====*/	
	E: gbl_var.h	*/				
/*===		======	:==== = =====		=====*/	
#ifdef	DIAG_SMV					
)						
/ * ===	, ====================================	z======			=	=====*/
/ * ===	====== DIAGNO	STICS FII	LE POINTERS =	=======	=======	====*/
/ * ===		2235552				=====*/
		* /				
/*	Encoder unquantized	*/				
/*		+/				
FILE	*fdia_vad;			•		
)						
/*		*/	•			
/*	Encoder quantized	*/				
/*		*/	•			
5 FILE	*fdia_sp_enc;					
	,					
/*		+/	•		•	
/*	Floating point parameter dump	*	/			
-	-	·+/	,			
10						

156 Decoder 5 #endif INT64 frm count = 0; = 0;15 INT64 frm_crasure 20 INT16 smv_mode; INT16 fix_rate, fix_rate_mem; FLOAT64 $MIN_GAIN = -13.0$;

FLOAT64 *pre_fit_num;

35 FLOAT64 *pre_fit_den;
FLOAT64 *pre_fit_buf_z;
FLOAT64 *pre_fit_buf_p;
FLOAT64 pre_gain;

40 FLOAT64 *lp_flt_num;

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	FLOAT64 *lp_flt_buf;
	/**/
5	INT16 *zeroed;
	FLOAT64 *zero_rate;
	FLOAT64 *low_rate;
	FLOAT64 *high_rate;
10	FLOAT64 *low_ncg;
	FLOAT64 *low_pos;
	INT32 min_dclta;
15	FLOAT64 zero_level;
	FLOAT64 12_neg, 11_neg, 11_pos, 12_pos;
	/**/
20	FLOAT64 tc_mem_dec;
	FLOAT64 tc_coeff;
	FLOAT64 tc_gain;
	FLOAT64 *tc_buff_exc;
25	/**/
	FLOAT64 *buff_LTpost;
	FLOAT64 *PF_mem_syn;
	FLOAT64 r_zero, r_pole;
30	FLOAT64 pst_scale;
	FLOAT64 pst_hp_mem;
	/**/
35	FLOAT64 *pst_flt_num;
	FLOAT64 *pst_flt_den;
	FLOAT64 *pst_flt_buf_z;
	FLOAT64 *pst_flt_buf_p;
40	FLOAT64 pst gain;

	======================================	
/*=:	======================================	
/ + =: 5	=======================================	
	OAT64 *siglpc;	
120	5.110 ·	
/*		*/
/*	LPC analysis window	*/
		+/
FL	OAT64 *lpc_window;	
FL	OAT64 *lpc_windowl;	
FL	OAT64 *lpc_window2;	
5		
/*-		
/*	Tilt analysis window	*/
/*-		·
	O (TC) tells usindam	
U FL	.OAT64 *tilt_window;	
/*		*/
/*- /*	Bandwidth expansion factor	
		*/
.5		
	_OAT64 *bwe_factor;	
	-,	
/*.		*/
/*		*/
30 /*		*/
	LOAT64 pderr;	
	LOAT64 *rxx, **refl, **pdcfq, **pdcfq, **pdcfq_dcc;	•
F	LOAT64 *Isf_new, *Isf_old, *Isf_mid;	
35		*/
/*	*	
F	LOAT64 erg, lpcgain;	

40 FLOAT64 sub_lpcg;

	/**/
5	FLOAT64 *IntLSF_C;
-	/*=====================================
	/*====================================
	/*=====================================
10	FLOAT64 r_pole_ws;
	FLOAT64 **wpdcf_zero, **wpdcf_pole;
	FLOAT64 *wspeech, *wspeech_mcm;
	FLOAT64 Z1_ws_1, Z1_ws_2;
	FLOAT64 *ModiSig_m, *tmp_ws_m;
15	
	/*=====================================
	/*====================================
	/*=====================================
20	FLOAT64 **fcod, **unfcod;
	FLOAT64 **qua_fcod, **qua_unfcod;
	/*=====================================
	/*====================================
25	/*=====================================
	/*====================================
	/*====================================
30	/*====================================
	INT16 GainNormDeci, GainModiDeci;
	FLOAT64 pgain_past;
35	FLOAT64 pgain_past_dec;
	FLOAT64 **qua_gainQ;
	FLOAT64 **gain_cb_2_128;
40	FLOAT64 **gain_cb_2_128_8_5;

```
FLOAT64 *past_energyq_4d;
  FLOAT64 *past_energyq_3d;
5 FLOAT64 *past_energyq_2d;
  FLOAT64 *gp_buf;
  FLOAT64 past_fixed_energy;
10
   FLOAT64 *Prev_Beta_Pitch;
   FLOAT64 *energy_pred_coeff_l;
15 FLOAT64 *energy_pred_coeff_2;
   FLOAT64 *cnergy_pred_coeff_3;
   FLOAT64 *energy_pred_coeff4d_1;
   FLOAT64 *energy_pred_coeff4d_2;
20 FLOAT64 *energy_pred_coeff4d_3;
   FLOAT64 *energy_pred_cocff4d_4;
   FLOAT64 *pred_energy_d38;
   FLOAT64 *pred_energy_d410;
25 FLOAT64 gp_mcan;
   FLOAT64 gp_fec;
    #include "gain_vq.tab"
 30
    35
    FLOAT64 *** isf_cb_08k;
    FLOAT64 ***lsf_cb_40k;
    FLOAT64 ***isf_cb_85k;
 40 INT16 *stage_cand_08k;
```

```
INT16 *stage_cand_40k;
 INT16 *stage_cand_85k;
5 #include "lsf_vq.tab"
 INT16 *MS_08k;
 INT16 *MS_40k;
10 INT16 *MS_85k;
 FLOAT64 *last_qlsf;
 FLOAT64 *isfq_old, *isfq_old_dec;
 FLOAT64 *Mean;
15
 FLOAT64 **qes;
 FLOAT64 **qes_dec;
20 FLOAT64 *lsfq_mcm_dcc;
 FLOAT64 *isfq_mem_enc;
  INT16 *ol_lag;
  INT16 *lag;
30 INT16 lagl, low_pit, high_pit;
  FLOAT64 *Rp_sub;
  FLOAT64
          pitchf;
35
40 FLOAT64 *PitLagTab5b;
```

	FLOAT64 *PitLagTab7b;
	FLOAT64 *PitLagTab8b;
	FLOAT64 *SincWindows;
	FEOA 104 Sinc Windows,
5	/+*/
	/ *
	INT16 VUVmm, pitch_m, pitch_mm, lopt0;
0	INT16 *PITmax0;
	INT16 LagCount, VadCount, Av_lag;
	FLOAT64 Rp_m;
	FLOAT64 *Rmax0;
15	
	/**/
	FLOAT64 *pitch_f_mem;
	• /
20	/*====================================
	/*
	/*====================================
	FLOAT64 *cxt;
2:	FLOAT64 *SincWindows_E;
	FLOAT64 *NewTg;
	FLOAT64 *lag_f; .
	INT16 pitch_index, pre_pitch_index;
3	0 /**/
	/*====================================
	/*====================================
	FLOAT64 *SincWindows_PP;
3	5
	FLOAT64 Delay_pp, Last_Rp;
	INT16 framc_class_pp_m;
	FLOAT64 *targ_mem;
	·
	40 /*====================================

```
163
       /*_________________________________//*
 FLOAT64 cl_Eg, ol_Eg;
5 FLOAT64 m_lpc_gain;
 /+______/
10
 INT64 sccd_exc;
 FLOAT64 Z1_gcb;
 FLOAT64 ref_eng_gcb;
 INT16 rate mem;
15
 INT64 secd_dcc;
 INT16 VUV, VUVm;
  INT16 frame_class, frame_class_m, frame_class_pp;
25 FLOAT64 energy, energy_m;
  INT16 frame_onset;
  INT16 frame_pit_tab, min_pit, max_pit;
30 INT16 *frame_class_mem, *onstplsv_mem, *voiced_mem;
  FLOAT64 *windowl;
  FLOAT64 *buffer_cla;
  FLOAT64 *Lp_buffer;
  FLOAT64 *buffer_ref10;
35 FLOAT64 *buffer_max_cla;
  FLOAT64 *buffer_wtilt, *buffer_wmax, *buffer_wRp;
  FLOAT64 E_noi, T_noi, M_noi, R_noi;
  FLOAT64 **P_w, MA_avg_wRp, MA_min_wtilt;
40 INT16 SVS_Count, Vad_0_Count, FlatSp_Flag;
```

164 FLOAT64 Av_value; 5 FLOAT64 Rp_m_cla, lpcg_m_cla; FLOAT64 av_pit_lag, av_pit_g; 10 INT16 PP_model_m, PP_frm_count; //INT16 SVS_deci_m; 15 INT16 OnSet; 20 INT16 framc_class_mm; INT16 SVS_dcci_mcm; 25 INT16 *lag_buf; FLOAT64 *pgain_buf; 35 INT16 Vad, *flag_vad_mem; INT16 flag; INT16 count_sil; 40 INT16 count_ext;

```
    INT16 count_noise;

  INT16 dec3_flg_mem;
  INT16 onset flg, count_onset;
5 FLOAT64 pitch_gain_mean;
  FLOAT64 **vad_lsf_mem;
  FLOAT64 min_cnergy;
  FLOAT64 mean_energy;
  FLOAT64 *prev_cml_lsf_diff;
10 FLOAT64 *prev_energy;
  FLOAT64 snr;
FLOAT64 mean_max;
  FLOAT64 mean_s_energy;
  FLOAT64 prev_min_energy;
15 FLOAT64 *min_energy_mem;
  FLOAT64 next_min_energy;
  FLOAT64 *mean_lsf;
  FLOAT64 *norm_mean_lsf;
  FLOAT64 cml_lsf_diff_filt;
20 FLOAT64 onset_trlisd;
  FLOAT64 NoiseGainFactor;
  INT16 n_nois_ext_enc;
30 FLOAT64 NoisEng_enc, eng_old_enc, diff_lsf_m_enc, diff_eng_m_enc;
  INT16 snr_frm_count, snr_count_vad;
  FLOAT64 eng_m_enc;
  FLOAT64 *lsf_smooth;
40
```

```
INT16 N_mode_sub_est, N_mode_frm;
  FLOAT64 *lsf_old_smo, *ma_lsf, *dSP_buf;
  INT16 *N_sub;
5 INT16 consec_low, consec_high, consec_vad_0;
  INT16 updates_noise, updates_speech, calls, lev_reset;
  FLOAT64 ma_max_noise, ma_max_speech;
  FLOAT64 *buffer_smo, *buffer_p, ma_cp;
  FLOAT64 *buffer_sum_smo, *buffer_max_smo;
10
  INT16     N_mode_sub_ref, consec;
  INT16 updates;
  FLOAT64 ma_max;
15
   20 FLOAT64 lpcg_m, alpha;
   INT16 lag_m, SVS_flag_m;
   FLOAT64 *wsp_m;
25 FLOAT64 *hh_hf;
   FLOAT64
                 **PHI;
   INT16 *MaxIdx;
   INT16 **track;
30
   INT16 **p_track_2_5_0, **p_track_2_7_1;
   INT16 ***p_track_3_2_80, ***p_track_3_2_54;
    INT16 **p_track_5_4_0, **p_track_5_3_1, **p_track_5_3_2, **p_track_8_4_0;
 35 FLOAT64 **unfcod_dec;
                                             */
                       13 bits
 40
```

167 FLOAT64 Stab_13b_enc; FLOAT64 Stab_13b_dcc; */ 15 bits INT64 seed_bfi_exc; FLOAT64 Stab_15b_cnc; 10 FLOAT64 Stab_15b_dec; 15 INT16 N_bfi, bfh_oh; INT16 past_bfi, ppast_bfi; INT16 bfi_caution, nbfi_count; FLOAT64 *enrg buff; 20 FLOAT64 *ext_dcc_mem; 25 FLOAT64 *synth mcm, *synth_mcm_dec, *dif_mem, *target_mem; FLOAT64 *qua_synth_mem, *qua_dif_mem, *qua_target_mem, *qua_ext; FLOAT64 *sigsyn_dec, *ext_dec; INT16 *bitno0, *bitno1;

-----*/

40

	168 -====================================
	*/
* Conexant System Inc.	*/
4311 Jamborce Road	*/
Newport Beach, CA 92660	*/
*	
* Copyright(C) 2000 Conexant System Inc.	*/
*	**/
* ALL RIGHTS RESERVED:	*/
* No part of this software may be reproduced in	any form or by any */
* means or used to make any derivative work (su	uch as transformation */
/* or adaptation) without the authorisation of Cor	nexant System Inc. */
/*====================================	*/
/* I IRD ADV: anutil C	*/
/*====================================	*/
	-
/*	*/
/*INCLUDE	
/*	**/
,	
#include "typedef.h"	
#include "main.h"	
#include "mcutil.h"	
#include "gputil.h"	
/*	
/* FUNCTIONS	
/*	*/
/*=====================================	
/* FUNCTIONS : file_open_r ()	*/
/+	·*/
/* PURPOSE : These functions open the	
e de la companie de l	
/* and return a pointer to this file.	
	*/
/* INPUT ARGUMENTS : _ file name.	·
/*	· · ·
0 /* OUTPUT ARGUMENTS : _ Nonc.	*/

```
169
 /* RETURN ARGUMENTS : _ pointer to the file.
 *file_open_r ( char *file_name )
5 FILE
       /+____*/
       FILE *file_ptr;
10
       if ( (file_ptr = fopen ( file_name, "r" ) ) == NULL)
15 #ifdef VERBOSE
            printf("\nfile name : %s\n", file_name);
            nrerror ("error opening file (r) ...");
  #endif
20
       return (filc_ptr);
25
/* FUNCTIONS : file_open_w ()
  /* PURPOSE : These functions open the file named file_name,*/
          and return a pointer to this file.
35 /*----*/
                                      */
  /* INPUT ARGUMENTS : _ file name.
  /* OUTPUT ARGUMENTS : _ Nonc.
40 /* RETURN ARGUMENTS: _ pointer to the file.
```

```
170
        *file_open_w ( char *file_name )
  FILE
5
         FILE *file_ptr;
10
         if ( (file_ptr = fopen ( file_name, "w" ) ) == NULL)
               {
  #ifdef VERBOSE
               printf("\nfile name : %s\n", file_name);
               nrerror ("error opening file (w) ...");
15
   #endif
               }
20
          return (file_ptr);
             */
25
   /* FUNCTIONS : file_open_rb ()
   /* PURPOSE : These functions open the file named file_name,*/
        and return a pointer to this file.
    /* INPUT ARGUMENTS : _ file name.
    /* OUTPUT ARGUMENTS : _ None.
    /* RETURN ARGUMENTS : _ pointer to the file.
  40
```

```
171
        *file open_rb ( char *file_name )
  FILE
        FILE *file_ptr;
5
        if ( (file_ptr = fopen ( file_name, "rb" ) ) == NULL)
10
  #ifdef VERBOSE
              printf("\nfile name : %s\n", file_name);
              nrerror ("error opening file (rb) ...");
  #endif
15
              }
         return (file_ptr);
20
25
                                          */
  /* FUNCTIONS : file_open_wb ()
  /* PURPOSE : These functions open the file named file_name,*/
     and return a pointer to this file.
30 /*
   /+_----+/
   /* INPUT ARGUMENTS : _ file name.
   /* OUTPUT ARGUMENTS : _ None.
/* RETURN ARGUMENTS : _ pointer to the file.
   FILE *file_open_wb ( char *file_name )
40
         {
```

```
172
        FILE *file_ptr;
5
        if ( (file_ptr = fopen ( file_name, "wb" ) ) == NULL)
              {
  #ifdef VERBOSE
              printf("\nfile name : %s\n", file_name);
10
              nrerror ("crror opening file (wb) ...");
  #endif
              }
15
         rcturn (filc_ptr);
         /*____*/
20
  25 /* FUNCTIONS : output_waves_file ()
   /* PURPOSE : These functions writes an array of double in */
             XWAVES format file.
30 /* INPUT ARGUMENTS : _ (char *) file_name : name of the xwaves file.*/
            : _(FLOAT64 *) x : array of double.
                       : size of the x array. */
            : _ (INT32) N
            : _(FLOAT64) scale : scale value.
                           : min range value.
            : _ (FLOAT64) min
            : _(FLOAT64) max : max range value.
35 /*
   /* OUTPUT ARGUMENTS : _ Nonc.
   /* RETURN ARGUMENTS : _ pointer to the file.
```

```
output_waves_file ( char *file_name, FLOAT64 *x, INT32 N, FLOAT64 scale, \
  void
                                                    FLOAT64 min, FLOAT64 max)
          {
5
          FILE *file_ptr;
          INT32 i;
          INT16 *waves;
10
          waves = svector (0, N-1);
          file_ptr = filc_open_wb (file_name);
15
          waves [0] = (INT16)(max);
          waves [1] = (INT16)(min);
           fwrite (waves, sizeof (INT16), 2L, file_ptr);
20
           for (i = 0; i < N; i ++)
                 waves [i] = (INT16)(scale * x [i]);
           fwrite (waves, sizeof (INT16), N, file_ptr);
25
           fclose (file_ptr);
           free_svector (waves, 0, N-1);
30
           return;
35
           }
```

/* FUNCTION : file_length ()	174 */ */
/* PURPOSE : This function returns the le /* bytes. */ 5 /*	ngth of a file in */
/* INPUT ARGUMENTS : _ Pointer to a file.	*/
/*	*/
10 /* RETURN ARGUMENTS: Length of the fil	
INT64 file_lcngth (FILE *filc_ptr) { 15 /*	
INT64 file_size;	
/*	*/
file_size = ftell(file_ptr);	*/
return (file_size);	
/*}	*/
30 /*	·*/
/*====================================	*/ */
35 /* PURPOSE : This function reads short /* stores them in an array of doub	samples from a file */ le. */
/* INPUT ARGUMENTS : _ Name of the sig /* _ Number of samples.	*/ */
40 /	

```
175
  /* OUTPUT ARGUMENTS : _ Array of double.
  /* RETURN ARGUMENTS : _ Nonc.
          rcad_signal_double (FILE *filc_ptr, FLOAT64 *array, INT64 N)
  void
          INT16 *array_short;
10
          INT64 i;
          array_short = svector (0, N-1);
15
          if ( (fread (array_short, sizeof (INT16), (INT32) N, file_ptr) ) !=
                               (sizc_t)N)
                 nrerror ("error reading file ...");
20
           for (i = 0; i < N; i ++)
                 *(array+i) = (FLOAT64) (*(array_short+i));
           free svector (array_short, 0, N-1);
25
           rcturn;
30
35
   /* FUNCTION : write_signal_double ()
   /* PURPOSE : This function writes short samples to a file */
                                              */
40 /*
              from an array of double.
```

	176 */	
	UT ARGUMENTS : _ Array of double. */	
/*	Number of samples. */	
•	*/	
5 /* OUT	TPUT ARGUMENTS: _ Output short file. */	-
/# DET	TURN ARGUMENTS : _ None.	: * /
/ *=== =		
) void	write_signal_double (FILE *file_ptr, FLOAT64 *array, INT64 N)	
	/**/	
	/*	
	INT16 *array_short;	
5	INT64 i;	
	*/	
	/**/	
	$array_short = svector(0, N-1);$	
20		
	for $(i = 0; i < N; i ++)$	
	(array_short+i) = (INT16) nint ((array+i));	
•	if ((fwritc (array_short, sizeof (INT16), (INT32) N, filc_ptr)) !=	
25		
	(sizc_t)N)	
	nrerror ("error writing file");	
	free_svector (array_short, 0, N-1);	
30	**	
	/**/	
	return;	
35	/**/	
	}	
	*/	
/*		
40 44 -	***************************************	===

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```
177
                                 */
 /* FUNCTION : gct_int ().
  /* PURPOSE : These function read a value from a text file. */
  /*____*/
                                     */
5 /* INPUT ARGUMENTS : _ file pointer.
  /*____*/
  /* OUTPUT ARGUMENTS : _ (INT32*) val : output value.
  /*____*/
  /* RETURN ARGUMENTS : _ None.
                                      */
get int (FILE *file_ptr, INT32 *val)
  void
15
       char buffer [NB_MAX_CHAR];
20
       fgets (buffer, NB_MAX_CHAR, file_ptr);
       sscanf (buffer, "%d", val);
       /*_____*/
25
       rcturn;
30
  /* FUNCTION : get_two_int ().
35 /*----*/
  /* PURPOSE : These function read two values from a text file. */
  /* INPUT ARGUMENTS : _ file pointer.
40 /* OUTPUT ARGUMENTS:
```

```
178
           _(INT32*) vall : output value.
           _(INT32*) val2 : output value.
  /* RETURN ARGUMENTS : _ None.
gct_two_int (FILE *file_ptr, INT32 *val1, INT32 *val2)
  void
10
             buffer [NB_MAX_CHAR];
        char
        fgets ( buffer, NB_MAX_CHAR, file_ptr );
15
        sscanf (buffer, "%d %d", val1, val2);
20
         return;
25
   /* FUNCTION : gct_long ().
   /* PURPOSE : These function read a value from a text file. */
   /* INPUT ARGUMENTS : _ file pointer.
   /<del>*</del>____*/
 35 /* OUTPUT ARGUMENTS: _(INT64*) val : output value.
    /* RETURN ARGUMENTS : _ None.
    gct_long (FILE *file_ptr, INT64 *val)
 40 void
```

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```
179
      char buffer [NB_MAX_CHAR];
5
       fgets (buffer, NB_MAX_CHAR, file_ptr);
       sscanf (buffer, "%ld", val );
10
               */
       return;
15
20
  /* FUNCTION : gct_float ().
25 /* PURPOSE : These function read a value from a text file. */
  /* INPUT ARGUMENTS : _ file pointer.
  /* OUTPUT ARGUMENTS : _ (FLOAT32*) val : output value.
  /* RETURN ARGUMENTS : _ None.
  get FLOAT32 (FILE *file_ptr, FLOAT32 *val)
  void
35
       /*____*/
       char buffer [NB_MAX_CHAR];
40
```

	fgcts (buffcr, NB_MAX_CHAR, file_ptr);
	sscanf (buffer, "%f", val);
5	/**/
	return;
10	/**/
	}
	/**/
	/*=======*/ /* FUNCTION : get_double ().
	/* PURPOSE : These function read a value from a text file. */ /**/
	/* INPUT ARGUMENTS : _ file pointer.
	/* OUTPUT ARGUMENTS : _ (FLOAT64*) val : output value.
25	/* RETURN ARGUMENTS : _ None.
	void get_double (FILE *file_ptr, FLOAT64 *val) {
30	/**/
	char buffer [NB_MAX_CHAR];
	/**/
35	fgets (buffer, NB_MAX_CHAR, file_ptr);
	sscanf (buffer, "%lf", val);
40	/**/

	return;		
	/*	*/	
		,	
5	}		
_		+/	•
,	,		
/ *== ==		.======================================	
/* FUN	ICTION: ini_dvector ().	* /	
0 /*		*/	
/* PUF	POSE : This function initializ	es a vector of FLOAT64 */	
/*	to a given value.	*/	
/* INP	UT ARGUMENTS : _ (FLOAT64*) v vector of double. */	
.5 /*	_ (INT32) nl	*/	
/*	_ (INT32) nh	*/	
/*	_ (FLOAT64) val	*/	
	TPUT ARGUMENTS: _ (FLOAT6		
	TURN ARGUMENTS: _ None.	*/	
/*===	=======================================		=========
void	ini_dvcctor (FLOAT64 *v, INT32	nl, INT32 nh, FLOAT64 val)	
25	{	***	
	/*	,	
	INT32 i;		
		*/	
30	/*	**************************************	
	for $(i = nl; i \le nh; i ++)$		
	v[i] = val;		
	/*	*/	
35	/*		
	return;		
		*/	
	/*		
40	}		

	22222222	125222222222222	= = ==
CTION : ini_svector ().	•	1	
	•		
to a given value.	*/		
		*/	
_(INT32) nl	*/		
_(INT32) nh	*/		
_(INT16) val	*/		
PUT ARGUMENTS : _ (INT16 *) i	nitialized v ve	ctor. */	
URN ARGUMENTS : _ None.	***************************************	*/	
INT32 i;			
for (i = nl; i <= nh; i ++)			
v[i] = val;			
/*		·*/	
return;			
/*		*/	
		*	
}			
	CTION : ini_svector (). POSE : This function initialize to a given value. TT ARGUMENTS : _ (INT16 *) v v (INT32) nl (INT32) nh (INT16) val PUT ARGUMENTS : _ (INT16 *) i JRN ARGUMENTS : _ None. ini_svector (INT16 *v, INT32 nl, I { /*	CTION : ini_svector (). POSE : This function initializes a vector of I to a given value. */ **TARGUMENTS : _ (INT16 *) v vector of int.	*/ POSE : This function initializes a vector of INT16 */ to a given value. */

```
183
 /* FUNCTION : ini_ivector ().
                           */
 /*____*/
 /* PURPOSE : This function initializes a vector of INT32 */
       to a given value.
 /*----*/
 /* INPUT ARGUMENTS: _ (INT32 *) v vector of int.
        _ (INT32 ) nl ·
 /*
 /*
        _(INT32 ) nh
        _ (INT32 ) val
10 /*
 /* OUTPUT ARGUMENTS : _ (INT32 *) initialized v vector.
 /*____*/
 /* RETURN ARGUMENTS: None.
ini_ivector ( INT32 *v, INT32 nl, INT32 nh, INT32 val )
 void
        */
20
      INT32 i:
      /*----*/
      for (i = nl; i \le nh; i ++)
25
         v[i] = val;
30
      return;
  /* FUNCTION : cpy_svector ().
40 /* PURPOSE : This function writes v1 to v2.
```

```
184
  /* INPUT ARGUMENTS: _ (INT16 *) v1 vector of short.
          _ (INT16 *) v2 vector of short.
          _(INT32) nl
                             */
  /*
                             */
5 /*
          _ (INT32) nh
  /* OUTPUT ARGUMENTS : _ (INT16*) new v2 vector.
  /* RETURN ARGUMENTS : _ None.
cpy_svector (INT16 *v1, INT16 *v2, INT32 nl, INT32 nh)
  void
15
       INT32 i;
       /*____*/
       for (i = nl; i \le nh; i \leftrightarrow)
20
           v2[i] = v1[i];
            */
25
       rcturn;
30 /*-----*/
/* FUNCTION : cpy_ivector ().
 /* PURPOSE : This function writes v1 to v2.
40 /* INPUT ARGUMENTS : _ (INT32*) v1 vector of int.
```

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```
185
             _ (INT32*) v2 vector of int.
             _ (INT32) nl
             _ (INT32) nh
5 /* OUTPUT ARGUMENTS: _(INT32*) new v2 vector.
  /* RETURN ARGUMENTS : _ None.
        cpy_ivector ( INT32 *v1, INT32 *v2, INT32 nl, INT32 nh)
10 void
         INT32 i;
15
         for (i = nl; i \le nh; i ++)
               v2[i] = v1[i];
20
         return;
25
/* FUNCTION : cpy_dvcctor ().
   /* PURPOSE : This function writes v1 to v2.
35 /* INPUT ARGUMENTS : _ (FLOAT64*) v1 vector of double.
              _ (FLOAT64*) v2 vector of double.
   /*
              _ (INT32) nl
   /*
              _ (INT32) nh
40 /* OUTPUT ARGUMENTS: _(FLOAT64*) new v2 vector.
```

/ *	·	186 **/
/* RI	ETURN ARGUMENTS : _ None.	*/ ====================================
5 void		64 *v2, INT32 nl, INT32 nh)
10	INT32 i;	*/
15	for (i = nl; i <= nh; i ++) v2 [i] = v1 [i];	·*/
20	return; /*}	
25 /*== /* F	FUNCTION : sfr_ivector ().	*/ ,
/* F /* 30 /*	PURPOSE : This function shift rigth position. INPUT ARGUMENTS : _(INT32 *) v1 v	v elements of n */ */*/
	_ (INT32) n number of rigth s _ (INT32) nl lower index _ (INT32) nh higher index	*/ */ **/
/*- /* :	OUTPUT ARGUMENTS : _ (INT32 *) v2	·
/+= 40		

```
sfr ivector ( INT32 *v1, INT32 *v2, INT32 n, INT32 nl, INT32 nh)
  void
        INT32 i;
5
        for (i = nh-n; i \ge nl; i --)
              v2[i+n] = v1[i];
10
         return;
15
/* FUNCTION : sfr_dvector ().
  /* PURPOSE : This function shift roigth v elements of n */
                                   */
             position.
  /* INPUT ARGUMENTS: _(FLOAT64*) v1 vector of double.
             _(INT32) n number of rigth shift
  /*
             _(INT32) nl lower index
                                        */
             _(INT32) nh higher index
30 /*----*/
  /* OUTPUT ARGUMENTS: _(FLOAT64*) v2 shifted rigth v1 vector. */
   /*_____*/
  /* RETURN ARGUMENTS : _ None.
35
         sfr_dvector (FLOAT64 *v1, FLOAT64 *v2, INT32 n, INT32 nl, INT32 nh)
   void
40
         INT32 i;
```

	/*	*/
•		•
	for $(i = nh-n; i >= nl; i)$	
	v2[i+n] = v1[i];	
	/*	*/
	return;	
		.,
	/*	*/
	}	
		••
/*	······································	
		**
	CTION : rev_dvector ().	*/
•		
	POSE : This function reverse the or	*/
•	clements of a vector.	•
	στ. Ο Δ Τζ (4\$) - 1 -	
/* INPU	UT ARGUMENTS : _(FLOAT64*) v1 v	*/
/*	_ (INT32) nl lower index	*/
/*	_ (INT32) nh higher index	·
	(CLOAT(A*) v	
/* OUT	TPUT ARGUMENTS : _ (FLOAT64*) v2	Z ICVCISCA VI VOCION
		*/
/* RET	TURN ARGUMENTS : _ None.	,
	======================================	
	rev_dvector (FLOAT64 *v1, FLOAT6	.4 *v2 INT32 nl INT32 nh)
void	_	, , , , , , , , , , , , , , , , , , , ,
	/*	*/
	/*	·
_	DET21 in	
5	INT32 i;	
	/*	*/
	/*	·
	for (i = nl; i <= nh; i++)	•
_	•	
40	v2 [i] = v1 [nh-i];	

	/**/
	rcturn;
5	
	/**/
	}
	*/
/ * 10	
/* F	UNCTION : sca_dvector (). */
-	URPOSE : This function scale v1 into v2 by the factor */
15 /*	s. */
	*/
/* II	NPUT ARGUMENTS: _(FLOAT64*) v1 vcctor of double. */
/*	_(FLOAT64) s scale factor. */
/*	_(INT32) nl */
20 /*	_ (INT32) nh */
	*/
	OUTPUT ARGUMENTS: _ (FLOAT64*) v2 scaled vector. */
•	*/
	ETURN ARGUMENTS : _ None.
25 /-=	
voic	sca_dvector (FLOAT64 *v1, FLOAT64 s, FLOAT64 *v2, INT32 nl, INT32 nh)
	{
	/**/
30	•
	INT32 i;
	/**/
35	for $(i = nl; i \le nli; i \leftrightarrow)$
	v2[i] = s * v1[i];
	/**/
40	return;

/* PURPOSE : This function calculate the dot product		/**/
/* FUNCTION : dot_dvector (). */ /* ————*/ /* PURPOSE : This function calculate the dot product */ /* between v1 and v2 */ /*————*/ /* INPUT ARGUMENTS : _(FLOAT64*) v1 vector of double. */ /*(FLOAT64*) v2 vector of double. */ /*(INT32) nl */ /*(INT32) nh */ /*(INT32) nh */ /*(FLOAT64*) s dot product. */ /*(* RETURN ARGUMENTS : None. */ /* RETURN ARGUMENTS : None. */ /* return arguments : */ /* return arguments : None. */ /* ** **/ **/ ** ** **/ ** **		}
/* FUNCTION : dot_dvector ().	/ *	*
/* FUNCTION : dot_dvector ().		·
/* PURPOSE : This function calculate the dot product /* between v1 and v2 */ /*	/* FUN	CTION : dot_dvector (). */
/* between v1 and v2	•	
/* INPUT ARGUMENTS : _ (FLOAT64*) v1 vector of double.	/*	between v1 and v2 */
/*(FLOAT64*) v2 vector of double.	•	•
/*(INT32) nh */ /**/ /* OUTPUT ARGUMENTS:		
/*	/ *	_ (INT32) nl */
/* OUTPUT ARGUMENTS: /*(FLOAT64 *) s dot product. /**/ /* RETURN ARGUMENTS: _ None. /* void dot_dvector (FLOAT64 *v1, FLOAT64 *v2, FLOAT64 *s, INT32 nl, INT32 nh) { /**/ INT32 i; /**/ (*s) = 0.0; for (i = nl; i <= nh; i ++)		_ (11132) 101
/*(FLOAT64 *) s dot product. */ /**/ /* RETURN ARGUMENTS : _ None. */ /*=================================		· · · · · · · · · · · · · · · · · · ·
/*		
/* RETURN ARGUMENTS: _ None.		
void dot_dvector (FLOAT64 *v1, FLOAT64 *v2, FLOAT64 *s, INT32 nl, INT32 nh) { /**/ INT32 i; /**/ (*s) = 0.0; for (i = nl; i <= nh; i ++)		
/**/ INT32 i; /**/ (*s) = 0.0; for (i = nl; i <= nh; i ++)	void	dot_dvector (FLOAT64 *v1, FLOAT64 *v2, FLOAT64 *s, INT32 nl, INT32 nh)
INT32 i; /**/ (*s) = 0.0; for (i = nl; i <= nh; i ++) (*s) += v1 [i] * v2 [i]; /**/ return;	5	·
/**/ (*s) = 0.0; for (i = nl; i <= nh; i ++)		/
(*s) = 0.0; for (i = nl; i <= nh; i ++) (*s) += v1 [i] * v2 [i]; /**/ return;		INT32 i;
for (i = nl; i <= nh; i ++) (*s) += vl [i] * v2 [i]; /*		/**/
(*s) += v1 [i] * v2 [i]; /**/ return;		(*s) = 0.0;
/**/ return;		for $(i = nl; i \le nh; i ++)$
return;	5	(*s) += v1 [i] * v2 [i];
return;		
		/**/
		return;

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```
191
 /* FUNCTION : add_dvector ().
 /* PURPOSE : This function adds v1 to v2 in v.
  /* INPUT ARGUMENTS : _(FLOAT64*) v1 vector of double.
          _(FLOAT64*) v2 vector of double.
          _(INT32) nl
  /*
          _ (INT32) nh
  /* OUTPUT ARGUMENTS: _(FLOAT64*) v=v1+v2 vector.
  /*____*/
  /* RETURN ARGUMENTS : _ Nonc.
  20
      add dvector (FLOAT64 *v1, FLOAT64 *v2, FLOAT64 *v, INT32 nl, INT32 nh)
  void
         .....*/
25
       INT32 i;
       /*_____*/
       for (i = nl; i \le nh; i ++)
           v[i] = v1[i] + v2[i];
30
       return;
35
40
```

```
*/
  /* FUNCTION : wad_dvector ().
  /* PURPOSE : This function adds v1 to v2 in v.
  /* INPUT ARGUMENTS: _ (FLOAT64*) v1 vector of double.
            _ (FLOAT64) who weight for v1.
            _ (FLOAT64*) v2 vector of double.
            _ (FLOAT64) w2 weigth for v2.
            (INT32) nl
10 /*
            (INT32) nh
  /* OUTPUT ARGUMENTS : _ (FLOAT64*) v = w1 * v1 + w2 * v2 vector. */
  /*____*/
15 /* RETURN ARGUMENTS: _ None.
  wad_dvector (FLOAT64 *v1, FLOAT64 w1, FLOAT64 *v2, FLOAT64 w2,
  void
                              FLOAT64 *v, INT32 nl, INT32 nh)
20
        INT32 i;
25
        for (i = nl; i \le nh; i ++)
              v[i] = w1 * v1[i] + w2 * v2[i];
30
        return;
35
        }
40 /* FUNCTION : mul_dvector ().
```

```
193
  /* PURPOSE : This function multiply v1 by v2 in v.
  /* INPUT ARGUMENTS : _ (FLOAT64*) v1 vector of double.
             _(FLOAT64*) v2 vector of double.
5 /*
           __ (INT32) nl
  /*
                                      */
             (INT32) nh
  /* OUTPUT ARGUMENTS : _ (FLOAT64*) v=v1*v2 vector.
                                                       */
  /* RETURN ARGUMENTS : _ None.
  mul_dvector (FLOAT64 *v1, FLOAT64 *v2, FLOAT64 *v, INT32 nl, INT32 nh)
  void
15
         {
         /*____*/
         INT32 i;
20
         for (i = nl; i \le nh; i ++)
               v[i] = v1[i] * v2[i];
25
         return;
30
35 /* FUNCTION : dif_dvector ().
   /* PURPOSE : This function substracts v2 to v1 into v. */
   /* INPUT ARGUMENTS: _(FLOAT64*) v1 vector of double.
40 /*
              (FLOAT64*) v2 vector of double.
```

```
194
             _ (INT32) nl
             (INT32) nh
  /* OUTPUT ARGUMENTS : (FLOAT64*) v=v1-v2 vector.
  /* RETURN ARGUMENTS : _ Nonc.
  dif dvcctor (FLOAT64 *v1, FLOAT64 *v2, FLOAT64 *v, INT32 nl, INT32 nh)
  void
10
        {
         INT32 i;
15
         for (i = nl; i \le nh; i ++)
               v[i] = v1[i] - v2[i];
20
         return;
25
30 /* FUNCTION : cml_dvector ().
  /* PURPOSE : This function accumulates v2 to v1.
  /*____*/
  /* INPUT ARGUMENTS : _ (FLOAT64*) v1 vector of double.
              _ (FLOAT64*) v2 vector of double.
35 /*
              (INT32) nl
              (INT32) nh
   /* OUTPUT ARGUMENTS : _ (FLOAT64*) ncw v1=v1+v2 vector.
```

```
195
 /* RETURN ARGUMENTS : _ None.
 cml dvector (FLOAT64 *v1, FLOAT64 *v2, INT32 nl, INT32 nh)
 void
5
      INT32 i;
10
      for (i = nl; i \le nh; i ++)
           v1[i] += v2[i];
15
      return;
20
  25 /* FUNCTION : cml_svector ().
  /*____*/
  /* PURPOSE : This function accumulates v2 to v1.
    */
  /* INPUT ARGUMENTS: (INT16*) v1 vector of short.
         _ (INT16*) v2 vector of short.
30 /*
         _ (INT32) nl
  /*
         _ (INT32) nh
  /* OUTPUT ARGUMENTS: _ (FLOAT64*) new v1=v1+v2 vector.
35 /*-----*/
  /* RETURN ARGUMENTS : _ None.
  cml_svcctor (INT16 *v1, INT16 *v2, INT32 nl, INT32 nh)
  void
40
```

	/*	196	*/	
	, · 			
	INT32 i;			
	/*		*/	
	J.			
	for $(i = nl; i \le nh; i ++)$			
	v1[i] += v2[i];			
	/ *		*/	
	rcturn;			
	/*		*/	
	}			
/*			*/	
/* FU	INCTION : max_dvector ().	*/		
/* PL	TRPOSE : This function finds the	e maximum of v.	*/	
-	PUT ARGUMENTS: _(FLOAT64*)		ile. */	
/ *	_(INT32) nl	*/		
/*	_(INT32) nh	*/		
	UTPUT ARGUMENTS : _ (FLOAT64		*/	
/*				
) /* R	ETURN ARGUMENTS: _ None.		*/	
/ * ==			me======	
void	max_dvcctor (FLOAT64 *v, FLO	AT64 *max, INT	732 nl, INT32 n	h)
	{			
5	/*		*/	
	ramena i.			
	INT32 i;			
	/*		*/	•
0				

```
for (i = nl+1, *max = *(v+nl); i \le nh; i ++)
               *max = ((*(v+i) > *max) ? *(v+i) : *max);
5
         return;
10
  /* FUNCTION : min_dvector ().
15 /*----*/
  /* PURPOSE : This function finds the minimum of v.
  /* INPUT ARGUMENTS: _(FLOAT64*) v vector of double.
              (INT32) nl
              _(INT32) nh
20 /*
   /* OUTPUT ARGUMENTS : _ (FLOAT64*) min.
   /* RETURN ARGUMENTS : _ None.
min_dvcctor (FLOAT64 *v, FLOAT64 *min, INT32 nl, INT32 nh)
   void
30
          INT32 i;
          for (i = nl+1, *min = *(v+nl); i \le nh; i ++)
35
                *min = ((*(v+i) < *min) ? *(v+i) : *min);
40
          return;
```

```
/* FUNCTION : limit_dvector ().
10 /* PURPOSE : This function limits the range of v.
  /* INPUT ARGUMENTS: _(FLOAT64*) v vector of double.
           _(INT32) nl
           _(INT32) nh
           _(FLOAT64) min
15 /*
           _(FLOAT64) max
     /* RETURN ARGUMENTS : _ None.
limit_dvector (FLOAT64 *v, INT32 nl, INT32 nh, FLOAT64 min, FLOAT64 max )
  void
25
        INT32 i;
        for (i = nl; i \le nh; i \leftrightarrow)
30
              *(v+i) = ((*(v+i) < min) ? min : *(v+i));
              (v+i) = ((v+i) > max) ? max : (v+i);
35
         rcturn;
40
```

	3
	/*
	/**/
5	/*=====================================

```
200
  /* Conexant System Inc.
                                      */
 5 /* 4311 Jamboree Road
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  15 /* PROTOYPE FILE: gputil.h
  /*----*/
  /*-----*/
  FILE
        *file_open_r
                          (char *);
  FILE
        *file_open_w
                          (char *);
  FILE
        *file_open rb
                          ( char * );
25 FILE
        *file_open_wb
                          ( char * );
                         (char *, FLOAT64 *, INT32, \
  void
        output_waves_file
                                     FLOAT64, FLOAT64, FLOAT64);
30 INT64 file length
                          (FILE *);
  void
        read_signal_double
                          (FILE *, FLOAT64 *, INT64);
        write_signal_double
  void
                          (FILE *, FLOAT64 *, INT64);
35 void
        get int
                          (FILE *file_ptr, INT32 *val);
                    (FILE *file_ptr, INT64 *val);
        get long
  void
        get_float
                    (FILE *file ptr, FLOAT32 *val);
  void
        get double
                          (FILE *file ptr, FLOAT64 *val);
  void
                         (FILE *file_ptr, INT32 *, INT32 *);
40 void
        get_two_int
```

```
(INT16 *, INT32, INT32, INT16);
          ini svector
   void
                          (INT32 *, INT32, INT32, INT32);
          ini ivector
   void
                                 (FLOAT64 *, INT32, INT32, FLOAT64);
          ini_dvector
   void
                                 (INT16 *, INT16 *, INT32, INT32);
5 void
          cpy_svector
          cpy_ivector
                                 (INT32 *, INT32 *, INT32, INT32);
   void
                                 (FLOAT64 *, FLOAT64 *, INT32, INT32);
          cpy_dvector
   void
                                 (FLOAT64 *, FLOAT64 *, INT32, INT32);
   void
          rev_dvector
                                 (FLOAT64 *, FLOAT64 *, INT32, INT32, INT32);
10 void
          sfr_dvector
                          ( INT32 *, INT32 *, INT32, INT32, INT32);
   void
          sfr_ivector
                                 (FLOAT64 *, FLOAT64, FLOAT64 *, INT32, INT32);
          sca_dvcctor
   void
                                 (FLOAT64 *, FLOAT64 *, FLOAT64 *, INT32, INT32);
          dot_dvector
   void
                                 (FLOAT64 *, FLOAT64 *, INT32, INT32);
15 void
          cml dvector
                                 (INT16 * , INT16 * , INT32, INT32 );
          cml svector
   void
                                 (FLOAT64 *, FLOAT64 *, INT32, INT32);
   void
          max dvector
                                 (FLOAT64 *, FLOAT64 *, INT32, INT32);
   void
          min dvector
                                 (FLOAT64 *, FLOAT64 *, FLOAT64 *, INT32, INT32);
          add dvector
   void
                          (FLOAT64 *, FLOAT64, FLOAT64 *, FLOAT64,
20 void
          wad_dvector
                                         FLOAT64 *, INT32, INT32);
                                 (FLOAT64 *, FLOAT64 *, FLOAT64 *, INT32, INT32);
          mul_dvector
   void
                                 (FLOAT64 *, FLOAT64 *, FLOAT64 *, INT32, INT32);
          dif dvector
   void
25
                          (FLOAT64 *, INT32, INT32, FLOAT64, FLOAT64);
   void "limit_dvector
```

202 */ /* Conexant System Inc. 5 /* 4311 Jamboree Road /* Newport Bcach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ */ 15 /* PROTOTYPE FILE: lib bit.c /*----/+_____*/ /*-----*/ 20 /*----*/ #include "typedef.h" #include "main.h" #include "const.h" 25 #include "ext_var.h" #include "gputil.h" #include "mcutil.h" #include "lib bit.h" 30 /*_____*/ ----- FUNCTIONS -----/* FUNCTION : BIT_init_lib (). /*____*/ /* PURPOSE : Performs the bit packing library initialisation. 40 /* ALGORITHM:

	/*		203 */			
	/*/ /* INPUT ARGUMENTS :		*/			
	/* _ None.	*/	*/			
	/*/* /* OUTPUT ARGUMENTS :		*/			
	/*None. /*	*/	*/		•	
	/*/ /* INPUT/OUTPUT ARGUMENTS :		*/	,		
	/* _ None.	*/	*/			
	/*/ /* RETURN ARGUMENTS :		*/			
	/* _ None.	*/		222222	· , :±28888#55	= * ,
	/· 					
15	void BIT_init_lib (void)					
	/* <u></u>		*/			
			,			
	bitno0[0] = 7;			·e		
20	bitno0[1] = 6;					
	bitno0[2] = 2;					
	bitno1[0] = 4;					
	bitnol[1] = 4;					
25	bitnol[2] = 3;					
	/*		*/			
	return;					
30	·					
	/*	***************************************	*/		٠	
	}					
	/*		*/			
35						*
	/*====================================		*/			
	/*			. 1		
40	/* PURPOSE : This function converts /* the bit-stream.	the encode: */	r indices into *	7		
411	77 INC OII-SUCAIII.	.,				

```
204
    /* INPUT ARGUMENTS:
          _ (INT16 ) in:
                            input data.
          _ (INT16) NoOfBits: number of bits.
  5 /*----
    /* OUTPUT ARGUMENTS:
          _ (UNS_INT16 *) TrWords : trasmitted word.
          _(INT16 *) ptr: bit and word pointers.
10 /* INPUT/OUTPUT ARGUMENTS:
                                               */
    /* RETURN ARGUMENTS:
    /*
                     _ None.
    void BIT_bitpack (INT16 in, UNS_INT16 *TrWords, INT16 NoOfBits,
                       INT16 *ptr)
20
           INT16 temp;
            UNS_INT16 *WordPtr,
25
            WordPtr = TrWords + ptr[1];
            *ptr -= NoOfBits;
30
            if (*ptr >= 0)
                   *WordPtr = *WordPtr | (in << *ptr);
            else
                   temp = in >> (-*ptr);
35
                   *WordPtr = *WordPtr | temp;
                   WordPtr++;
                   *ptr = 16 + *ptr;
                   *WordPtr = (INT16) ((INT64) ((INT64) in << *ptr) & 0xffff);
                   }
40
```

	205 ptr[1] = (INT16) (WordPtr - TrWords);	
	pit[1] = (11110) (1101a ti = 1110000)	
	/*	*/
5	5 return;	
	,	
	/*	*/
	}	
10	0 /*	- /
	/*====================================	
		*/
	/* FUNCTION : BIT_bitunpack (). /**/	•
15	15 /* PURPOSE : This function converts the decoder bit-s	
13	/* into indices. */	
	/**	•
	·	*/
	/* _ (UNS_INT16 *) RecWords : received word.	*/
20	20 /* _(INT16) NoOfBits : number of bits.	*/
	/**	1
	/* OUTPUT ARGUMENTS :	*/
	/* _(INT16 *) out: output data.	
	/**	
25	25 /* INPUT/OUTPUT ARGUMENTS :	*/
	/* _ (INT16 *) ptr: bit and word pointers.	*/ _.
	/**	, */
	/* RETURN ARGUMENTS :	,
20	/* _ None. */ 30 /*====================================	
30	30 / 222222	
	void BIT_bitunpack (INT16 *out, UNS_INT16 *RecWo	rds, INT16 NoOfBits,
	INT16 *ptr)	
	{	
35	35 /*	*/
	INT64 temp;	
	UNS_INT16 *WordPtr;	
40	40 /*	*/

```
WordPtr = RecWords + ptr[1];
            *ptr -= NoOfBits;
 5
            if (*ptr >= 0)
                 temp = (INT64) (*WordPtr) << NoOfBits;
            else
                    temp = (INT64) (*WordPtr) << (NoOfBits + *ptr);
10
                    WordPtr++;
                    temp = (temp << (-*ptr)) | ((INT64) *WordPtr << (-*ptr));
                    *ptr = 16 + *ptr;
15
            *WordPtr = (INT16) (temp & 0xffff);
            *out = (INT16) ((INT64) (temp & 0 \times 100000 >> 16);
            ptr[1] = (INT16) (WordPtr - RecWords);
20
            return;
25
30 /* FUNCTION : BIT_cdbk_index_to_bits ().
   /* PURPOSE : This function converts the fixed codebook index */
               into the bit-stream representation.
35 /* INPUT ARGUMENTS:
          _(PARAMETER *) indices : fixed codebook indices. */
   /* OUTPUT ARGUMENTS:
         _ (INT16 []) packedwords: bit-stream.
                                                        */
```

```
207
 /* INPUT/OUTPUT ARGUMENTS:
     _(INT16 []) packw_ptr: pointer to the bit-stream. */
 /* RETURN ARGUMENTS:
           None.
 void BIT_cdbk_index_to_bits (PARAMETER *chan, INT16 packedwords [],
                            INT16 packw_ptr [])
10
      INT16 i, i_sf;
      /+_____*/
15
            Initalizing bit packing parameters
      packw_ptr[0] = 16;
20
              Points to the second word
      packw_ptr[1] = 1;
25
      ini_svector(packedwords, 0, PACKWDSNUM-1, 0);
       30
       switch (chan->fix_rate)
           case RATE8_5K: packedwords[0] = 4;
35
               break;
           case RATE4_0K: packedwords[0] = 3;
               break;
```

```
case RATE2_0K: packedwords[0] = 2;
              break;
          case RATE0_8K: packedwords[0] = 1;
5
              break;
          default: nrerror("Invalid rate !!");
              break;
          }
10
      if ((chan->fix_rate == RATE8_5K) || (chan->fix_rate==RATE4_0K))
15
          BIT_bitpack(chan->idx_SVS_deci, (unsigned short *) packedwords,
                                       1, packw_ptr);
      20
      switch (chan->fix_rate)
          /*____*/
25
          /*____*/
          case RATE8 5K:
30
                      25 bits
                                  */
              BIT_bitpack(chan->idx_lsf[0], (unsigned short *) packedwords,
                                   7, packw_ptr);
35
               BIT_bitpack(chan->idx_lsf[1], (unsigned short *) packedwords,
                                   6, packw_ptr);
               BIT_bitpack(chan->idx_lsf[2], (unsigned short *) packedwords,
                                   6, packw_ptr);
40
```

209 BIT_bitpack(chan->idx_lsf[3], (unsigned short *) packedwords, 6, packw_ptr); LSF interpolation 2 bits 5 if (chan->idx SVS_deci == 0) BIT_bitpack(chan->idx_lpc_int, (unsigned short *) packedwords, 2, packw_ptr); 10 break; 15 case RATE4_0K: 21 bits 20 BIT_bitpack(chan->idx_Isf[0], (unsigned short *) packedwords, 7, packw_ptr); BIT_bitpack(chan->idx_lsf[1], (unsigned short *) packedwords, 25 7, packw_ptr); BIT bitpack(chan->idx_lsf[2], (unsigned short *) packedwords, 6, packw_ptr); 30 BIT_bitpack(chan->idx_lsf[3], (unsigned short *) packedwords, 1, packw_ptr); break; 35 case RATE2_0K:

/**/
/* 25 bits */
/**/
BIT_bitpack(chan->idx_lsf[0], (unsigned short *) packedwords,
7, packw_ptr);
BIT_bitpack(chan->idx_lsf[1], (unsigned short *) packedwords,
6, packw_ptr);
BIT_bitpack(chan->idx_lsf[2], (unsigned short *) packedwords,
6, packw_ptr);
BIT bitpack(chan->idx_lsf[3], (unsigned short *) packedwords,
6, packw_ptr);
/**/
/* LSF interpolation 2 bits */
/**/
BIT_bitpack(chan->idx_lpc_int, (unsigned short *) packedwords,
2, packw_ptr);
break;
/**/
/**/
· ·
case RATEO_8K:
/**/
/* 11 bits */
/**/
BIT_bitpack(chan->idx_lsf[0], (unsigned short *) packedwords,
4, packw_ptr);
BIT_bitpack(chan->idx_lsf[1], (unsigned short *) packedwords,
4, packw_ptr);

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```
211
                          BIT bitpack(chan->idx_lsf[2], (unsigned short *) packedwords,
                                                                 3, packw_ptr);
                          brcak;
                   default: nrerror ("Invalid rate !!");
5
                          break;
                  }
10
           if ((chan->fix_rate == RATE8_5K) || (chan->fix_rate == RATE4_0K))
15
                   if (chan->idx_SVS_deci == 1)
                           if (chan->fix_rate == RATE8_5K)
                                  BIT bitpack(chan->idx_pitch[0],
                                          (unsigned short *) packedwords, 8, packw_ptr);
20
                           clsc if (chan->fix_ratc==RATE4_0K)
                                  BIT_bitpack(chan->idx_pitch[0],
                                          (unsigned short *) packedwords, 7, packw_ptr);
25
                           }
                   else
                           if (chan->fix_rate == RATE8_5K)
                                   BIT_bitpack(chan->idx_pitch[0],
30
                                          (unsigned short *) packedwords, 8, packw_ptr);
                                   BIT_bitpack(chan->idx_pitch[1],
                                          (unsigned short *) packedwords, 5, packw_ptr);
35
                                   BIT_bitpack(chan->idx_pitch[2],
                                          (unsigned short *) packedwords, 8, packw_ptr);
                                   BIT_bitpack(chan->idx_pitch[3],
                                          (unsigned short *) packedwords, 5, packw_ptr);
40
```

```
212
                                                                                                                              }
                                                                                                   else if (chan->fix_rate==RATE4_0K)
                                                                                                                                for (i = 0; i < N_SF2; i++)
    5
                                                                                                                                                          BIT_bitpack(chan->idx_pitch[i],
                                                                                                                                                                                      (unsigned short *) packedwords, 7, packw_ptr);
                                                                                                                              }
                                                                                                  }
                                                                     }
 10
                                           /*proposition and the composition of the compositio
 15
                                          switch (chan->fix_rate)
                                                                       20
                                                                      case RATE8_5K:
                                                                                                   if (chan->idx_SVS_deci==1)
                                                                                                                                BIT_bitpack(chan->idx_Gp_VQ,
25
                                                                                                                                                          (unsigned short *) packedwords, 6, packw_ptr);
                                                                                                                               BIT_bitpack(chan->idx_Gc_VQ,
                                                                                                                                                          (unsigned short *) packedwords, 10, packw_ptr);
                                                                                                                              }
30
                                                                                                   clsc
                                                                                                                               for (i = 0; i < N_SF4; i++)
                                                                                                                                                          BIT_bitpack(chan->idx_gainVQ[i],
                                                                                                                                                                                      (unsigned short *) packedwords, 7, packw_ptr);
35
                                                                                                                              }
                                                                                                  break;
                                                                       40
```

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```
case RATE4_0K:
                       if (chan->idx_SVS_deci==1)
                             BIT_bitpack(chan->idx_Gp_VQ,
5
                                    (unsigned short *) packedwords, 4, packw_ptr);
                             BIT_bitpack(chan->idx_Gc_VQ,
                                    (unsigned short *) packedwords, 8, packw_ptr);
                             }
10
                       else
                             for (i = 0; i < N_SF2; i++)
                                    BIT_bitpack(chan->idx_gainVQ[i],
                                          (unsigned short *) packedwords, 7, packw_ptr);
15
                             }
                      break;
20
                 case RATE2_0K:
                       for (i = 0; i < N_SF2; i++)
25
                             BIT_bitpack(chan->idx_gainVQ[i],
                                    (unsigned short *) packedwords, 6, packw_ptr);
                       break;
                 case RATEO_8K:
30
                             BIT_bitpack(chan->idx_gainVQ[0],
                                    (unsigned short *) packedwords, 5, packw_ptr);
                       break;
                 default: nrerror("Invalid rate !!");
35
                       break;
                }
          40
```

```
214
           switch (chan->fix_rate)
5
                  case RATE8_5K:
10
                  if (chan->idx_SVS_deci == 1)
                                     */
                                    Stationary voiced speech
15
                          for (i_sf = 0; i_sf < N_SF4; i_sf++)
                       /* 8 pulses CB : 6p \times 3b + 2p \times 4b + 4b \text{ signs} = 30 \text{ bits } */
20
                                 for (i = 0; i < 4; i++)
                                        BIT_bitpack((chan->idx_cpcbsign[i_sf][i]),
                                                               (UNS_INT16 *) packedwords, 1,
25
   packw_ptr);
                                 BIT bitpack((chan->idx_cpcb[i_sf][0]),
                                                               (UNS INT16 *) packedwords, 4,
30 packw_ptr);
                                 for (i = 1; i < 4; i++)
                                        BIT bitpack((chan->idx_cpcb[i_sf][i]),
                                                               (UNS_INT16 *) packedwords, 3,
35 packw_ptr);
                                  BIT_bitpack((chan->idx_cpcb[i_sf][4]),
                                                               (UNS_INT16 *) packedwords, 4,
   packw_ptr);
                                  for (i = 5; i < 8; i++)
40
```

```
215
                   BIT_bitpack((chan->idx_cpcb[i_sf][i]),
                                            (UNS_INT16 *) packedwords, 3, packw_ptr);
                                    }
 5
                   elsc
                                      Non-Stationary voiced speech
10
                            for (i_sf = 0; i_sf < N_SF4; i_sf++)
                                     BIT_bitpack((chan->idx_subcpcb[i_sf][0]),
                                                             (UNS_INT16 *) packedwords, 1, packw_ptr);
15
                                     if (chan->idx_subcpcb[i_sf][0]==1)
                                             /*----*/
                                             /* 5 pulses CB : 3p \times 4b + 2p \times 3b + 3b \text{ signs} */
20
                                                        = 21 bits
                                             for (i = 0; i < 3; i++)
                                                     BIT_bitpack((chan->idx_cpcbsign[i_sf][i]),
25
                                                             (UNS_INT16 *) packedwords, 1, packw_ptr);
                                             BIT_bitpack((chan->idx_cpcb[i_sf][0]),
                                                             (UNS_INT16 *) packedwords, 4, packw_ptr);
30
                                             BIT_bitpack((chan->idx_cpcb[i_sf][1]),
                                                             (UNS_INT16 *) packedwords, 4, packw_ptr);
                                             BIT_bitpack((chan->idx_cpcb[i_sf][2]),
                                                             (UNS_INT16 *) packedwords, 3, packw_ptr);
35
                                              BIT_bitpack((chan->idx_cpcb[i_sf][3]),
                                                             (UNS_INT16 *) packedwords, 4, packw_ptr);
                                              BIT_bitpack((chan->idx_cpcb[i_sf][4]),
40
```

```
216
                                                        (UNS_INT16 *) packedwords, 3, packw_ptr);
                                         }
                                  clse
 5
                                          BIT_bitpack((chan->idx_subcpcb[i_sf][1]),
                                                        (UNS_INT16 *) packedwords, 1, packw_ptr);
                                                5 pulses CB: 5p \times 3b + 5b \text{ signs}
10
                                                    = 20 bits
                                          for (i = 0; i < 5; i++)
15
                      BIT_bitpack((chan->idx_cpcbsign[i_sf][i]),
                          (UNS_INT16 *) packedwords, 1, packw_ptr);
                      BIT_bitpack((chan->idx_cpcb[i_sf][i]),
                          (UNS_INT16 *) packedwords, 3, packw_ptr);
20
                                  }
                          }
                  break;
25
                   case RATE4_0K:
30
                   if (chan->idx_SVS_deci==1)
                                     Stationary voiced speech
35
                          for (i_sf = 0; i_sf < N_SF3; i_sf++)
                                  BIT_bitpack((chan->idx_subcpcb[i_sf][0]),
40
                                                        (UNS_INT16 *) packedwords, 1, packw_ptr);
```

```
if (chan->idx_subcpcb[i_sf][0] == 1)
                                            /*____*/
                                            /* 2 pulses CB : 2pulses x 5bits/pulse + 2 signs */
 5
                                                       = 12 bits
                                            for (i = 0; i < 2; i++)
                                                    {
10
                                                    BIT_bitpack((chan->idx_cpcbsign[i_sf][i]),
                                                            (UNS_INT16 *) packedwords, 1, packw_ptr);
                                                    BIT_bitpack((chan->idx_cpcb[i_sf][i]),
                                                            (UNS_INT16 *) packedwords, 5, packw_ptr);
                                                    }
15
                                            }
                                    elsc
                                                 3 pulses CB: 3 pulses x 2 bits/pulse + */
20
                                                 3 signs + 3bits center = 12 bits
                                             for (i = 0; i < 3; i++)
25
                                                     BIT_bitpack((chan->idx_cpcbsign[i_sf][i]),
                                                            (UNS_INT16 *) packedwords, 1, packw_ptr);
                                                     BIT_bitpack((chan->idx_cpcb[i_sf][i]),
                                                             (UNS INT16 *) packedwords, 2, packw_ptr);
                                                    }
30
                                             BIT_bitpack((chan->idx_center[i_sf]),
                                                             (UNS_INT16 *) packedwords, 3, packw_ptr);
                                           }
                                    }
35
                            }
                    clse
                            {
                                       Non-Stationary voiced speech
40
```

```
218
                           for (i_sf = 0; i_sf < N_SF2; i_sf++)
5
                                   BIT_bitpack((chan->idx_subcpcb[i_sf][0]),
                                                           (UNS_INT16 *) packedwords, 1, packw ptr);
                                   if (chan->idx_subcpcb[i_sf][0]==1)
                                           {
                                                 */
10
                                            /* 2 pulses CB: 2pulses x 6.5bits/pulse */
                                                     + 1 sign = 12 bits
15
                                           BIT_bitpack((chan->idx_cpcbsign[i sf][0]),
                                                           (UNS_INT16 *) packedwords, 1, packw_ptr);
                                           BIT bitpack((chan->idx cpcb[i_sf][0]),
                                                           (UNS_INT16 *) packedwords, 13, packw_ptr);
                                           }
20
                                   clsc
                                           BIT_bitpack((chan->idx_subcpcb[i_sf][1]),
                                                           (UNS_INT16 *) packedwords, 1, packw_ptr);
25
                                           if (chan->idx_subcpcb[i_sf][1] == 1)
                                                   /* 3 pulses CB: 3 pulses x 2 bits/pulse */
                                                   /* 3 signs + 4bits center = 13 bits */
30
                                                   for (i = 0; i < 3; i++)
                                                           BIT_bitpack((chan->idx_cpcbsign[i_sf][i]),
35
                                                            (UNS_INT16 *) packedwords, 1, packw_ptr);
                                                           BIT_bitpack((chan->idx_cpcb[i_sf][i]),
                                                            (UNS INT16 *) packedwords, 2, packw_ptr);
                                                    BIT_bitpack((chan->idx_center[i_sf]),
                                                           (UNS_INT16 *) packedwords, 4, packw_ptr);
40
```

```
219
                               }
                           eise
                                    Gaussian codebook, 13 bits
                                for (i = 0; i < 2, i++)
                                    BIT_bitpack((chan->idx_cpcbsign[i_sf][i]),
                                     (UNS_INT16 *) packedwords, 1, packw_ptr);
10
                                BIT_bitpack((chan->idx_cpcb[i_sf][0]),
                                    (UNS_INT16 *) packedwords, 11, packw_ptr);
                                }
                           }
15
                      }
                 }
            break;
20
            case RATE2_0K:
25
                 break;
            30
            case RATE0_8K:
                 break;
35
            default: nrerror ("Invalid rate !!");
                 break;
40
```

220 } 5 return; 10 /*-----*/ /* FUNCTION : BIT_bits_to_cdbk_index (). 15 /* PURPOSE : This function converts the the bit-stream representation into the codebook index /* INPUT ARGUMENTS : _(INT16 []) packedwords: bit-stream. 20 /*-----/* OUTPUT ARGUMENTS: _(PARAMETER *) indices : fixed codebook indices. */ /* INPUT/OUTPUT ARGUMENTS: _(INT16 []) packw_ptr: pointer to the bit-stream. */ /* RETURN ARGUMENTS: None. 30 void BIT_bits_to_cdbk_index (INT16 packedwords [], INT16 packw_ptr [], PARAMETER *chan) /*____*/ 35 INT16 i, i_sf; 40

```
221
             if ((chan->fix_rate == RATE8_5K) ||
                        (chan->fix_rate == RATE4_0K))
5
                              BIT_bitunpack (&chan->idx_SVS_deci,
                                    (UNS INT16 *) packedwords, 1, packw_ptr);
             else
                   chan->idx_SVS_deci=0;
10
               /*poppopopopopopopopo LSF quantizer index poppopopopopopopopo
              switch (chan->fix_rate)
15
                   20
                   case RATE8_5K:
                                   25 bits
25
                         BIT_bitunpack (&chan->idx_lsf[0],
                                    (UNS INT16 *) packedwords, 7, packw_ptr);
                         BIT bitunpack (&chan->idx_lsf[1],
30
                                    (UNS_INT16 *) packedwords, 6, packw_ptr);
                         BIT_bitunpack (&chan->idx_lsf[2],
                                    (UNS_INT16 *) packedwords, 6, packw_ptr);
35
                         BIT_bitunpack (&chan->idx_lsf[3],
                                    (UNS_INT16 *) packedwords, 6, packw_ptr);
                                                     */
                                LSF interpolation 2 bits
40
```

	222 /**/
	if (chan->idx_SVS_dcci == 0)
	BIT_bitunpack (&chan->idx_lpc_int,
5	(UNS_INT16 *) packedwords, 2, packw_ptr);
-	break;
	/**/
	/*====================================
10	/**/
	case RATE4_0K:
	/**/
15	/* 21 bits */
	/**/
	BIT_bitunpack (&chan->idx_lsf[0],
	(UNS_INT16 *) packedwords, 7, packw_ptr);
20	BIT_bitunpack (&chan->idx_lsf[1],
	(UNS_INT16 *) packedwords, 7, packw_ptr);
	BIT_bitunpack (&chan->idx_lsf[2],
25	(UNS_INT16 *) packedwords, 6, packw_ptr);
	DVD 11: 1.40 ston 514: 1-021
	BIT_bitunpack (&chan->idx_lsf[3], (UNS_INT16 *) packedwords, 1, packw_ptr);
	(UNS_INTIO) packedwords, 1, packin_po),
20	break;
30	Ulcak,
	/**/
	/*====================================
	/**/
35	
	case RATE2_0K:
	/**/
	/* 25 bits */
40	/**/

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```
BIT_bitunpack (&chan->idx_lsf[0],
                                            (UNS_INT16 *) packedwords, 7, packw_ptr);
                               BIT_bitunpack (&chan->idx_lsf[1],
5
                                            (UNS_INT16 *) packedwords, 6, packw_ptr);
                               BIT_bitunpack (&chan->idx_lsf[2],
                                            (UNS_INT16 *) packedwords, 6, packw_ptr);
10
                               BIT_bitunpack (&chan->idx_lsf[3],
                                            (UNS_INT16 *) packedwords, 6, packw_ptr);
                                       LSF interpolation 2 bits
15
                               BIT bitunpack (&chan->idx_lpc_int,
                                             (UNS_INT16 *) packedwords, 2, packw_ptr);
20
                        brcak;
                        /*____*/
                        25
                 case RATE0_8K:
                                           11 bits
30
                               BIT_bitunpack (&chan->idx_lsf[0],
                                             (UNS_INT16 *) packedwords, 4, packw_ptr);
                               BIT_bitunpack (&chan->idx_lsf[1],
35
                                             (UNS_INT16 *) packedwords, 4, packw_ptr);
                               BIT_bitunpack (&chan->idx_lsf[2],
                                             (UNS_INT16 *) packedwords, 3, packw_ptr);
                        break;
40
```

```
default: nrerror ("Invalid rate !!");
                        break;
                  }
5
                  10
                  if ((chan->fix_rate == RATE8_5K) ||
                                       (chan->fix_rate == RATE4_0K))
                                        if (chan->idx_SVS_deci == 1)
15
                                                if (chan->fix_rate == RATE8_5K)
                                                       BIT_bitunpack (&chan->idx_pitch[0],
                                                              (UNS_INT16 *) packedwords, 8,
                                                                      packw_ptr);
20
                                                       }
                                                clse if (chan->fix_rate == RATE4_0K)
                                                       BIT_bitunpack (&chan->idx_pitch[0],
                                                              (UNS_INT16 *) packedwords, 7,
25
                                                                      packw_ptr);
                                                       }
                                                }
                                         elsc
 30
                                                if (chan->fix_rate == RATE8_5K)
                                                        BIT_bitunpack (&chan->idx_pitch[0],
                                                                      (UNS_INT16 *) packedwords, 8,
                                                                              packw_ptr);
 35
                                                        BIT_bitunpack (&chan->idx_pitch[1],
                                                                       (UNS_INT16 *) packedwords, 5,
                                                                              packw_ptr);
 40
```

```
225
                                                                                                                                                                                                                               BIT bitunpack (&chan->idx_pitch[2],
                                                                                                                                                                                                                                                                                        (UNS_INT16 *) packedwords, 8,
                                                                                                                                                                                                                                                                                                                      packw_ptr);
                                                                                                                                                                                                                               BIT_bitunpack (&chan->idx_pitch[3],
                                                                                                                                                                                                                                                                                         (UNS INT16 *) packedwords, 5,
                                                                                                                                                                                                                                                                                                                      packw_ptr);
                                                                                                                                                                                                 clse if (chan->fix_rate == RATE4_0K)
10
                                                                                                                                                                                                                               for (i = 0; i < N_SF2; i++)
                                                                                                                                                                                                                                                             BIT_bitunpack (&chan->idx_pitch[i],
                                                                                                                                                                                                                                                                                         (UNS_INT16 *) packedwords, 7,
                                                                                                                                                                                                                                                                                                                      packw_ptr);
 15
                                                                                                                                                                                                                                                           }
                                                                                                                                                                                                                               }
                                                                                                                                                                   }
 20
                                               /*присонанивания при в присонания при в при в присонания в присона
                                               switch (chan->fix_rate)
 25
                                                                           {
                                                                             30
                                                                             case RATE8_5K:
                                                                                                        if (chan->idx_SVS_deci == 1)
                                                                                                                                        BIT_bitunpack (&chan->idx_Gp_VQ,
  35
                                                                                                                                                                                                  (UNS_INT16 *) packedwords, 6, packw_ptr);
                                                                                                                                        BIT_bitunpack (&chan->idx_Gc_VQ,
                                                                                                                                                                                                  (UNS_INT16 *) packedwords, 10, packw_ptr);
                                                                                                                                        }
   40
```

```
226
                      else
                             for (i = 0; i < N_SF4; i++)
                                   BIT_bitunpack (&chan->idx_gainVQ[i],
                                          (UNS_INT16 *) packedwords, 7, packw_ptr);
5
                             }
                      break;
                   10
                case RATE4_0K:
                       if (chan->idx_SVS_deci == 1)
15
                             BIT_bitunpack (&chan->idx_Gp_VQ,
                                          (UNS_INT16 *) packedwords, 4, packw_ptr);
                              BIT_bitunpack (&chan->idx_Gc_VQ,
20
                                          (UNS INT16 *) packedwords, 8, packw_ptr);
                             }
                       elsc
                              for (i = 0; i < N_SF2; i++)
25
                                    BIT_bitunpack (&chan->idx_gainVQ[i],
                                           (UNS_INT16 *) packedwords, 7, packw_ptr);
                              }
                       break;
30
                 case RATE2_0K:
 35
                        for (i = 0; i < N_SF2; i++)
                              BIT_bitunpack (&chan->idx_gainVQ[i],
                                           (UNS_INT16 *) packedwords, 6, packw_ptr);
                        break;
 40
```

227

```
case RATEO 8K:
                                                                                           BIT_bitunpack (&chan->idx_gainVQ[0],
                                                                                                                 (UNS_INT16 *) packedwords, 5, packw_ptr);
                                                                      break;
  5
                                                   default: nrerror("Invalid rate !!");
                                                                       break;
                                                  }
                               10
                               /*принародного принародного собрания и принародного прин
                               switch (chan->fix_ratc)
15
                                                  {
                                                    /*_____*/
                                                    /*_____*/
                                                    case RATE8_5K:
20
                                                    if (chan->idx_SVS_deci == 1)
                                                                          /*____*/
                                                                                                       Stationary voiced speech
25
                                                                         for (i_sf = 0; i_sf < N_SF4; i_sf++)
                                                                                              /*_____*/
30
                                                                /* 8 pulses CB : 6p \times 3b + 2p \times 4b + 4b \text{ signs} = 30 \text{ bits } */
                                                                                              /*----*/
                                                                                              for (i = 0; i < 4; i++)
                                                                                                                  BIT_bitunpack (&chan->idx_cpcbsign[i_sf][i],
35
                                                                                                                                                                                (UNS_INT16 *) packedwords, 1,
          packw_ptr);
```

BIT_bitunpack (&chan->idx_cpcb[i_sf][0],

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```
228
                                                                    (UNS_INT16 *) packedwords, 4,
  packw_ptr);
                                    for (i = 1; i < 4; i++)
                                           BIT_bitunpack (&chan->idx_cpcb[i_sf][i],
                                                                    (UNS_INT16 *) packedwords, 3,
5
   packw_ptr);
                                    BIT_bitunpack (&chan->idx_cpcb[i_sf][4],
                                                                    (UNS_INT16 *) packedwords, 4,
10 packw_ptr);
                                    for (i = 5; i < 8; i++)
                                            BIT_bitunpack (&chan->idx_cpcb[i_sf][i],
                                                                     (UNS_INT16 *) packedwords, 3,
15 packw_ptr);
                                    }
                            }
                    else
20
                                       Non-Stationary voiced speech
                             for (i sf = 0; i_sf < N_SF4; i_sf++)
 25
                                     BIT_bitunpack (&chan->idx_subcpcb[i_sf][0],
                                                                      (UNS INT16 *) packedwords, 1,
    packw_ptr);
 30
                                      if (chan->idx_subcpcb[i_sf][0] == 1)
                                                              */
                                              /* 5 pulses CB : 3p \times 4b + 2p \times 3b + 3b signs */
                                                          = 21 bits
  35
                                               for (i=0; i<3; i++)
                                                      BIT_bitunpack (&chan->idx_cpcbsign[i_sf][i],
```

```
229
                                                                    (UNS INT16 *) packedwords, 1,
   packw_ptr);
                                            BIT_bitunpack (&chan->idx_cpcb[i_sf][0],
                                                                    (UNS_INT16 *) packedwords, 4,
5
   packw_ptr); /
                                            BIT_bitunpack (&chan->idx_cpcb[i_sf][1],
                                                                    (UNS_INT16 *) packedwords, 4,
10 packw_ptr);
                                            BIT_bitunpack (&chan->idx_cpcb[i_sf][2],
                                                                    (UNS_INT16 *) packedwords, 3,
   packw_ptr);
15
                                            BIT bitunpack (&chan->idx_cpcb[i_sf][3],
                                                                    (UNS_INT16 *) packedwords, 4,
   packw ptr);
                                            BIT bitunpack (&chan->idx_cpcb[i_sf][4],
20
                                                                    (UNS_INT16 *) packedwords, 3,
   packw_ptr);
                                            }
                                    else
25
                                             BIT_bitunpack (&chan->idx_subcpcb[i_sf][1],
                                                                     (UNS_INT16 *) packedwords, 1,
    packw_ptr);
30
                                                    5 pulses CB : 5p x 3b + 5b signs
                                                        = 20 bits
35
                                             for (i = 0; i < 5; i++)
                                                     BIT_bitunpack (&chan->idx_cpcbsign[i_sf][i],
                                                                     (UNS_INT16 *) packedwords, 1,
40 packw_ptr);
```

```
BIT_bitunpack (&chan->idx_cpcb[i_sf][i],
                                                            (UNS INT16 *) packedwords, 3,
  packw_ptr);
                                              }
5
                                      }
                  break;
10
                  15
                  case RATE4_0K:
                  if (chan->idx_SVS_deci == 1)
                         {
20
                                   Stationary voiced speech
                          for (i_sf = 0; i_sf < N_SF3; i_sf++)
25
                                 {
                                 BIT_bitunpack (&chan->idx_subcpcb[i_sf][0],
                                                             (UNS_INT16 *) packedwords, 1,
    packw_ptr);
 30
                                  if (chan->idx_subcpcb[i_sf][0] == 1)
                                         /* 2 pulses CB : 2pulses x 5bits/pulse + 2 signs */
                                                   = 12 bits
 35
                                         for (i = 0; i < 2; i++)
                                                BIT_bitunpack (&chan->idx_cpcbsign[i_sf][i],
  40
```

```
231
                                                                      (UNS_INT16 *) packedwords, 1,
   packw_ptr);
                                                      BIT_bitunpack (&chan->idx_cpcb[i_sf][i],
                                                                      (UNS_INT16 *) packedwords, 5,
   packw_ptr);
                                                      }
                                             }
                                     else
10
                                                  3 pulses CB: 3 pulses x 2 bits/pulse +
                                                   3 signs + 3bits center = 12 bits
15
                                              for (i = 0; i < 3; i++)
                                                      BIT_bitunpack (&chan->idx_cpcbsign[i_sf][i],
                                                                      (UNS_INT16 *) packedwords, 1,
20 packw_ptr);
                                                      BIT_bitunpack (&chan->idx_cpcb[i_sf][i],
                                                                      (UNS_INT16 *) packedwords, 2,.
    packw_ptr);
                                                      }
25
                                              BIT bitunpack (&chan->idx_center[i_sf],
                                                                      (UNS_INT16 *) packedwords, 3,
    packw_ptr);
                                              }
30
                                     }
                             }
                     clsc
35
                                        Non-Stationary voiced speech
                              for (i_sf = 0; i_sf < N_SF2; i_sf++)
                                      {
 40
```

```
232
                                    BIT_bitunpack (&chan->idx_subcpcb[i_sf][0],
                                                                    (UNS INT16 *) packedwords, 1,
  packw_ptr);
                                    if (chan->idx_subcpcb[i_sf][0] == 1)
5
                                            {
                                                  2 pulses CB: 2pulses x 6.5bits/pulse */
                                                       + 1 sign = 12 bits
10
                                             BIT_bitunpack (&chan->idx_cpcbsign[i_sf][0],
                                                                     (UNS_INT16 *) packedwords, 1,
   packw_ptr);
15
                                             BIT_bitunpack (&chan->idx_cpcb[i_sf][0],
                                                                     (UNS_INT16 *) packedwords, 13,
   packw_ptr);
                                             }
                                     clse
20
                                              BIT_bitunpack (&chan->idx_subcpcb[i_sf][1],
                                                                      (UNS_INT16 *) packedwords, 1,
    packw_ptr);
25
                                              if (chan->idx_subcpcb[i_sf][1] == 1)
                                                       /* 3 pulses CB: 3 pulses x 2 bits/pulse */
                                                       /* 3 signs + 4bits center = 13 bits */
 30
                                                       for (i = 0; i < 3; i++)
                                                               BIT_bitunpack (&chan->idx_cpcbsign[i_sf][i],
 35
                                                                       (UNS_INT16 *) packedwords, 1,
     packw_ptr);
                                                               BIT_bitunpack (&chan->idx_cpcb[i_sf][i],
                                                                       (UNS INT16 *) packedwords, 2,
  40 packw_ptr);
```

```
233
                                           }
                                      BIT_bitunpack (&chan->idx_center[i_sf],
                                                 (UNS_INT16 *) packedwords, 4,
5 packw_ptr);
                                     }
                               elsc
                                           Gaussian codebook, 13 bits
10
                                      for (i = 0; i < 2; i++)
                                            BIT_bitunpack (&chan->idx_cpcbsign[i_sf][i],
                                                 (UNS_INT16 *) packedwords, 1,
15
  packw_ptr);
                                      BIT_bitunpack (&chan->idx_cpcb[i_sf][0],
                                                 (UNS_INT16 *) packedwords, 11,
20 packw_ptr);
                    }
                               }
                          }
                    }
25
                    break;
               30
               case RATE2_0K:
                    break;
35
               case RATE0_8K:
                    break;
40
```

	/**/
	/*====================================
	/**/
5	•
	default: nrerror ("Invalid rate !!");
	break;
	}
10	/**/
	return;
	*/
	/**/
15	}
	/*
	/**/
	/*

		235	
/ * =			
/ * =	=======================================		=======================================
/* (Conexant System Inc.	*/	
5 /* 4	311 Jamborec Road	*/	
	Newport Beach, CA 92660	*/	
/*		·*/	
	Copyright(C) 2000 Conexant System Inc.	*/	
/*-			•
	ALL RIGHTS RESERVED:	*/	
	No part of this software may be reproduced i		
	means or used to make any derivative work (
/*	or adaptation) without the authorisation of C	onexant System Inc. */	· */
			=======================================
5 /*:	LIBRARY: lib_bit.h	*/	*/
/*=			·
		*/	•
•	FUNCTIONS		
	FUNCTIONS		
20 /*-		•	
	id BIT_init_lib (void);		
VO	id Bri_nin_no (void),		
vo	id BIT_bitpack (INT16, UNS_INT16 *, INT	`16, INT16 *);	
25	at Dir_onphion (marro, ovi=_ss =,	,	
	id BIT_bitunpack (INT16 *, UNS_INT16 *,	INT16, INT16 *);	
,,			
vo	id BIT_cdbk_index_to_bits (PARAMETER	*, INT16 [], INT16 []);	
. •	 		
30 vo	id BIT_bits_to_cdbk_index (INT16 [], INT1	6 [], PARAMETER *);	
			
/*			:======================================
/*	END	*/	

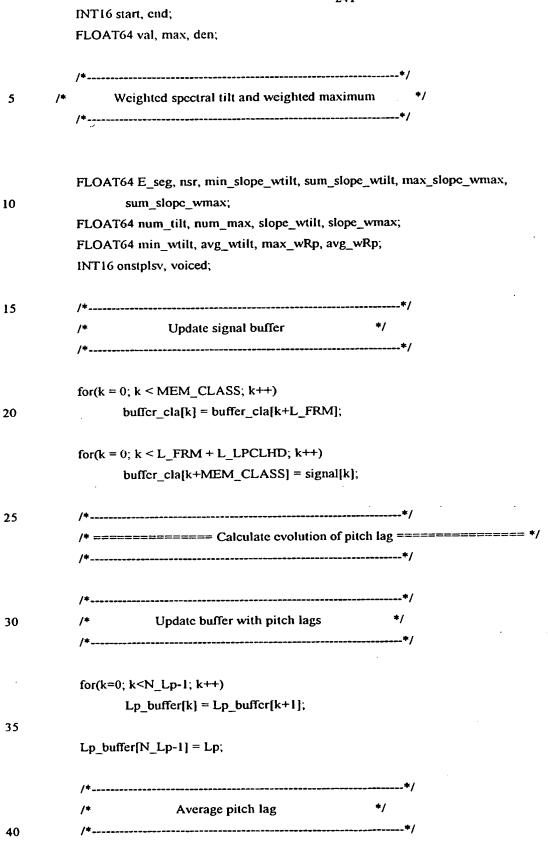
	:#====================================	236 ====================================	
	ant System Inc.	*/`	,
	amborce Road	*/	
* Newpo	ort Beach, CA 92660	*/	
*		- +/	
* Соруті	ght(C) 2000 Conexant System Inc.	*/	
/*		*/	
	UGHTS RESERVED:	*/	
	rt of this software may be reproduced in		
	or used to make any derivative work (s		
/* or ada	ptation) without the authorisation of Co	nexant System Inc. */	
/ * ====	:====================================	:35;=25;=25;=35;=35;=32	========*/
/* LIBR/	ARY: lib_cla.c	*/	
/*====			=======================================
-			
-	INCLUDE		
/*		*/	
#include	e "typedef.h"		
	•		
#include	e "main.h"		
#include	e "const.h"		
#include	e "gputil.h"		•
#include	e "ext_var.h"		
#include	e "lib_cla.h"		
#includ	e "lib_lpc.h"		
#ifdef D	DIAGNOSTICS		
#includ	e "lib_dia.h"		
#endif			
		*/	

/* FUNCTION : CLA_init_lib ().	
# FUNCTION : CLA_init_lib ().	
/* PURPOSE : This function initialise the global variables * /* of the CLA library. */ /*	
/* of the CLA library. */ /* INPUT ARGUMENTS : */ /* _ None. */ /* OUTPUT ARGUMENTS : */ /* _ None. */ /* INPUT/OUTPUT ARGUMENTS : */ /* _ None. */ /* RETURN ARGUMENTS : */ /* _ None. */ /* _ None. */ /* RETURN ARGUMENTS : */ /* _ None. */ /* The content of the class is a content of the clas	1
/* INPUT ARGUMENTS :	'
/* INPUT ARGUMENTS :	
/* OUTPUT ARGUMENTS:	
/* OUTPUT ARGUMENTS :	
/* _ None.	
/* INPUT/OUTPUT ARGUMENTS:	
/* INPUT/OUTPUT ARGUMENTS:	
/* _ None.	•
/**/ /* RETURN ARGUMENTS:	,
/* RETURN ARGUMENTS :	
/* _ None.	
/*====================================	
void CLA_init_lib (void) {	
<pre>{ /* INT16 k; FLOAT64 x; /* VUV = 0; VUVm = 2; frame_class = 0; frame_onset=0;</pre>	
<pre>{ /* INT16 k; FLOAT64 x; /* VUV = 0; VUVm = 2; frame_class = 0; frame_onset=0;</pre>	
/*	
INT16 k; FLOAT64 x; /*	*/
FLOAT64 x; /* VUV = 0; VUVm = 2; frame_class = 0; frame_onset=0;	- ,
FLOAT64 x; /* VUV = 0; VUVm = 2; frame_class = 0; frame_onset=0;	
/*	
/*	
VUVm = 2; frame_class = 0; frame_onset=0;	
VUVm = 2; frame_class = 0; frame_onset=0;	*/
VUVm = 2; frame_class = 0; frame_onset=0;	
VUVm = 2; frame_class = 0; frame_onset=0;	
frame_class = 0; frame_onset=0;	
frame_onset=0;	
energy_m = 0.0;	
energy $= 0.0$;	

```
0, CLA_MEM_SIZE-1, (INT16)0);
            ini_svector (frame_class_mem,
                                           0, CLA_MEM_SIZE-1, (INT16)0);
            ini_svector (onstplsv_mem,
                                                   0, CLA_MEM_SIZE-1, (INT16)0);
            ini_svector (voiced_mem,
5
            ini_dvector (buffer_cla, 0, L_FRM+MEM_CLASS+L_LPCLHD-1, 0.0);
            ini_dvector (Lp_buffer, 0, N_Lp-1, (FLOAT64)MIN_LAG);
            MA_avg_wRp = 0.0;
10
            MA_min_wtilt = 0.0;
            ini_dvector (P_w[0], 0, SLOPE_MAX_SIZE-1, 0.0);
            ini dvector (P_w[1], 0, SLOPE_MAX_SIZE-1, 0.0);
15
                       Generate Hamming window
            for(k = 0; k < LPC_WIN1; k++){
20
             x = cos(2.0*PI*(FLOAT64)k/(FLOAT64)(LPC_WIN1-1));
             windowl[k] = 0.54 - 0.46 * x;
            }
            ini_dvector (buffer_refl0, 0, CLA_MEM_SIZE-1, 0.0);
25
            E_{noi} = 1.0;
            T noi = 0.0;
            M_{noi} = 0.0;
30
            R_{noi} = 0.0;
            ini dvector (buffer_wtilt, 0, MAX_N_SF-1, 0.0);
             ini_dvector (buffer_wmax,
                                            0, MAX_N_SF-1, 0.0);
             ini_dvector (buffer_wRp, 0, MAX_N_SF-1, 0.0);
             ini_dvector (buffer_max_cla,
                                           0, MAX_N_SF-1, 0.0);
35
             SVS Count = 0;
             Vad_0_Count = 0;
40
```

```
239
         FlatSp_Flag = 0;
          Av value = 0.0;
5
         Rp_m_cla = 0.0;
         lpcg_m_cla = 0.0;
10
       OnSct = 0;
       framc_class_mm = 5;
15
          SVS deci_mem = 0;
20
          rcturn;
25
            -----*/
   /* FUNCTION : CLA signal_classifier ().
   /* PURPOSE : This function classify teh input signal frame: */
                                      */
   /*
             file.
   /* frame class 0: silence
   /* frame class 1: noise-like (CURRENTLY, NOT ACTIVATED)
35 /* frame_class 2: unvoiced
   /* frame_class 3: onset
   /* framc_class 4: plosive (CURRENTLY, NOT ACTIVATED)
   /* frame_class 5: non-stationary voiced
   /* framc_class 6: stationary voiced (CURRENTLY, NOT DISTINGUISHED */
                        FROM CLASS 5)
40 /*
```

/+		240 */
/*/* IN	PUT ARGUMENTS :	*/
/*	_(FLOAT64 []) signal: input frame.	*/
/ *	_ (FLOAT64) Rp : Pitch correlation.	*/
/*	_(FLOAT64) Lp : Pitch lags.	*/
/*	_(INT16) Vad : VAD of current frame	e. */
/*		*/
	UTPUT ARGUMENTS :	*/
/*	_ None. */	
		*/
	IPUT/OUTPUT ARGUMENTS :	*/
	_ None. */	
		*/
/* RI	ETURN ARGUMENTS :	*/
	_ (INT16) frame_class: current fame fram	ne_class. */
/+==		
	/ *	*/
	. *	•
	INT16 frame_class, i_sf, k, idx;	
_	/*	*/
5	/* Parameters for local first order L	•
	/*/*	
		·
	FLOAT64 siglpc1[LPC_WIN1], rxx1[2],	nderr:
0	TECHTO Signor(Ex O_ virial), virial (i)	•
	/*	*/
	/* Pitch lag evolution	*/
	/*	*/
35	FLOAT64 Lp_avg, Lp_std;	
, ,	T TO CE CAL TAN DI TAN	
	/*	·*/
	/*/// /* Maximum Tracking	*/
	/* Maximum Tracking	*/
40		*/



```
for(Lp avg=0.0, k=0; k<N_Lp; k++)
                Lp_avg += Lp_buffer[k];
          Lp_avg = (FLOAT64)N_Lp;
5
                     Standard deviation on lag
10
          for(Lp_std=0.0, k=0; k<N_Lp; k++)
                Lp_std += (Lp_buffer[k]-Lp_avg)*(Lp_buffer[k]-Lp_avg);
          Lp_std = sqrt(Lp_std/(FLOAT64)N_Lp)/Lp_avg;
15
                 Buffer onset parameters of the previous frame
          for (k = 0; k < SLOPE_MAX_SIZE; k++)
20
                 P_{w[0][k]} = P_{w[1][k]}
          25
          for(i\_sf = 0; i\_sf < CLA\_MEM\_SIZE; i\_sf++)
                 /* ======== Local first order LPC analysis ======== */
30
                              LPC windowing
35
                  mul dvector (signal+i_sf*L_LPC_SF-OVERLAP, window1, siglpc1,
                 0, LPC_WINI-1);
40
```

```
243
                       Autocorrelation
              LPC_autocorrelation (siglpc1, LPC_WIN1, rxx1, 2);
5
                      Leroux-Gueguen recursion
10
              LPC_leroux_gueguen (rxx1, &buffer_refl0[i_sf], &pderr, 1);
             }
        /*____*/
        15
        for(i_sf=0; i_sf < MAX_N_SF; i_sf++)
20
                       Find max in pitch cycle
              if(Lp > MAX_L_SF)
25
                    start = MEM_CLASS+MAX_L_SF-(INT16)(1.5*Lp) +
  i sf*MAX L_SF+L_LPCLHD;
                    start = (start < 0?0: start);
30
                   }
              else
                   start = MEM_CLASS+i_sf*MAX_L_SF+L_LPCLHD;
              end = MEM_CLASS+(i_sf+1)*MAX_L_SF+L_LPCLHD;
35
              for(max=-MAXFLT, k=start; k<end; k++)
                    val = (buffer_cla[k] > 0.0 ? buffer_cla[k] : -buffer_cla[k]);
40
```

```
if(val > max)
                           max = val;
                     }
5
                        Update buffer of maxima
               buffer_max_cla[i_sf] = max;
               }
10
         /* ====== Calculate weighted tilt, weighted maximum, and =======*/
         /* ======= the parametric noise component is suppressed ======= */
15
         min_slope_wtilt = MAXFLT;
         sum slope_wtilt = 0.0;
20
         max_slope_wmax = -MAXFLT;
         sum_slope_wmax = 0.0;
         min_wtilt = MAXFLT;
         avg wtilt = 0.0;
         max wRp = -MAXFLT;
25
         avg_wRp = 0.0;
         for(i_sf = 0; i_sf < MAX_N_SF; i_sf++)
30
                        Find energy in pitch cycle
                if(Lp > MAX_L_SF)
35
                      start = MEM_CLASS+MAX_L_SF-(INT16)(2.0*Lp)+i_sf*MAX_L_SF;
                      start = (start < 0?0:start);
                 }
                else
                      start = MEM_CLASS+i_sf*MAX_L_SF;
 40
```

```
end = MEM_CLASS+(i_sf+1)*MAX_L_SF;
                  /*____*/
                            Update segment energy
5
                  E_{seg} = 0.0;
                  for(k=start; k<end; k++)
                         E scg += buffer_cla[k]*buffer_cla[k];
10
                  /*____*/
                            Normalize energy
15
                  E scg = 1.0+E_scg/(FLOAT64)(cnd-start+1);
                  /*-----*/
                         Update noise level, noise tilt, noise max
20
                 if (frm_count < 6 && buffer_refl0[(INT16)((FLOAT64)i_sf/2.0)] <
                                -0.4 && Rp < 0.50)
                         {
25
                                 Relaxed update with fast adaptation
                         E_{noi} = 0.80 * E_{noi} + 0.20 * E_{seg};
30
                         idx = (INT16)((FLOAT64)i_sf/2.0);
                         T noi = 0.75 * T noi + 0.25 * buffer_ref10 [idx];
                         M \text{ noi} = 0.75*M \text{ noi} + 0.25*buffer \max_{cla[i\_sf]};
                         R \text{ noi} = 0.75*R_{noi}+0.25*Rp;
35
                  else if(Vad == 0)
                                Stringent update with slow adaptation
40
```

```
E_{noi} = 0.999*E_{noi}+0.001*E_{seg};
                           idx = (INT16)((FLOAT64)i_sf/2.0);
                           T_{noi} = 0.990 * T_{noi} + 0.010 * buffer_refl0[idx];
                            M noi = 0.990*M_noi+0.010*buffer_max_cla[i_sf];
5
                            R_{noi} = 0.990*R_{noi}+0.010*Rp;
                           }
                                Calculate weighting factor
10
                    nsr = sqrt(E_noi/E_scg);
15
                              Limit noise suppression to 30dB.
                                nsr_lim=1-10^(-dB_lim/20)
                    /*
                             10dB: 0.684, 20dB: 0.900, 30dB: 0.968
20
                     nsr = (nsr > 0.968 ? 0.968 : nsr);
                           .___.*/
                     /* Update buffer of weighted tilt, weighted maximum, and */
                                  weighted pitch correlation
 25
                     for(k = 0; k < MAX_N_SF-1; k++)
                             buffer_wtilt[k] = buffer_wtilt[k+1];
 30
                              buffer_wmax[k] = buffer_wmax[k+1];
                              buffer_wRp[k] = buffer_wRp[k+1];
  35
                      /* Suppress noise component in tilt, maximum, and pitch */
                                     correlation
                      idx = (INT16)((FLOAT64)i_sf/2.0);
  40
```

```
buffer_wtilt[MAX_N_SF-1] = buffer_refl0[idx] - nsr*T_noi;
                 buffer_wmax[MAX_N_SF-1] = buffer_max_cla[i_sf] - nsr*M_noi;
                 buffer_wRp[MAX_N_SF-1] = Rp - nsr*R_noi;
5
                 /* Calculate slope of weighted tilt and weighted maximum */
                 /*-----*/
                 num_tilt = 0.0;
                 num_max = 0.0;
10
                 den = 0.0;
                 for (k = 0; k < MAX_N_SF; k++)
                        num_tilt += (FLOAT64)k*(buffer_wtilt[k]-buffer_wtilt[0]);
                        num_max += (FLOAT64)k*(buffer_wmax[k]-buffer_wmax[0]);
15
                        den += (FLOAT64)(k*k);
                        }
                  /*----*/
                      Update sum, max, min, avg, etc of weighted tilt,
20
                      weighted maximum, and weighted pitch correlation */
                  slope wtilt = num_tilt/(den+EPSI);
25
                  if(slope_wtilt < min_slope_wtilt)
                        min_slope_wtilt = slope_wtilt;
                  sum_slope_wtilt += slope_wtilt;
30
                  slope_wmax = num_max/(den+EPSI);
                  if(slope_wmax > max_slope_wmax)
                        max_slopc_wmax = slope_wmax;
                  sum slope_wmax += slope_wmax;
35
                        Update minimum and average of weighted tilt
                  /*_____*/
                  if(buffer_wtilt[MAX_N_SF-1] < min_wtilt)
40
```

```
min_wtilt = buffer_wtilt[MAX_N_SF-1];
               avg_wtilt += buffer_wtilt[MAX_N_SF-1];
               /*-----*/
               /* Update maximum and average of weighted pitch correlation */
5
               /*-----*/
               if(buffer\_wRp[MAX\_N\_SF-1] > max\_wRp)
                     max_wRp = buffer_wRp[MAX_N_SF-1];
10
                avg_wRp += buffer_wRp[MAX_N_SF-1];
15
              Normalize average weighted tilt and pitch correlation
                      /= (FLOAT64)MAX_N_SF;
          avg_wRp
20
                      /= (FLOAT64)MAX_N_SF;
          avg_wtilt
             */
                Update moving average of weighted spectral tilt
          /*____*/
 25
          MA_min_wtilt = 0.75*MA_min_wtilt + 0.25*min_wtilt;
                  Update moving average of pitch correlation
 30
           MA\_avg\_wRp = 0.75*MA\_avg\_wRp + 0.25*avg\_wRp;
           /*-----*/
 35
           /* The first reflection coefficient of the last subframe with
                  inverted sign for probability interpretation,
                                                 */
                      i.e. mapped to [-1:1]
           /*
  40
```

```
249
          P_w[1][4] = -buffer_wtilt[MAX_N_SF-1];
          /*_____*/
          /* Limit sum of slopes to min of -1.0, max of 1.0, mapped to [-1:1] */
          /* inverted sign for probability interpretation,
5
          /*____*/
          P w[1][0] = -sum_slope_wtilt;
          if(P_w[1][0] > 1.0)
10
                P_w[1][0] = 1.0;
          else if(P_w[1][0] < -1.0)
                P_w[1][0] = -1.0;
          P_w[1][0] = 1.0;
15
          /*____*/
          /* Limit sum of slopes to min of -0.5, max of 0.5, mapped to [-1:1] */
                 inverted sign for probability interpretation,
          /+_____*/
20
          P_w[1][1] = -min_slope_wtilt;
          if(P_w[1][1] > 0.5)
                P_w[1][1] = 0.5;
25
          else if(P_w[1][1] < -0.5)
                P_w[1][1] = -0.5;
          P_{w[1][1]} = 2;
                Limit sum of slopes to min of -10000, max of 10000,
30
                      mapped to [-1:1]
          P_w[1][2] = sum_slope_wmax;
35
          if(P_w[1][2] > 10000.0)
                P_w[1][2] = 10000.0;
          else if(P w[1][2] < -10000.0)
                P_{w[1][2]} = -10000.0;
          P_w[1][2] = 0.0001;
40
```

```
250
                   Limit sum of slopes to min of -1000, max of 1000,
                             mapped to [-1:1]
5
            P_{w[1][3]} = max_slope_wmax;
            if(P_w[1][3] > 1000.0)
                    P_w[1][3] = 1000.0;
            elsc if(P_w[1][3] < -1000.0)
                    P_{w[1][3]} = -1000.0;
10
            P w[1][3] *= 0.001;
            onstplsv = 0;
15
             /* Rewritten in order to output criterion that triggers onset */
             if(P_w[1][1] > 0.3409 \&\& P_w[1][2] > 0.025 \&\& P_w[1][4] > -0.075)
20
                      onstplsv = 1;
             clse if
(P_w[1][1] > 0.2273 && P_w[1][2] > 0.027 && P_w[1][4] > -0.075)
 25
                      onstplsv = 1;
              elsc if(P_w[1][1] > 0.1818 && P_w[1][2] > 0.031 && P_w[1][4] > -0.075)
                      onstplsv = 1;
 30
             else if(P_w[1][1] > 0.0909 && P_w[1][2] > 0.040 && P_w[1][4] > 0.075)
                      onstplsv = 1;
 35
              else if(P_w[1][1] > 0.0773 && P_w[1][2] > 0.060 && P_w[1][4] > 0.075)
                       onstplsv = 1;
              else if(P_w[1][1] > 0.0591 && P_w[1][2] > 0.080 && P_w[1][4] > 0.100)
  40
```

```
251
                    onstplsv = 1;
           clsc if(P_w[1][1] > 0.03636 && P_w[1][2] > 0.100 && P_w[1][4] > 0.100)
5
                    onstplsv = 1;
           else if(P_w[1][1] > 0.1818 && P_w[1][2] > 0.025 && P_w[1][3] > 0.100 &&
            P_w[1][4] > -0.075
10
                    onstplsv = 1;
            else if(P_w[1][1] > 0.1364 && P_w[0][1] > 0.1364 &&
                    P_{w[1][2]} > 0.015 && P_{w[0][2]} > 0.015 && P_{w[1][4]} > -0.075
15
                    {
                    onstplsv = 1;
            else if(P_w[1][1] > 0.1364 && P_w[1][3] > 0.120 && P_w[1][4] > 0.150)
                    onstplsv = 1;
20
            else if(P_w[1][1] > 0.2273 && P_w[1][3] > 0.070 && P_w[1][4] > 0.150)
                     onstplsv = 1;
25
         else if(P_w[1][1] > 0.2500 \&\& P_w[1][3] > 0.060 \&\& P_w[1][4] > 0.150)
                    {
                     onstplsv = 1;
            clsc if(P_w[1][1] > 0.4091 && P_w[1][3] > 0.030 && P_w[1][4] > 0.150)
30
                     onstplsv = 1;
         elsc if(P_w[1][1] > 0.1818 && P_w[0][3] > 0.30 && P_w[1][4] > 0.000)
35
                     onstplsv = 1;
             voiced = 0;
             if(avg_wRp >= 0.70)
 40
```

```
252
                    voiced = 1;
                    }
           clsc if(avg_wRp \ge 0.5 \&\& Lp_std < 0.10)
5
                    voiced = 1;
           else if(MA_avg_wRp \geq 0.6 && Lp_std \leq 0.10)
                    voiced = 1;
10
           elsc if(avg_wtilt < -0.50 && avg_wRp >= 0.2)
                     voiced = 1;
15
            else if(min_wtilt < -0.70 && avg_wRp >= 0.2)
                     voiced = 1;
                     }
            clsc if(avg_wtilt < -0.75)
20
                      voiced = 1;
             clsc if(MA_avg_wRp > 0.50 && max_wRp > 0.40 && min_wtilt < -0.40)
25
                      voiced = 1;
             elsc if(avg_wRp > 0.50 && min_wtilt < -0.35)
                      voiced = 1;
 30
             else if(avg_wRp > 0.50 && min_wtilt < -0.05)
                      voiced = 1;
                      }
 35
                                                               */
                                Offset handling
  40
```

```
clsc if(voiced_mem[3] == 1 && voiced_mem[2] == 1)
                 if(MA_avg_wRp > 0.55 \&\& MA_min_wtilt < -0.10 \&\& Lp_std < 0.05)
                        voiced = 1;
5
                 }
          if (Vad == 0)
                 {
10
                 voiced = 0;
                 }
           /+_----*/
           15
          frame class = 2;
20
                       Onset frame_classification
           if ((frame_class_mem[3] == 0 \parallel frame_class_mem[3] == 1 \parallel
                  frame_class_mcm[3] == 2) && voiced == 1)
25
                        framc_class = 3;
           elsc if (onstplsv == 1 && (frame_class_mem[3] != 5 ||
                        frame_class_mcm[3] != 6 || voiced != 1))
                                frame_class = 3;
30
                       Voiced frame_classification
           else if(onstplsv_mem[3] == 1 && voiced == 0)
35
                  frame_class = 5;
           else if(voiced == 1)
                  frame_class = 5;
           if(Vad == 0)
 40
```

```
frame_class = 0;
                      Update frame_class buffer
5
         for (k = 0; k < CLA\_MEM\_SIZE-1; k++)
                frame_class_mem[k] = frame_class_mem[k+1];
                onstplsv mem[k] = onstplsv_mem[k+1];
10
                voiced mcm[k] = voiced_mem[k+1];
                }
          frame_class_mem [CLA_MEM_SIZE-1] = frame_class;
          onstplsv mem [CLA_MEM_SIZE-1] = onstplsv;
15
          voiced mem [CLA_MEM_SIZE-1] = voiced;
          /*-----
          /* frame class 0: silence
          /* frame_class 1: noisc-like (CURRENTLY, NOT ACTIVATED)
20
          /* frame_class 2: unvoiced
          /* frame class 3: onset
          /* frame_class 4: plosive (CURRENTLY, NOT ACTIVATED)
          /* frame_class 5: non-stationary voiced
          /* frame_class 6: stationary voiced (CURRENTLY, NOT DISTINGUISHED */
25
                              FROM CLASS 5
30
          return frame_class;
35
    */
    /* FUNCTION: CLA_NoisUnvoiceDetect ().
```

```
/* PURPOSE. This function temporally detects noise-like unvoiced */
           speech to reset the delay for the LT preprocessing and */
           control the excitation weighting. The decision will be */
   /*
           finalized after pitch-preprocessing.
   /* INPUT ARGUMENTS:
          _ (FLOAT64 []) residu: input residual signal.
   /*
                                                         */
          _ (FLOAT64 []) sig:
                                input speech signal.
                                input frame size.
                     I frm:
          (INT16)
10 /*---
   /* OUTPUT ARGUMENTS:
          _(INT16 *) frame_class: frame_class frame.
   /* INPUT/OUTPUT ARGUMENTS:
                                               */
15 /*
   /* RETURN ARGUMENTS:
                     _ None.
20
   void CLA_NoisUnvoiceDetect (FLOAT64 residu[], FLOAT64 sig[], INT16 *uv_mode,
                              FLOAT64 *frm_erg, FLOAT64 *frm_sharp, INT16 1_frm)
25
            FLOAT64 P1 SHP, P2 R1, P3_ZC, P4_RE;
            FLOAT64 X, X1, X2, Max, M, val;
            INT16 i, N;
30
                         Calculate parameter 1 : P1_SHP
            X = 0:
            Max = 0;
35
            for (i = 0; i < l_frm; i++)
                    M = fabs(residu[i]);
                    X += M;
                    if (M > Max)
40
```

```
256
                             Max = M;
                    }
            P1\_SHP = X / (I\_frm*MAX(1.0, Max));
         (*frm_sharp) = P1_SHP;
5
                          Calculate parameter 2: P2_R1
10
            \label{local_dvector} dot\_dvector\ (sig+l\_frm/2,\ sig+l\_frm/2+1,\quad \&X,
                                                                        0, 1_frm/2-2);
                                                                &X2, 0, 1_frm/2-1);
             dot_dvector (sig+l_frm/2, sig+l_frm/2,
             P2_R1 = X / MAX(X2, 1.0);
15
                           Calculate parameter 3 : P3_ZC
             N = 0;
             for (i = 0; i < l_frm/2-1; i++)
20
                      if (sig[i+l_frm/2]*sig[i+l_frm/2+1] < 0)
                              N++;
                      }
25
             P3 ZC = (FLOAT64)N/(1_frm/2-1.0);
                            Calculate parameter 4 : P4_RE
 30
              dot_dvector (residu, residu, frm_erg, 0, l_frm/2-1);
              dot_dvector (residu+l_frm/2, residu+l_frm/2, frm_erg+1, 0, 1_frm/2-1);
 35
              X1 = frm_erg[1];
              X1 = sqrt(X1/MAX(1.0, X2));
              P4 RE = 1.0-MIN(1.0, X1);
                                                                     */
                              Make noise-like decision
  40
```

```
257
          N = 0;
5
          for (i = 0; i < l_frm; i++)
                {
                 if (fabs(sig[i]) < 0.1)
                       N++;
10
                }
          dot dvector (sig, sig, &val, 0, (INT32)(l_frm*0.75)-1);
          X2 = val / (l_frm*0.75);
          if (X2 < 1000.0 \parallel N*1.0/1_frm > 0.5)
                *uv_mode=0;
15
       if (P1 SHP>0.15) *uv_mode=1;
          if ((P2_R1 < 0.60) && (P1_SHP > 0.20))
                (*uv_modc) = 2;
          if ((P3_ZC > 0.40) && (P1_SHP > 0.18))
20
                (*uv_mode) = 2;
          if ((P4_RE < 0.40) && (P1_SHP > 0.20))
                (*uv_mode) = 2;
25
          return;
30
/* FUNCTION: CLA_Identify_InputSpeech ().
                                                      */
   /*____*/
   /* PURPOSE: This function identifys input speech as flat speech. */
          The decision needs at least two sentences to update
                                           */
40 /*
          the memory.
```

```
258
  /* INPUT ARGUMENTS :
        _(INT16) pp_mode: = mode 1 or mode 0
      _(FLOAT64) refl0 : first reflection coeff.
        _(FLOAT64) lsf0 : first lsf
        _(FLOAT64) ltp_g: past LTP gain.
        _(FLOAT64) lpc_g : current LPC gain.
        _(INT16) vad : vad decision.
10 /* OUTPUT ARGUMENTS:
         _(INT16 *) flat_deci: flat speech decision
                                                   */
   /* INPUT/OUTPUT ARGUMENTS :
                                           */
   /*
   /* RETURN ARGUMENTS:
                    _ None.
   20 void CLA_Identify_Input(INT16 pp_mode, FLOAT64 *pdcfq, FLOAT64 lsf0, FLOAT64 ltp_g,
               FLOAT64 *lpc_g, INT16 fix_rate, INT16 *flat_deci)
           {
         INT16
                   i;
 25
         FLOAT64 sub_refl[NP], x;
          LPC_pred2refl(pdcfq, sub_refl, NP);
  30
          (*lpc_g) = 1.0;
          for (i = 0; i < NP; i++)
           (*lpc_g) *= (1.0-sqr(sub_refl[i]));
   35
           (*lpc_g) = -10.0*log10((*lpc_g)+EPSI);
                             Vad counter
   40
```

```
if (fix rate==RATE0_8K)
                   Vad_0_Count++,
           clsc
5
                   Vad_0_Count = 0;
        if (pp_mode==1) {
          x=MIN((*lpc_g)/35.0, 1.0) - 2.0*sub_refl[0] + MIN(ltp_g, 1.0) - lsf0*15;
          x = 2.5;
          if (SVS_Count==0)
10
            Av_value = 0.75*x;
          clsc {
            if (SVS_Count<10)
               Av_value = 0.9*Av_value + 0.1*x;
15
            clsc {
                if (SVS_Count<100)
                 Av_value = 0.99*Av_value + 0.01*x;
                else Av_value = 0.998*Av_value + 0.002*x;
                }
20
             }
          SVS_Count++;
          SVS Count = MIN(SVS_Count, 10000);
        }
        else {
           if ((Av_value > 5.8) && ((*flat_deci) == 0) && (Vad_0_Count > 10)
25
            && (SVS_Count > 200))
                    (*flat_deci) = 1;
           if ((Av_value < 5.3) && ((*flat_deci) == 1) && (Vad_0_Count > 10)
30
            && (SVS_Count > 200))
                    (*flat_deci) = 0;
         }
35
            return;
40
```

```
260
    /* FUNCTION: CLA_Class_Correct ().
5 /*----*/
  /* PURPOSE: This function works for
          (1) improving voiced-unvoiced decision;
          (2) improving noise-like unvoiced decision;
  /*
           (3) detecting specific "onset";
  /+
          (4) making classification for pitch preprocessing; */
10 /*
           (5) improving vad;
   /*
           (6) detecting voied speech with high prediction gain */
              but noise-like HF excitation.
15 /* INPUT ARGUMENTS:
         _(INT16 *) framc_class_m: vad decision.
   /*
           class_m, Rp_sub[], NSR, **refl, frm_sharp,
   /*
           energy_m, energy, FrmResEng, lpcgain
20 /* OUTPUT ARGUMENTS:
          *OnSetFlag, *NoisyV_flag
    /* INPUT/OUTPUT ARGUMENTS:
          *class, *class_pp, *Vad, *VUV
    /* RETURN ARGUMENTS:
           None
    30 void CLA_Class_Correct(INT16 *frame_class, INT16 *frame_class_pp, INT16 *Vad,
                        INT16 *VUV, INT16 *OnSetFlag,
                        INT16 frame_class_m, FLOAT64 Rp_sub[], FLOAT64 NSR,
                         FLOAT64 **rcfl, FLOAT64 frm_sharp, INT16 smv_mode,FLOAT64 energy_m,
                         FLOAT64 energy, FLOAT64 FrmResEng, FLOAT64 lpcgain,
                          INT16 *NoisyV_flag)
  35
            FLOAT64 x, y;
  40
```

```
261
                                                             */
                             Class correction
5
                             unvoiced to voiced
         if (((*frame_class) < 3) && (frame_class_m > 2) &&
           (Rp\_sub[0] > 0.43) && (Rp\_sub[1] > 0.43))
10
                    if ((frin_sharp < 0.15) && ((Rp_sub[0] + Rp_sub[1]) > 0.9) &&
              (rcf[2][0] > -0.4))
                                     (*frame_class) = 5;
                     if ((frm_sharp < 0.25) && ((Rp_sub[0]+Rp_sub[1]) > 1.0) &&
15
              (ref[2][0] > -0.1))
                             (*frame_class) = 5;
                     if ((frm_sharp < 0.3) && ((Rp_sub[0]+Rp_sub[1]) > 1.15) &&
              (refl[2][0] > -0.1))
                             (*frame\_class) = 5;
20
                    }
                              voiced to unvoiced
25
             x = MIN(3*frm_sharp-0.2, 0.65);
             if ((*frame_class>2) && (Rp_m_cla<x) && (Rp_sub[0]<x) &&
                     (Rp_sub[1] < x) && (Rp_sub[2] < x))
30
                             (*framc_class) = 2;
                              noise-like unvoiced
35
             if ((*VUV==2) \&\& ((Rp_sub[0]>0.75) || (Rp_sub[1]>0.75)))
                     (*VUV) = 3;
             if ((*VUV==2) && (refl[2][0]>-0.1) &&
 40
```

```
((Rp\_sub[0]>0.55) || (Rp\_sub[1]>0.55)))
                            (*VUV) = 3;
            if ((*VUV==2) && (frm_sharp>0.2) && ((Rp_m_cla>0.75) \parallel (Rp_sub[2]>0.75)))
                             (*VUV) = 3;
5
            if ((*frame_class<3) && (*frame_class>0) &&
                     (((NSR < 0.25) && (Rp_sub[1]<0.7) && (frm_sharp>0.16)) \parallel
                        ((Rp_sub[1]<0.5) && ((*VUV)==2))))
                                     (*frame_class) = 1;
10
                                Onset detection
15
             x = \text{energy/MAX}(\text{energy}_m, 0.1);
             y = ref[0][0]-ref[N_SF4-1][0];
              (*OnSctFlag) = (*frame_class==3) && (y+0.2*x-0.6>0) && (Rp_sub[2]>0.5);
 20
                          Classification for pitch preprocessing
           if (*frame_class>=3)
 25
                        (*frame_class_pp) = 2;
                        if (Rp_sub[1] > 0.5)-
                                (*frame_class_pp) = 3;
                        if (Rp\_sub[1] > 0.75)
  30
                                (*frame_class_pp) = 4;
                        }
                else
                        (*framc_class_pp) = 0;
            if ((*VUV) == 0)
   35
                (*framc_class_pp) = 0;
            if ((frame_class_m < 3) &&
              ((*frame\_class < 2) \parallel (((*frame\_class) == 2) \&\& (Rp\_sub[1] < 0.5))))
                         (*framc_class_pp) = -1;
    40
```

```
263
                           Making the VAD more agressive
  5
          if (smv_mode == 2)
             { ,
                      if ((FrmResEng < 400*L_FRM) && (refl[2][0] < 0.125) &&
                              (Rp\_sub[1] < 0.55) && ((*frame\_class) < 3) &&
                                       (frm_sharp > 0.15))
. 10 ,
                                                (*frame\_class) = 0;
                                                (*Vad) = 0;
                                                }
                      }
 15
              else
                      if (((NSR > 0.6) \parallel (FrmResEng < 100*L_FRM)) &&
                                       (refl[2][0] < 0) && (Rp_sub[1] < 0.4) &&
                                                ((*frame_class) < 3) && (frm_sharp > 0.18))
 20
                                                (*frame_class) = 0;
                                                (*Vad) = 0;
                      }
 25
                     Voiced speech with high prediction gain but noise-like
                                HF excitation
 30
              x = 10*frm_sharp - 1.5*ref[2][0] + Rp_sub[1] + lpcgain/20.0;
              *NoisyV_flag = 0;
              if ((*frame\_class) > 2)
 35
                       if (x > 4.5)
                               (*NoisyV_flag) = 1;
                       if ((x > 5.7) && (frin_sharp > 0.29) && (refi[2][0] < -0.92))
                               (*NoisyV_flag) = 2;
  40
                      }
```

	$Rp_m_cla = Rp_sub[1];$	
	lpcg_m_cla = lpcgain;	
		•1
5 .	/*	*/
	4	•
	return;	•
	/*	······································
10	}	
	Α.	· •/
/*		
	· .	
/ * ==		*/
15 /* F	UNCTION: CLA_Raic_Select ().	*/
/*	UNCTION. OZ. I	*/
/* P	URPOSE: This function select the SMV bitrate.	·
		*/
/* I	NPUT ARGUMENTS :	, */
20 /*	class_m, Rp_sub[], NSR, **refl, frm_sharp,	, */
/*	energy_m, energy, FrmResEng, lpcgain	
	01141 D)	*/
/* (OUTPUT ARGUMENTS:	*/
/*	*OnSetFlag, *NoisyV_flag	*/
25 /*-	TOTAL CAR CARTES .	*/
/*	INPUT/OUTPUT ARGUMENTS:	*/
/*	*class, *class_pp, *Vad, *VUV	*/
-		*/
	RETURN ARGUMENTS:	
30 /*	_Nonc	*
/*		
	oid CLA_Ratc_Select(INT16 smv_mode, INT16 fra	ume_class, INT16 frame_class_m,
V	INT 16 onsetflag, FLOAT	64 RP[], FLOATO4 Nois, Polition 17
	FI OAT64 reflo, FLOAT	64 frm_eng, INT16 *codec_rate)
35		
	{ 	*/
	,	
	FLOAT64 x;	
	LFOUTAL W	
40		

```
265
           switch (smv_mode)
                  {
5
                        Mode 0. Note: average rate must be equal to EVRC
                   case 0: (*codec_rate) = RATE8_5K;
10
                                  if ((framc_class == 1) && (sharp > 0.2) &&
                                                  (Rp[1] < 0.32) && (Rp[2] < 0.3))
                                                         (*codec_rate) = RATE4_0K;
                                   if ((frame_class == 1) && (NSR > 0.15) &&
15
                                                  (Rp[1] < 0.5) && (Rp[2] < 0.5))
                                                          (*codec_rate) = RATE4_0K;
                                   if ((frame_class < 3) && (NSR > 0.5) && (refl0 < 0.0)
                                                          && (Rp[1] < 0.5)
20
                                                          (*codec_rate) = RATE4_0K;
                                   if (frame_class==0)
                                                  (*codec_rate) = RATE4_0K;
25
                                   if ((frame_class == 0) && (framc_class_m==0))
                                                  (*codec rate) = RATE0_8K;
                           break;
                    /*----*/
30
                    /* Mode 1. Note: average rate must be equal to 0.7 EVRC
                   case 1: (*codec_rate) = RATE8_5K;
                                   x = MAX(0.77-NSR, 0.7);
35
                                   if ((frame_class > 3) && (frame_class_m > 5) &&
                                                  (Rp[0] > x) && (Rp[1] > x))
                                                          (*codec_rate) = RATE4_0K;
                                   if ((frame_class == 2) && (Rp[0] > 0.31) &&
40
```

```
266
                                                     (Rp[1] > 0.31))
                                                             (*codec_rate) = RATE4_0K;
                                     if ((frame_class == 2) && (sharp > 0.18))
                                             (*codec_rate) = RATE4_0K;
5
                                     if ((frame_class == 2) && (NSR > 0.5))
                                             (*codec_rate) = RATE4_0K;
                                     if (frame_class == 1)
10
                                             (*codec_rate) = RATE4_0K;
                                     if ((framc_class == 1) && (Rp[0] < 0.5) && (Rp[1] < 0.5)
                                                       && (Rp[2] < 0.5) &&
                                                       (((refl0 > 0.0) && (sharp > 0.15)) ||
15
                                                                       (sharp > 0.25)))
                                                              (*codec_rate) = RATE2_0K;
                                      if ((NSR > 0.08) && (frame_class == 1) && (sharp > 0.15))
                                              (*codec_rate) = RATE2_0K;
20
                                      if (frame_class == 0)
                                              (*codec_rate) = RATE0_8K;
                              break;
 25
                      /* Mode 2. Note: average rate must be equal to 0.55 EVRC */
                      case 2: (*codec_rate) = RATE4_0K;
 30
                                       if ((OnSet == 1) && (NSR > 0.02) && (frame_class > 2))
                                               (*codcc_rate) = RATE8_5K;
                                        if ((OnSet == 1) && (Rp[0] < 0.85) && (frame_class > 2))
  35
                                                (*codec_rate) = RATE8_5K;
                                        if ((onsetflag == 1) \parallel ((frame_class_m < 3) &&
                                                (frame_class >= 3)) || (frame_class == 3))
                                                         OnSet = 1;
                                        else
   40
```

```
267
                                                     OnSet = 0;
                                     if (OnSet == 1)
                                             (*codec_rate) = RATE8_5K;
                                     if ((frame_class > 3) && (rcfl0 < -0.8) && (Rp[1] < 0.5)
5
                                                      && (sharp < 0.15))
                                                             (*codec_rate) = RATE8_5K;
                                     if ((NSR > 0.025) && (frame_class > 2) && (Rp[1] < 0.57))
                                                     (*codec_rate) = RATE8_5K;
10
                                     if (frame_class < 3)
                                                     (*codec_rate) = RATE4_0K;
                                     else
                                              if (frm_eng < 2500*L_FRM)
15
                                                     (*codec_rate) = RATE4_0K;
                                              if ((frm_cng < 5000*L_FRM) && (framc_class_m < 3)
                                                             && (Rp[1] < 0.6))
                                                     (*codec_rate) = RATE4_0K;
                                             }
20
                                     if ((frame_class == 1) && (Rp[0] < 0.5) && (Rp[1] < 0.5)
                                                      && (Rp[2] < 0.5) && (((refl0 > 0.0)) &&
                                                              (sharp > 0.15)) \parallel (sharp > 0.25)))
                                                                      (*codec_rate) = RATE2_0K;
25
                                     if ((NSR > 0.08) && (frame_class == 1) && (sharp > 0.15))
                                             (*codec_rate) = RATE2_0K;
                                     if (frame_class == 0)
30
                                             (*codec_rate) = RATE0_8K;
                             brcak;
                     default:
35
    #ifdef VERBOSE
                                      printf ("Invalid SMV mode: %d\n", smv_mode);
                                      exit(1);
    #endif
                             break;
40
```

	200
	}
	/**/
5	frame_class_mm = frame_class_m;
	/**/
	relum;
10	/**/
	}
1.5	/**/
15	/*====================================
	/**/

3NSDQCID: -WO____0122402A1 1 >

```
269
                   _____*/
                                     */
  /* Conexant System Inc.
5 /* 4311 Jamborcc Road
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  15 /* LIBRARY: lib_cla.h
  /*_____*/
  /*----*/
20 /*----*/
        CLA_NoisUnvoiceDetect (FLOAT64 [], FLOAT64[], INT16 *, FLOAT64 *,
  void
                                          FLOAT64 *, INT16);
                         (FLOAT64 *, FLOAT64, FLOAT64, INT16);
  INT16 CLA_signal_classifier
25
                              (INT16 *, INT16 *, INT16 *, INT16 *, INT16 *,
        CLA_Class_Correct
  void
                         INT16, FLOAT64 [], FLOAT64, FLOAT64 **,
                              FLOAT64, INT16, FLOAT64, FLOAT64,
                                    FLOAT64, FLOAT64, INT16 *);
30
                         (INT16, INT16, INT16, INT16, FLOAT64 [].
  void
        CLA_Rate_Sclect
                                    FLOAT64, FLOAT64, FLOAT64, FLOAT64, INT16 *);
       CLA_init_lib
                               (void);
35 void
        CLA Identify_Input (INT16, FLOAT64 *, FLOAT64, FLOAT64,
  void
                                                                 FLOAT64 *.
  INT16, INT16 *);
40
```

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INSDOCID: <WO 0122402A1,1, >

	271	·
	/*/	=======================================
	/* Conexant System Inc. */	
5	5 /* 4311 Jamboree Road */	
	/* Newport Beach, CA 92660 */	
	/**/	·
	/* Copyright(C) 2000 Conexant System Inc.	*/
	/ * */	
10	10 / ADD IGGITTS IGGERTED.	*/
	/* No part of this software may be reproduced in any form or	•
	/* means or used to make any derivative work (such as transfe	
	/* or adaptation) without the authorisation of Conexant Syste	
15	15 /* LIBRARY: lib_cpr.c */	
	/*=====================================	=======================================
	/*	
	/* INCLUDE	
20	20 /*	₹/
	#include "main.h"	
	#include main.it	
	/*	*/
25	25 /*FUNCTIONS	
23	/*	
	, 	•
	/*====================================	
	/* FUNCTION : print_copyright ().	•
30	30 /+*/	
20	/* PURPOSE : Print the Copyright Information.	*/
	/**/	
	/* ALGORITHM : */	
	/**/	
35	35 /* INPUT ARGUMENTS : */	•
,,,	/* None */	
	/**/	
	/* OUTPUT ARGUMENTS :	· */
	/* None */	
40	40 /**/	

```
272
   /* INPUT/OUTPUT ARGUMENTS:
                                               */
                _ None
   /* RETURN ARGUMENTS:
                                               */
5 /*
                 None.
                           print_copyright (void)
   void
10
            printf("\n /******************
                                                    */\n");
            printf("
                         SMV Speech Codec Floating Point Simulation *\n");
            printf(" /*
                                                    */\n");
            printf("
15
                     /* These executable and sources files are delivered \Lambda^n;
            printf("
                         under NDA and are SOLELY to be used for the */\n");
            printf("
                          purpose of the 3GPP SMV Collaboration Phase. *\n");
            printf("
                                                    */\n");
            printf("
                           Any other use is STRICTLY prohibited.
                                                                   */\n");
            printf("
20
                                                    */\n");
            printf("
                                                     */\n");
            printf("
                               Version 1.0, 30 June 2000
                                                              */\n");
            printf("
                                                     */n");
            printf("
                                                               */\n");
                          This executable, all sources, object
25
             printf("
                           and processed files should be deleted by
                                                                 */n");
             printf("
                                                                */\n");
                           the and of the collaboration phase or at
             printf("
                           any time upon the Conexant's request.
                                                                 */\n");
             printf("
                                                     */\n");
             printf("
                                 CONEXANT SYSTEMS Inc.
                                                                    */\n");
             printf("
 30
                                                     */\n");
             printf("
                         All Rights Reserved, November 1999 - June 2000 */\n");
             printf("
                                                     */\n");
             printf("
             printf("
 35
             rcturn;
 40
```

	}
	/**/
_	/*
	/**/
	/*

	/*/	274
	/*=====================================	
	/* Conexant System Inc.	*/
	/* 4311 Jamborec Road	*/
	/* Newport Beach, CA 92660	*/ */
	/* Copyright(C) 2000 Conexant System Inc.	*/
	/* ALL RIGHTS RESERVED:	*/
10	/* No part of this software may be reproduced in a	ny form or by any */
	/* means or used to make any derivative work (suc /* or adaptation) without the authorisation of Cond /*====================================	ch as transformation */ exant System Inc. */
15	/* PROTOYPE FILE: lib_cpr.h /*====================================	*/
20	/*FUNCTIONS /*	*/ */
25	/*END	==============+*/ */ ============================

		275	·
•	=======================================		
-	Conexant System Inc.	*/	:========*/
	4311 Jamboree Road	*/	,
	Newport Beach, CA 92660	*/	
	/ / / / / / / / / / / / / / / / / / /	*/	
-	Copyright(C) 2000 Conexant System Inc.	*/	
•	ALL RIGHTS RESERVED:	*/	
	No part of this software may be reproduced in	any form or by any */	
	means or used to make any derivative work (su		
	or adaptation) without the authorisation of Con		•
-	LIBRARY: lib_dia.c	*/	
,	:	•	•
#i	ifdef DIAG_SMV		
/*	·	*/	
	INCLUDE		
•			
#i	include "typedef.h"		,
#i	include "main.h"		
	include "const.h"		·
	include "ext_var.h"		
	<u>.</u>		
#	include "mcutil.h"		
	include "gputil.h"		
	- Spanning		
#1	include "lib_dia.h"		
	include "lib_swb.h"		
	include no_swo.ii		
/+	*	*/	
•	* FUNCTIONS		
/*	*		
/*			
			~*/

	NCTION : DIA_		276	*/		
/* PU	RPOSE : This fu	inction open the diag	nostics files	s. * /		
5 /* INI /*	PUT ARGUMENTS _ None.	:	*/	*/		
	TTPUT ARGUMENT		*/	*/	·	
	_ None.		*/ */	,		
/* IN	PUT/OUTPUT ARGU _ Nonc.		*/	*/		
/*	ETURN ARGUMENT		* _/	/ */		
15 /*	_ None.		*/			====*/
*	DIA_enc_open_files					
20						٠
		Encoder unquantize		*/ */		
25	fdia_vad = filc_e	open_wb ("vad.enc");	;			
23	/ *	Encoder quantized		*/		
30	fdia_sp_enc = fi	ile_open_wb ("sp_ou	t.enc");	•		
	/*			*/	_	
35	return;					
,,	/*}			*/		
/*				*/		
40						

	211	
	/*====================================	*/
_	/* PURPOSE : This function close the diagnostics files.	*/
3	/* INPUT ARGUMENTS : *	/
	/* _ None. */	
	/**/	•
	/* OUTPUT ARGUMENTS :	*/
10	/*None. */	
	/**/	
	/* INPUT/OUTPUT ARGUMENTS :	*/
	/* _ None. */	•
	/**/	
15	/* RETURN ARGUMENTS :	*/
	/* _ Nonc.	·
20	/* Encoder unquantized /* fclose (fdia_vad);	*/
	/+	*/
	/* Encoder quantized	*/
	/*	·*/
30		
	fclose (fdia_sp_enc);	
35	/+	+/
	return;	
	/*	*/
	}	
40		

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	27	78 +/
•		
/* FINCTIO	DN : DIA_dec_open_files ().	*/
/* PURPOS	E : This function open the diagnostics fi	iles. */
	RGUMENTS:	*/
/ *	_ None. */	*/
	r arguments :	*/
/*	Nonc. */	*/
	A CID CITY :	*/
· /*	OUTPUT ARGUMENTS : None. */	•/
	N ARGUMENTS :	*/
	*/	
/ /*======	_ Nonc.	
4	_dec_open_files (void)	*/
·	/+	
25	/*	*/
	return;	
	/*	*/
30)	
		*/
/*		
35 /* FUN	CTION : DIA_dec_close_files ().	*/ */
/* PUR	POSE : This function close the diagnost	tics files. */
	UT ARGUMENTS :	*/
40 /*	_ None. */	

		219 */	
	/*/* OUTPUT ARGUMENTS :	·*/ */ *	
	/* _ None. */	•	
	/*/	*/	
	/* INPUT/OUTPUT ARGUMENTS :		*/
	/* None. */		
	/*	*/	
	/* RETURN ARGUMENTS :	*/	
	/* _ None. */		
10	/+=====================================		***************************************
	void DIA_dec_close_files (void)		
	{		
	/*	********	· [∓] /
15	/*		41
	/*		· - -
	return;		
20	/*		*/
20	}		·
	,		
	/*		*/
25			
	/*=====================================		=======================================
	/* FUNCTION : DIA_trace_data ().	*/	•
	/*	*/	
	/* PURPOSE : Trace the value of the variable by writing	ing a file	*/
30	/* with data alined with the input signal.	*/	
	/*	*/	
	/* INPUT ARGUMENTS :	*/	
	/* _ (FLOAT64) val: variable to trace.	*/	
	/* _ (INT16) data_size: output data size.	*/	
35	- ·		
	/* _ (FILE *) file_data: output file name.	*/	
	/*		
	/* OUTPUT ARGUMENTS :	*/	
	/* _ None	*/	•
4()	/+	' /	

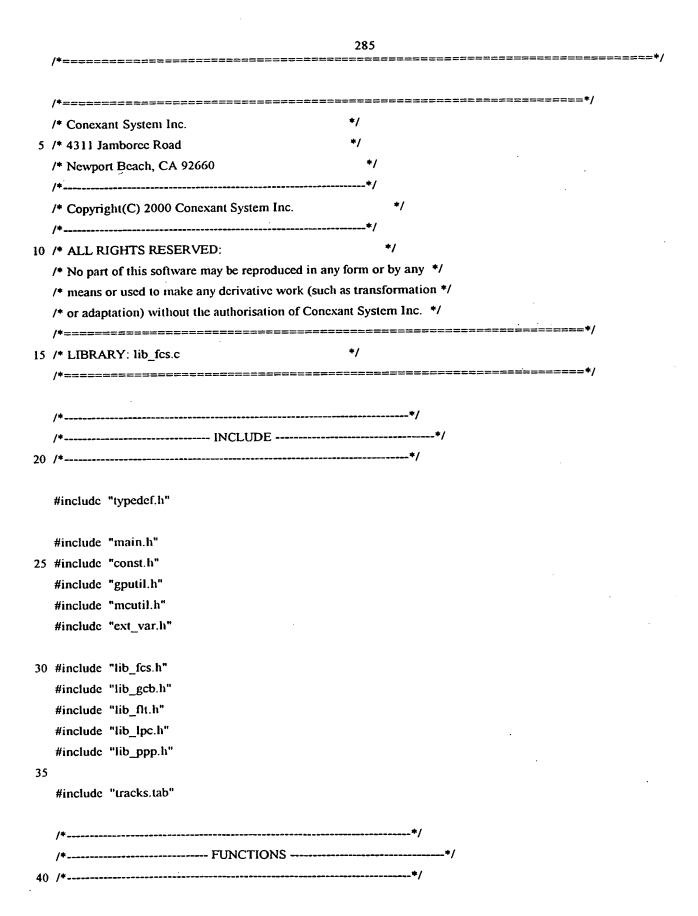
		280	
/* INPU	T/OUTPUT ARGUMENTS	S: */	
/*	_ None	*/	
		*/	
		*/	
	JRN ARGUMENTS :	*/	
5 /*	_ None.		===*/
/ * ====	=======================================	#=#=##################################	
			·
void	DIA trace data (FLOAT6	64 val, INT16 data_size, FLOAT64 scale_factor,	
Volu	D.1.1_0000_000	FILE *file_data)	
		_	
10	{	*********	
	/*	*/	
	`		
	INT16 *s_val;		
	<u>-</u>		
	n male; and commi		
15	INTI6 i, wt_samp;	·. *	
		· */	
		*/	
	/* Memo	nory Allocation */	
	/*	*/	
20	,		
20	t comme (O. doto)	cizo-1).	
	s_val = svector (0, data_	_5/20-1),	
		·	
	/*	*/	
25	$s_{val}[0] = (INT16)$ (sca	cale_factor * val);	
43	5_144 (4)		
		: +43	
	for (i = 1; i < data_size;		
	s_val {	$[i] = s_val[0];$	
30 #ifde	f BYTE_SWAP_OUTPUT	•	
50			
	for $(i = 0; i < data_size)$	o: i ++)	
		al[i] = byte_swap_int16(s_val[i]);	
	s_vai	dili = pare_zwah_mero(s_vart-1);	
35 #eno	lif		
	Comita la va	val, sizeof (INT16), data_size, file_data);	
	wt_samp = Iwrite (s_va	vai, siesor (in the), and	
		•	
40 #ifd	lef VERBOSE		

```
if (wt_samp != data_size)
                nrerror("Error Writing output Samples!");
  #endif
5
                         Memory Free
         free_svector (s_val, 0, data_size-1);
10
         return;
15
/* FUNCTION : DIA_trace_sig ().
  /* PURPOSE : Trace a signal by scaling it and writing a file
          with data alined with the input signal.
  /* INPUT ARGUMENTS:
         _ (FLOAT64 []) x:
                            signal to trace.
          _ (INT16 ) x_size:
                             output signal size.
          (FLOAT64 ) scale_factor: scale factor for val.
                            output file name.
          (FILE *) file_x:
  /* OUTPUT ARGUMENTS:
                                       */
35 /* INPUT/OUTPUT ARGUMENTS:
                                       */
              None
  /* RETURN ARGUMENTS:
              None.
```

```
DIA_trace_sig (FLOAT64 x [], INT16 x_size, FLOAT64 scale_factor,
   void
                                             FILE *file_x)
5
            INT16 *s_x;
            INT16 i, wt_samp;
10
                              Memory Allocation
            s_x = svector(0, x_size-1);
15
             for (i = 0; i < x_size; i ++)
                     s x [i] = (short) nint(scale_factor * x [i]);
20
    #ifdef BYTE_SWAP_OUTPUT
             for (i = 0; i < x_size; i ++)
                              s_x[i] = byte_swap_int16(s_x[i]);
25
    #endif
             wt_samp = fwrite (s_x, sizeof (INT16), x_size, file_x);
 30 #ifdef VERBOSE
             if (wt_samp != x_size)
                      nrerror("Error Writing output Samples!");
    #endif
 35
                                 Memory Free
              free_svector (s_x, 0, x_size-1);
 40
```

	283
	/**/
	return,
_	/+*/
5	,
	}
	/**/
10	#1:C
10	#endif
	/*====================================
	/**/
	/*====================================
15	

				284	*/	
•	/*====	p=====================================	:=========		•	
	/*		:::::::::::::::::::::::::::::::::::::::		=======================================	
		xant System Inc.		*/		
	/* 4311 Jamborec Road		*/			
		port Beach, CA 92660		. */		
				*/	•	
	/* Сору	right(C) 2000 Conexant S	ystem Inc.	*/		
	/*					
_		RIGHTS RESERVED:		*/		
		art of this software may be				
		* means or used to make any derivative work (such as transformation */ * or adaptation) without the authorisation of Conexant System Inc. */				
	/* ог ас	laptation) without the auth	orisation of Col	======================================	=======================================	
				*/		
15	/* PROTOYPE FILE: lib_dia.h */ /*==================================					
	/*====					
	/*			*/		
	•	**/				
20						
	void	DIA_enc_open_files	(void);			
	void	DIA_enc_close_files	(void);			
25	void	DIA_dec_open_files	(void);			
	void	DIA_dec_close_files	(void);			
		DIA_trace_data (FLOAT64, INT16, FLOAT64, FILE *);				
30	void	DIA_trace_data (PLOATO4, 18110, PLOATO4, PLDE),				
	void	DIA_trace_sig (FLOAT64 [], INT16, FLOAT64, FILE *);				
	void	DIA_ttace_sig (i boil	10 / [[] 21 / 1 - 1 /			
	/ * ===	:======================================	=======================================	=======================================		
3.	5 /*		END	*/		



```
/* FUNCTION : FCS_init_lib ().
  /+____+/
5 /* PURPOSE : This function performs the initialisation of the */
    global variables of the library FCS.
  /* INPUT ARGUMENTS:
       _ None.
10 /*-----
  /* OUTPUT ARGUMENTS:
                                   */
       _ None.
  /* INPUT/OUTPUT ARGUMENTS :
                                   */
       None.
15 /*
  /* RETURN ARGUMENTS:
                                   */
       _ None.
20
  void FCS_init_lib (void)
        {
         secd_bfi_exc = (INT64)21845;
25
                    = 0.0;
         lpcg_m
         alpha
                    = 0.0125;
30
                    = 20;
         lag_m
         SVS_flag_m = 0;
                 High frequency Noise filter initialisation
35
          FCS Init_HF_Noise (hh_hf, L_HF);
 40
```

	/* /*	Pulse code-books ini		*/	
	•			·	
	FCS_Init_	CPCB();			•
	/*		# + = = = = = = = = = = + + + + + + + +	*/	
	/*	13 bits	*/		
	/*			*/	
	Stab_13b_	enc = 0.0;			
	Stab_13b_	_dec = 0.0;		•	
	/*			+/	
	/*	15 bits	*/		• .
	/*			/	
	Stab_15b_	enc = 0.0;			
	Stab_15b_	_dec = 0.0;			
٠					
.	/		+	*/	
,	•				
	rcturn;				
••				/	
	}				
/*				+/	
•		FCS_Init_CPCB ().			
•		This function performs (of the */	
		This function performs to code-books.	ne initialisation */	or the '/	
	_		*/		
	JT ARGUM	ENTS:	*	1	
	_ None.		*/		
,		MENTS:		*/	

```
288
        _ None.
  /* INPUT/OUTPUT ARGUMENTS:
        None.
5 /*-----
  /* RETURN ARGUMENTS:
         None.
10 void FCS_Init_CPCB (void)
          INT16 n, i, ph_mode;
15
           20
           /* 2 pulses CB : 2pulses x 5bits/pulse + 2 signs = 12 bits
25
           for (i = 0; i < 2; i++)
                 for (n = 0; n < 32; n++)
                        p_{track_2_5_0[i][n]} = track_2_5_0[i][n];
30
           /* 2 pulses CB: 2 pulses x 7 bits/pulse + 1 sign = 15 bits
        for (i = 0; i < 2; i++)
 35
                  for (n = 0; n < 80; n++)
                         p_{track_2_7_1[i][n]} = track_2_7_1[i][n];
 40
```

```
289
         for (ph_mode = 0; ph_mode < 16; ph_mode++)
5
               for (i = 0; i < 3; i++)
                    for (n = 0; n < 4; n++)
                           p_{track_3_2_80[ph_mode][i][n]} = track_3_2_0[i][n] +
        track_1_4_0[0][ph_mode];
10
               for (i = 0; i < 3; i++)
                     for (n = 0; n < 4; n++)
                           p_{track_3_2_54[ph_mode][i][n] = track_3_2_0[i][n] +
15
        track_1_3_0[0][ph_mode];
         20
         for (i = 0; i < 5; i++)
              for (n = 0; n < 16; n++)
                     p_{\text{track}_5_4_0[i][n]} = \text{track}_5_4_0[i][n];
25
         for (i = 0; i < 5; i++)
               for (n = 0; n < 8; n++)
                     p_{track_5_3_1[i][n]} = track_5_3_1[i][n];
30
         for (i = 0; i < 5; i++)
               for (n = 0; n < 8; n++)
                     p_{track_5_3_2[i][n]} = track_5_3_2[i][n];
35
```

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10

290 for (i = 0; i < 8; i++)for (n = 0; n < 16; n++) $p_{track_8_4_0[i][n]} = track_8_4_0[i][n];$ return; } /* FUNCTION : FCS_Set_CPCB (). /* PURPOSE : This function performs the fixed codebook initialisation. */ 20 /*-----*/ /* INPUT ARGUMENTS: _(INT16) phas_mode: initial phase for the pulses. _(INT16) bit_p: number of bits. _(INT16) n_p: number of pulses. _(INT16) Lex: excitation length. 25 /* /* OUTPUT ARGUMENTS : None. 30 /* INPUT/OUTPUT ARGUMENTS: None. /* RETURN ARGUMENTS: _ None. void FCS_Set_CPCB (INT16 phas_mode, INT16 bit_p, INT16 n_p, INT16 Lex)

```
INT16 i;
5
       switch (n_p)
             10
             case 2:
             /*____*/
             /* 2 pulses CB : 2 pulses x 5 bits/pulse + 2 signs = 12 bits */
15
             /*____*/
             if (phas_mode == 0)
                  for (i = 0; i < 2; i++)
20
                       Maxldx[i] = 32;
                  track = p_track_2_5_0;
                  }
25
             /*_----*/
             /* 2 pulses CB: 2pulses x 6.5bits/pulse + 1 sign = 14 bits */
             /*____*/
           if (phas_mode == 1)
30
             {
          for (i = 0; i < 2; i++)
                       MaxIdx[i] = 80;
35
                  track = p_track_2_7_1;
             break;
40
```

```
case 3:
5
               if (Lex == L_SF)
                     {
                     /* 3 pulses CB: 3 pulses x 2 bits/pulse +
                                   3 signs = 9 bits */
10
                      for (i = 0; i < 3; i++)
                           MaxIdx[i] = 4;
15
                      track = p_track_3_2_80 [phas_mode];
                      }
                else
20
                      /* 3 pulses CB: 3 pulses x 2 bits/pulse +
                                         3 signs = 9 bits */
                      /* 3 pulses CB : 3 pulses x 2 bits/pulse + 3 signs */
 25
                                     + 3bits center = 11 bits */
                       /*
                       for (i = 0; i < 3; i++)
                             MaxIdx[i] = 4;
 30
                       track = p_track_3_2_54 [phas_mode];
                       }
                       break;
 35
```

```
case 5:
                      /*_____*/
                      /* 5 pulses CB : 3p \times 4b + 2p \times 3b + 3b \text{ signs} = 21 \text{ bits} */
5
                       if (phas_mode == 0)
                               for (i = 0; i < 5; i++)
                                       MaxIdx[i] = 16;
10
                               MaxIdx [2] = 8;
                               MaxIdx [4] = 8;
                               track = p_track_5_4_0;
15
                      /* 5 pulses CB : 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
                       if (phas_mode == 1)
20
                               for (i = 0; i < 5; i++)
                                       MaxIdx[i] = 8;
                               track = p_track_5_3_1;
25
                      /* 5 pulses CB : 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
30
                       if (phas_mode == 2)
                                for (i = 0; i < 5; i++)
                                        MaxIdx [i] = 8;
                               track = p_track_5_3_2;
35
                               }
                       break;
40
```

```
294
              case 8:
5
              /* 8 pulses CB : 6p \times 3b + 2p \times 4b + 4b \text{ signs} = 30 \text{ bits} */
               for (i = 0; i < 8; i++)
10
                    MaxIdx[i] = 8;
               MaxIdx[0] = 16;
               MaxIdx[4] = 16;
               track=p_track_8_4_0;
15
               break;
               default:
20
   #ifdef VERBOSE
                     nrerror(" Error in the definition of track[][] !!");
   #endif
                     break;
 25
 30
          return;
          }
 35
    /* FUNCTION : FCS_Init_HF_Noise ().
```

```
295
  /* PURPOSE : This function generated a impulse response
             introducing a random noise in the high
  /*
             frequencies.
 5 /* INPUT ARGUMENTS:
        _(INT16 ) l_sf: sub-frame size.
  /* OUTPUT ARGUMENTS:
        (FLOAT64 []) hh_hf: high frequency noise impulse
             response.
10 /*
  /* INPUT/OUTPUT ARGUMENTS:
           None.
15 /* RETURN ARGUMENTS:
           None.
  void FCS_Init_HF_Noise (FLOAT64 hh_hf[], INT16 l_hf)
20
          INT64 seed;
          FLOAT64
                       Z1=0, Z2=0;
          INT16 i;
25
          FLOAT64
                       x;
                        Initialisation
30
          sced = 56672;
          Z1
                = 0;
          Z2
                = 0:
35
          for (i = 0; i < l_hf; i++)
                hh_hf[i] = GCB_gauss_noisc(&seed);
          for (i = 0; i < l_h f; i++)
40
                 {
```

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```
296
                  x = hh_h[i] - Z1;
                  Z1 = hh_h[i];
                   hh_h[i] = x;
                          = hh_h[i] - Z2;
5
                   Z2
                          = hh_hf[i];
                   hh hf[i] = x;
                   }
           for (i = 0; i < l_hf; i++)
10
                   {
                   x = cos(i*Pl*1.0/l_hf);
                   hh_h[i] *= 0.03*(0.55 + 0.45*x);
15
            hh_h[0] = 1.0;
            return;
 20
 25
     /* FUNCTION : FCS_DetcrmPulsLoc ().
     /* PURPOSE : This function determines all pulse positions
                  and their magnitudes in terms of the pitch
      /*
                  function and the main pulse whose position
                                                     */
                  needs to be coded.
      /* INPUT ARGUMENTS:
             _(FLOAT64 ) PitGain: current pitch gain.
             _(INT16 ) pitch: current pitch value.
             _(FLOAT64 ) Mag:
                                      current pulse mgnitude.
              _(INT16 ) Loc: current pulse position.
   40 /*
```

```
297
         _(INT16 ) l_sf: sub-frame length.
  /* OUTPUT ARGUMENTS:
         _(FLOAT64 []) MagV: array of pulse magnitudes. */
  /*
         _(INT16 []) LocV: array of locations magnitudes.*/
5 /*
         _(INT16 *) NumPit: number of pulses.
  /*
  /* INPUT/OUTPUT ARGUMENTS:
                                            */
         _ None.
10 /*-----
  /* RETURN ARGUMENTS:
         None.
15 void FCS_DetermPulsLoc (FLOAT64 PitGain, INT16 pitch, FLOAT64 Mag, INT16 Loc,
                                         FLOAT64 MagV[], INT16 LocV[], INT16 *NumPit,
                                                INT161 sf)
20
           INT16 i, N, M, max_i;
25
           MagV[0] = Mag;
           LocV[0] = Loc;
           N = 0;
           if (PitGain < 0.4)
                  max_i = 3;
30
           elsc
                  max_i = 4;
           for (i = 1; i < \max_{i} i; i++)
                   if ((Loc + pitch * i) < l_sf)
35
                           MagV[i] = MagV[i-1]*PitGain;
                           LocV[i] = Loc + pitch*i;
                           N = i;
                          }
40
```

```
elsc
                           break;
                   }
5
            M = N;
10
            if (((Loc-pitch) \geq 0) && ((N+1) < max_i))
                    MagV[1+N] = MagV[0]*PitGain;
                     LocV [1+N] = Loc-pitch;
                     M = N+1;
15
                     for (i = 2; i < \max_{i} N; i++)
                             if ((Loc - pitch*i) \geq 0)
 20
                                      MagV[i+N]=MagV[i+N-1]*PitGain;
                                      LocV[i+N]=Loc-pitch*i;
                                      M = N+i;
                                      }
                               clse
 25
                                      break;
                               }
                      }
  30
               (*NumPit) = M+1;
  35
               return;
   40
```

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```
299
  /* FUNCTION : FCS_Onc_Search_CPCB ().
                                             */
/* PURPOSE : This function determines the position of one
           pulse using the forward pitch enhancement.
  /* INPUT ARGUMENTS:
                                        */
10 /* _ (INT16 ) n_p: number of pulses.
       _(INT16 []) Idx: array of idexes.
       _(FLOAT64 []) Mag: array of current pulse mgnitude.*/
       _(FLOAT64 []) rcf_sig: target signal.
       _(INT16 []) IdxOpt: optimized index array.
       _(INT16 []) PosOpt: optimized pulse postion array. */
15 /*
  /+____+/
  /* OUTPUT ARGUMENTS:
       _(FLOAT64 *) Rn_m: maximized criterion numerator. */
     _(INT16 *) Rd_m: maximized criterion denominator.*/
20 /*----*/
  /* INPUT/OUTPUT ARGUMENTS:
  /* (FLOAT64 *) MaxCrit: maximized criterion.
  /* RETURN ARGUMENTS:
25 /* None.
  void FCS_One_Search_CPCB(INT16 n_p, INT16 Idx [], FLOAT64 Mag [],
                               FLOAT64 rcf_sig [], INT16 IdxOpt [],
                                     INT16 PosOpt [], FLOAT64 *MaxCrit,
30
                                                 FLOAT64 *Rn_m, FLOAT64 *Rd_m)
               */----*/
         INT16 n, k, Pos[MAXPN];
35
         FLOAT64
                    GG, Rn, Rd;
         for (n = 0; n < n_p; n++)
40
```

```
300
                      Pos[n] = track[n][Idx[n]];
              Rn = 0;
              for (n = 0; n < n_p; n \leftrightarrow)
  5
                      Rn += rcf_sig[Pos[n]]*Mag[n];
              Rd=0;
 10
              for (n = 0; n < n_p-1; n++)
                      for (k = n+1; k < n_p; k++)
                               Rd += Mag[n]*PHI[Pos[n]][Pos[k]]*Mag[k];
              Rd = 2;
 15
             for (n = 0; n < n_p; n++)
                     Rd += Mag[n]*PHI[Pos[n]][Pos[n]]*Mag[n];
20
             GG = Rn*Rn / MAX(0.1,Rd);
             if (GG > (*MaxCrit))
                      {
                      (*MaxCrit) = GG;
25
                      for (n = 0; n < n_p; n++)
                              IdxOpt[n] = Idx[n];
                              PosOpt[n] = Pos[n];
30
                      (*Rn_m) = Rn;
                      (*Rd_in) = Rd;
35
             rcturn;
40
```

```
301
  /* FUNCTION : FCS_One_Search_4kCB ().
                                                     */
 5 /*-----*/
  /* PURPOSE : This function determines the position of one
  /*
             pulse using the forward + backward pitch
             enhancement.
10 /* INPUT ARGUMENTS:
        _(INT16 []) SignTab: array of pulse signs.
        (FLOAT64) pit_g: current pitch gain.
        (INT16 ) lag: current pitch value.
  /*
        _(INT16 ) n_p: number of pulses.
        (INT16 ) no_p: pulse number.
15 /*
        _(INT16 []) Idx: array of idexes.
        (FLOAT64 []) Mag: array of current pulse mgnitude.*/
        (FLOAT64 []) ref sig: target signal.
         (INT16 ) I sf: sub-frame size.
  /* OUTPUT ARGUMENTS:
        _(INT16 []) IdxOpt: optimized index array.
        _(INT16 []) PosOpt: optimized pulse postion array. */
        _(FLOAT64 *) Rn_m: maximized criterion numerator. */
25 /*
         _(INT16 *) Rd_m: maximized criterion denominator.*/
  /* INPUT/OUTPUT ARGUMENTS:
         _(FLOAT64 *) MaxCrit: maximized criterion.
30 /* RETURN ARGUMENTS:
         None.
  void FCS_One_Search_4kCB (INT16 SignTab[], INT16 lag, FLOAT64 pit_g, INT16 no_p,
                              INT16 n_p, INT16 Idx [], FLOAT64 Mag [],
35
                                     FLOAT64 ref_sig [], INT16 IdxOpt [],
                                           INT16 PosOpt [], FLOAT64 *MaxCrit,
                                                 FLOAT64 *Rn_m, FLOAT64 *Rd_m, INT16 1_sf)
         {
40
```

בי בבטטור זווט מיסטנים

INT16 n, k, Pos[MAXPN], Num, NumP, LocP[MAX_FB_PULSE_NUM*MAXPN];

```
FLOAT64 GG, Rn, Rd, Rd1, MagP[MAX_FB_PULSE_NUM*MAXPN];
 5
                          Determine pulses positions
            NumP = 0;
10
            for (n = 0; n < n_p; n++)
                    Pos[n] = track[n][Idx[n]];
                    FCS_DetermPulsLoc (pit_g, lag, (Mag[n]*SignTab[Pos[n]]),
                                                          Pos[n], MagP+NumP, LocP+NumP, &Num, 1 sf);
15
                   NumP += Num;
                             Correlation
20
          Rn = 0;
            for (n = 0; n < NumP; n++)
                   Rn += ref_sig[LocP[n]]*MagP[n];
25
                                                       */
                              Energy
30
            Rd = 0;
          . for (n = 0; n < NumP-1; n++)
                   Rd1 = 0.0;
                   for (k = n+1; k < NumP; k++)
35
                           Rd1 += PHI[LocP[n]][LocP[k]]*MagP[k];
                   RdI *= MagP[n];
                   Rd += Rd1;
                   }
40
```

```
Rd *= 2;
         for (n = 0; n < NumP; n++)
               Rd += MagP[n]*PHI[LocP[n]][LocP[n]]*MagP[n];
5
                  Criterion calculation and maximisation
         GG = Rn*Rn / MAX(0.1, Rd);
10
         if (GG > (*MaxCrit))
                (*MaxCrit) = GG;
                IdxOpt [no_p] = Idx[no_p];
15
                PosOpt [no_p] = Pos[no_p];
                (*Rn_m) = Rn;
                (*Rd_m) = Rd;
20
          return;
25
   /* FUNCTION : FCS_Full_Search_CPCB ().
35 /* PURPOSE : This function determines the pulses position
           using forward pitch enhancement.
                                               */
   /*
   /* INPUT ARGUMENTS :
         (INT16 ) phas_mode: initial phase for the pulses. */
         _(FLOAT64 ) pmag: pulse magnitude.
```

```
304
  /*
         _(INT16 ) n_p:
                           number of pulses.
         _ (INT16 []) SignTab: array of pulse signs.
         _ (FLOAT64 []) ref_sig: target signal.
                           excitation size.
         (INT16 ) Lex:
  /* OUTPUT ARGUMENTS:
         _(INT16 []) SignOpt: optimized array of pulse signs. */
         _(FLOAT64 []) EXopt: optimized excitation signal. */
         _(INT16 []) SignOpt: optimized array of indexes.
                                                      */
  /* INPUT/OUTPUT ARGUMENTS:
         _(FLOAT64 *) Criterion: maximized criterion.
  /* RETURN ARGUMENTS:
15 /*
                   Nonc.
   void FCS_Full_Search_CPCB (INT16 phas_mode, INT16 SignOpt[], FLOAT64 pmag,
                                      INT16 bits_p, INT16 n_p, INT16 SignTab[],
                                             FLOAT64 ref_sig[], FLOAT64 *Criterion,
20
                                                    FLOAT64 EXopt[], INT16 Lex,
                                                           INT16 IndexOpt[])
25
          INT16 k, Idx[MAXPN], IdxOpt[MAXPN], PosOpt[MAXPN];
                        GG, Rn m, Rd_m, MaxCrit, Mag[MAXPN];
          FLOAT64
           30
                        Set-up the track positions
35
           FCS Set_CPCB (phas_mode, bits_p, n_p, Lex);
                         Magnitudes of pulses
40
```

```
305
          GG = 1.0;
          for (k = 0; k < n_p; k++)
5
                               GG;
                 Mag[k] =
                 GG
                                      pmag;
10
          MaxCrit = -1;
15
          switch (n_p)
                 {
20
                               2 pulses CB
                  case 2:
                        for (Idx[0] = 0; Idx[0] < MaxIdx[0]; Idx[0]++)
25
                               for (Idx[1] = 0; Idx[1] < MaxIdx[1]; Idx[1]++)
                                      FCS One_Scarch_CPCB (n_p, Idx, Mag, ref_sig, IdxOpt,
                                                                          PosOpt, &MaxCrit,
   &Rn_m, &Rd_m);
30
                  break;
                                3 pulses CB
35
                  case 3:
                        for (Idx[0] = 0; Idx[0] < MaxIdx[0]; Idx[0]++)
                                for (Idx[1] = 0; Idx[1] < MaxIdx[1]; Idx[1]++)
                                       for (Idx[2] = 0; Idx[2] < MaxIdx[2]; Idx[2]++)
                                              {
40
```

```
306
                                                      FCS_One_Search_CPCB(n_p, ldx, Mag, ref_sig,
                                                                                                        IdxOpt,
   PosOpt,
5
            &MaxCrit, &Rn_m,
            &Rd_m);
                                                      }
                            break;
10
                    default:
   #ifdef VERBOSE
                     nrerror("Invalid number of pulses !! ");
15
   #endif
                     break;
20 .
                                Excitation
25
             if (MaxCrit > (*Criterion))
                      (*Criterion) = MaxCrit;
30
                      for (k = 0; k < n_p; k++)
                             SignOpt [k] = SignTab [PosOpt[k]];
                      for (k = 0; k < n_p; k++)
                             IndexOpt[k] = IdxOpt[k];
35
                      ini_dvector(EXopt, 0, Lex-1, 0.0);
                      for (k = 0; k < n_p; k++)
                             EXopt[PosOpt[k]] += SignOpt[k]*Mag[k];
 40
```

MRUUCIU: <MU TUSSAUSATEL >

307 } 5 return; 10 /* FUNCTION : FCS Simp_Scarch_CPCB (). /* PURPOSE : This function determines the pulses position using a simplified algorithm and the forward pitch enhancement. 20 /* INPUT ARGUMENTS: _(INT16) turn0: first turn number. /* _(INT16) turn1: last turn number. /* _(INT16) phas_mode: initial phase for the pulses. */ _ (FLOAT64) pmag: pulse magnitude. number of pulses. _ (INT16) n_p: 25 /* _ (INT16 []) SignTab: array of pulse signs. (FLOAT64 []) ref_sig: target signal. _(INT16) Lex: excitation size. 30 /* OUTPUT ARGUMENTS: (INT16 []) SignOpt: optimized array of pulse signs. */ /* _ (FLOAT64 []) EXopt: optimized excitation signal. */ _ (INT16 · []) IndexOpt: optimized array of indexes. 35 /* INPUT/OUTPUT ARGUMENTS: _(FLOAT64 *) Criterion: maximized criterion. /* RETURN ARGUMENTS: */

Nonc.

```
void FCS_Simp_Search_CPCB (INT16 turn0, INT16 turn1, INT16 phas_mode,
                            INT16 SignOpt[], FLOAT64 pmag, INT16 bits_p, INT16 n_p,
                                   INT16 SignTab[], FLOAT64 rcf_sig[],
                                         FLOAT64 *Criterion, FLOAT64 EXopt[], INT16 Lex,
5
                                                      INT16 IndexOpt[])
         {
10
         INT16 n, flag;
         INT16 k0, k1, k, i, m, POS, Idx[MAXPN], IdxOpt[MAXPN], PosOpt[MAXPN];
         FLOAT64 GG, Rn_m, Rd_m, MaxCrit, Mag[MAXPN];
          /*_____*/
          15
                      Set-up the track positions
20
          FCS_Sct_CPCB (phas_mode, bits_p, n_p, Lex);
                      Magnitudes of pulses
25
          GG = 1.0;
          for (k = 0; k < n_p; k++)
                {
30
                Mag[k] =
                             GG;
                 GG
                                   pmag;
                }
35
                       Reset for signed pulses
          if (turn0 == 0)
                {
40
```

```
309
                     for (k = 0; k < n_p; k++)
                                     Idx [k] = 0;
 5
                     for (k = 0; k < n_p; k++)
                             MaxCrit = -32767*32767;
10
                             for (i = 0; i < MaxIdx[k]; i++)
                                      flag
                                             = 1;
                                      POS = track[k][i];
                                      for (n = 0; n < k; n++)
15
                                              if (POS == PosOpt[n])
                                                     flag = 0;
                                      if ((rcf_sig[POS] > MaxCrit) && (flag == 1))
                                              MaxCrit = rcf_sig[POS];
20
                                              Idx[k] = i;
                                              PosOpt[k]=POS;
                                      }
25
                             IdxOpt[k] = IndexOpt[k];
30
             clse
                     for (k = 0; k < n_p; k++)
35
                              Idx [k] = IndexOpt[k];
                              IdxOpt[k]=IndexOpt[k];
                              PosOpt[k]=track[k][Idx[k]];
                             }
40
```

```
310
          if (((n_p == 5) || (n_p == 8)) && (turn 0 > 0))
5
                              5 and 8 pulses
10
                 if (n_p == 5)
                        {
                                                         */
                                     5 pulses
15
                         MaxCrit = -1;
                         k = 0;
                         while (k < n_p)
                               {
20
                                k0 = k;
                                k1 = k+1;
                                if (kl == n_p)
                                       k1 = 0;
25
                                for (Idx[k0] = 0; Idx[k0] < MaxIdx[k0]; Idx[k0]++)
                                       for (Idx[k1] = 0; Idx[k1] < MaxIdx[k1]; Idx[k1]++)
                                        FCS_One_Search_CPCB(n_p, Idx, Mag, ref_sig,
                                              IdxOpt, PosOpt, &MaxCrit, &Rn_m, &Rd_m);
 30
                                 Idx[k0]=IdxOpt[k0];
                                 Idx[k1]=IdxOpt[k1];
                                 if (k == n_p-1)
                                        k = 1;
                                 else
 35
                                        k += 2;
                                 }
                          }
                   if(n_p == 8)
 40
```

```
311
                            {
                                                                   */
                                            8 pulses
 5
                             MaxCrit = -1;
                             k = 0;
                             while (k < MAXPN)
                                     k0 = srchpuls[k];
10
                                     k1 = srchpuls[k+1];
                                     for (Idx[k0] = 0, Idx[k0] < MaxIdx[k0], Idx[k0]++)
                                             for (Idx[k1] = 0; Idx[k1] < MaxIdx[k1]; Idx[k1]++)
                                             FCS_One_Search_CPCB(n_p, Idx, Mag, ref_sig,
15
                                                      IdxOpt, PosOpt, &MaxCrit, &Rn_m, &Rd_m);
                                      Idx[k0] = IdxOpt[k0];
                                      Idx[k1] = IdxOpt[k1];
20
                                     k += 2;
                             }
          • }
25
           else
                                 Others pulse configuration
30
                     for (m = turn0; m \le turn1; m++)
                             for (k = 0; k < n_p; k++)
                                     {
                                      MaxCrit=-1;
35
                                      for (Idx[k] = 0; Idx[k] < MaxIdx[k]; Idx[k]++)
                                               flag = 1;
                                               POS = track[k][Idx[k]];
40
```

```
312
                                        for (i = 0; i < k; i++)
                                               if (POS == PosOpt[i])
                                                      flag = 0;
                                               }
5
                                        for (i = k+1; i < n_p; i++)
                                               if (POS == PosOpt[i])
                                                      flag = 0;
                                               }
10
                                        if (flag == 1)
                                               FCS_One_Scarch_CPCB(n_p, Idx, Mag, ref_sig,
                                                      IdxOpt, PosOpt, &MaxCrit, &Rn_m, &Rd_m);
                                        }
                                 Idx[k] = IdxOpt[k];
15
                         }
20
            25
           if (MaxCrit > (*Criterion))
                   (*Criterion) = MaxCrit;
 30
                   for (k = 0; k < n_p; k++)
                          SignOpt[k] = SignTab[PosOpt[k]];
                   for (k = 0; k < n_p; k++)
                          IndexOpt[k] = IdxOpt[k];
 35
                    ini dvector(EXopt, 0, Lex-1, 0.0);
                    for (k = 0; k < n_p; k++)
                           EXopt[PosOpt[k]] += SignOpt[k]*Mag[k];
  40
```

313 } 5 return; /* FUNCTION : FCS_Simp_Scarch_4kCB (). 15 /* PURPOSE : This function determines the pulses position using a simplified algorithm and the forward + */ backward pitch enhancement. /* INPUT ARGUMENTS: _(INT16) turn0: first turn number. 20 /* */ _(INT16) turn1: last turn number. _(INT16) phas_mode: initial phase for the pulses. */ _(FLOAT64) pmag: pulse magnitude. _(INT16) bits_p: bits per pulse. _(INT16) n_p: number of pulses. _(INT16 []) SignTab: array of pulse signs. _(FLOAT64 []) ref_sig: target signal. _(INT16) Lex: excitation size. _(INT16) lag: current pitch lag. 30 /* _(FLOAT64) pit_g: pitch gain. /* OUTPUT ARGUMENTS: __(INT16 []) SignOpt: optimized array of pulse signs. */ (FLOAT64 []) EXopt: optimized excitation signal. */ _(INT16 []) IndexOpt: optimized idex array. 35 /* /*______ */ /* INPUT/OUTPUT ARGUMENTS: _(FLOAT64 *) Criterion: maximized criterion.

40 /* RETURN ARGUMENTS:

```
None.
   void FCS_Simp_Search_4kCB (INT16 turn0, INT16 turn1, INT16 phas mode,
 5
                         INT16 SignOpt[], FLOAT64 pmag, INT16 bits p, INT16 n p,
                         INT16 SignTab[], FLOAT64 ref sig[],
                         FLOAT64 *Criterion, FLOAT64 EXopt[],
                         INT16 Lex, INT16 IndexOpt[], INT16 lag,
                         FLOAT64 pit_g)
10
          INT16 flag;
          INT16 k, i, m, POS, Idx[MAXPN], IdxOpt[MAXPN], PosOpt[MAXPN], LocV[6];
                        GG, Rn_m, Rd_m, MaxCrit, Mag[MAXPN], MagV[6];
15
          FLOAT64
          FLOAT64 x;
          20
                       Sct-up the track positions
25
         FCS_Set_CPCB (phas_mode, bits_p, n_p, Lex);
                        Magnitudes of pulses
30
          GG = 1.0;
          for (k = 0; k < n_p; k++)
35
                 Mag[k] =
                               GG;
                 GG
                                     pmag;
                        Reset for signed pulses
40
```

```
315
          if (turn0 == 0)
5
                 for (k = 0; k < n_p; k++)
                        {
                        POS = track[k][0];
                        MaxCrit = ref_sig[POS];
10
                        Idx[k] = 0;
                        for (i = 1; i < MaxIdx[k]; i++)
                                POS = track[k][i];
15
                                x = fabs(ref_sig[POS]);
                                if (x > MaxCrit)
                                       {
20
                                       MaxCrit = x;
                                       ldx[k] = i;
                                       PosOpt[k] = POS;
                               }
25
                        IdxOpt[k]=Idx[k];
                        }
30
          else
                 for (k = 0; k < n_p; k++)
                         Idx[k] = IndexOpt[k];
                         IdxOpt[k]=IndexOpt[k];
35
                         PosOpt[k]=track[k][ldx[k]];
                        40
```

```
316
             for (m = turn0; m \le turn1; m++)
  5
                     for (k = 0; k < n_p; k++)
                              MaxCrit = -1;
                              for (Idx[k] = 0; Idx[k] < MaxIdx[k]; Idx[k]++)
10
                                      flag = 1;
                                      POS = track[k][Idx[k]];
                                      for (i = 0; i < k; i++)
                                              {
15
                                              if (POS == PosOpt[i])
                                                      flag = 0;
                                              }
20
                                      for (i = k+1; i < n_p; i++)
                                              if (POS == PosOpt[i])
                                                     flag = 0;
                                              }
25
                                      if (flag == 1)
                                             FCS_One_Search_4kCB(SignTab, lag, pit_g, k, n_p,
                                                                      Idx, Mag, ref_sig, IdxOpt, PosOpt,
                                                                              &MaxCrit, &Rn_m, &Rd_m,
   Lex);
30
                             Idx[k] = IdxOpt[k];
                    }
35
                ========= Criterion calculation and maximisation ==========*/
            if (MaxCrit > (*Criterion))
40
```

```
317
                   {
                    (*Criterion) = MaxCrit;
                    for (k = 0; k < n_p; k++)
                                   SignOpt[k] = SignTab[PosOpt[k]];
5
                    for (k = 0; k < n_p; k++)
                                   IndexOpt[k] = IdxOpt[k];
                    ini dvector(EXopt, 0, Lex-1, 0.0);
                    for (k = 0; k < n_p; k++)
10
                           {
                            FCS DetermPulsLoc (pit_g, lag,Mag[k], PosOpt[k], MagV,
                                                                    LocV, &m, Lcx);
                            for (i = 0; i < m; i++)
                                   EXopt[LocV[i]] += MagV[i]*SignOpt[k];
15
                           }
                   }
20
            return;
25
                                                             */
   /* FUNCTION : FCS_Decod_CPCB ().
   /* PURPOSE : This function decode the fixed codebook.
   /* INPUT ARGUMENTS:
          _ (INT16 ) phas_mode: initial phase for the pulses.
          _(INT16 []) Sign:
                                array of pulse signs.
35 /*
          _(FLOAT64 ) pmag:
                                   magnitude of pulses.
          _(FLOAT64 ) bits_p: number of bits per pulse.
          _(INT16 ) n_p:
                               number of pulses.
          _(INT16 []) Index: optimized array of indexes.
   /*
           (INT16 ) Lex:
                                excitation size.
40 /*
```

```
318
   /* OUTPUT ARGUMENTS:
         _(FLOAT64 []) EXopt: optimized excitation signal.
 5 /* INPUT/OUTPUT ARGUMENTS:
                                        */
   /* RETURN ARGUMENTS:
void FCS_Decod_CPCB (INT16 phas_mode, INT16 Sign[], FLOAT64 pmag, INT16 bits_p,
                        INT16 n_p, FLOAT64 EXopt[], INT16 Lex, INT16 Index[])
          {
15
           INT16 n, k, Pos[MAXPN];
           FLOAT64 GG;
 20
                        Sct-up the track positions
           FCS Set_CPCB (phas_mode, bits_p, n_p, Lex);
 25
           for (n = 0; n < n_p; n++)
                 Pos[n] = track[n][Index[n]];
. 30
                                                     */
                     Calculate the excitation signal
           ini_dvector(EXopt, 0, Lex-1, 0.0);
 35
            GG = 1.0;
           for (k = 0; k < n_p; k++)
                  EXopt[Pos[k]] += Sign[k]*GG;
                                             *= pmag;
                  GG
 40
```

40 void FCS_Decod_PitCB (INT16 phas_mode, INT16 Sign[], FLOAT64 pmag, INT16 bits_p,

320 INT16 n_p, FLOAT64 EXopt[], INT16 Lex, INT16 Index[], INT16 lag, FLOAT64 pit_g)

```
{
5
            INT16 k, i, Pos[MAXPN], LocP[5], NumP;
            FLOAT64 GG, MagP[5];
                            Set-up the track positions
10
            FCS Set_CPCB (phas_mode, bits_p, n_p, Lex);
             for (k = 0; k < n_p; k++)
15
                    Pos[k] = track[k][Index[k]];
                           Calculate the excitation signal
20
             ini dvector(EXopt, 0, Lex-1, 0.0);
             GG = 1.0;
             for (k = 0; k < n_p; k++)
 25
                     {
                      FCS_DetermPulsLoc (pit_g, lag, GG, Pos[k], MagP, LocP, &NumP,
                                                               Lex);
                      for (i = 0; i < NumP; i++)
                              EXopt[LocP[i]] += Sign[k]*MagP[i];
 30
                      GG *= pmag;
                      }
 35
              return;
  40
```

```
321
   /* FUNCTION : FCS Calc pre_scarch ().
   /* PURPOSE : This function determines .
   /* INPUT ARGUMENTS :
          _ (FLOAT64 []) hh:
                                impulse sesponse W(z)/A(z).
   /*
10 /*
         _(INT16 ) lag: pitch lag.
          _ (FLOAT64 ) pgain: pitch gain .
                                                     */
          _ (FLOAT64 []) target: target signal.
          _(INT16 []) SignTab: array of pulse signs.
          _(INT16 []) res:
                             "ideal excitation" signal.
          _ (FLOAT64 ) SignWeight: weigthing factor for sign pulse*/
15 /*
                        determination (0.0 in all the */
   /*
   /*
                        calls).
                                            */
          (INT16 ) AppSign: use of sign table flag.
          (INT16 ) | sf:
                             excitation size.
20 /*----
   /* OUTPUT ARGUMENTS:
          (FLOAT64 *) Gp_m: modified pitch gain for harmonic*/
   /*
                        weighting.
          _ (FLOAT64 []) ref: reversed target signal.
          _(FLOAT64 []) hh_v: modified impulse sesponse.
25 /*
   /* INPUT/OUTPUT ARGUMENTS:
                     None.
30 /* RETURN ARGUMENTS:
                     _ None.
   /*
   void FCS_Calc_pre_search(FLOAT64 hh[], FLOAT64 hh_v[], INT16 lag, FLOAT64 pgain,
                                         FLOAT64 *Gp_m, FLOAT64 target[], FLOAT64 ref[],
35
                                                 INT16 SignTab[], FLOAT64 res[],
                                                         FLOAT64 SignWeight, INT16 AppSign,
                                                                INT16 1_sf)
40
```

```
INT16 i, j, k;
           FLOAT64
                           tmp[L_SF], GG, CC;
           FLOAT64
                           *buf;
 5
           buf = dvector (0, l_sf-1);
10
                       Harmonic weighting on hh
           cpy_dvector (hh, hh_v, 0, l_sf-1);
15
           (*Gp_m) = MIN(pgain, PAST_PGAIN_MAX);
           (*Gp_m)
                         = MAX(PAST_PGAIN_MIN, (*Gp_m));
            if (AppSign == 1)
                   for (i = lag; i < l_sf; i++)
                            hh_v[i] = hh_v[i] + (*Gp_m) * hh_v[i-lag];
20
                            Backward filtering
25
           for (j = 0; j < l_sf; j++)
                   ref[j] = target[l_sf-1-j];
           filterAZ (hh_v, ref, tmp, buf, (INT16)(l_sf-1), l_sf);
30
           for (j = 0; j < l_sf; j++)
                   ref[j] = tmp[l_sf-l-j];
                            Calculte PHI[][]
35
           for (i = 0; i < l_sf; i++)
                    PHI[i][l_sf-1] = hh_v[l_sf-1-i]*hh_v[0];
40
```

hh_v[l_sf-k+i-

for $(k = 1; k < 1_sf; k++)$

```
323
```

```
for (i = 1; i \le k; i++)
                              PHI[k-i][l_sf-1-i] = PHI[k-i+1][l_sf-i] +
5 1]*hh_v[i];
                               Set-up Sign table
10
            if (SignWeight > 0.01)
                     {
                      dot_dvector(ref, ref, &GG, 0, 1_sf-1);
                      dot_dvcctor(res, res, &CC, 0, 1_sf-1);
15
                      CC = sqrt(GG/MAX(CC, 0.1));
                      CC *= SignWeight;
                      for (i = 0; i < l_sf; i++)
                              tmp[i] = CC*res[i]+ref[i];
20
                      for (i = 0; i < l_sf; i++)
                               if (tmp[i] > 0)
                                       SignTab[i] = 1;
25
                               clse
                                       SignTab[i]=-1;
                              }
                     }
30
             clse
                      for (i = 0; i < 1_sf; i++)
                               if (ref[i] > 0)
                                       SignTab[i] = 1;
35
                               clse
                                       SignTab[i] = -1;
                               }
                      }
40
```

```
324
                              Apply sign table
            if(AppSign == 1)
 5
                    {
                    for (i = 0; i < l_sf; i++)
                            ref[i] *= SignTab [i];
                    for (i = 0; i < l_sf; i++)
10
                            for (j = i; j < l_sf; j++)
                                    PHI[i][j] *= SignTab[i]*SignTab[j];
                    }
            for (i = 1; i < l_sf; i++)
15
                    for (j = 0; j < i; j++)
                            PHI[i][j] = PHI[j][i];
20
            free_dvector (buf, 0, 1_sf-1);
            return;
25
   /* FUNCTION : FCS_PulseExchange ().
35 /* PURPOSE : This function swaps pulse 1 and 2 sign and index.*/
   /* INPUT ARGUMENTS:
                                                 */
              None.
40 /* OUTPUT ARGUMENTS:
```

```
325

    None.

                                                            */
   /* INPUT/OUTPUT ARGUMENTS:
             _(INT16 *) index1: pulse 1 index.
             _ (INT16 *) sign1 : pulse 1 sign.
             _(INT16 *) index2: pulse 2 index.
             _(INT16 *) sign2 : pulse 2 sign.
   /* RETURN ARGUMENTS:
                                              */
             None.
   void FCS_PulseExchange(INT16 *index1, INT16 *sign1, INT16 *index2, INT16 *sign2)
15
            INT16 i;
20
           i = (*index1),
            (*index1) = (*index2);
            (*index2) = i;
          i = (*sign1);
25
            (*sign1) = (*sign2);
           (*sign2) = i;
30
            return;
   /* FUNCTION : FCS_ST_parameter ().
40 /* PURPOSE : This function determines the stationary signal */
```

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```
/*
              coefficient that is comprise between 0.0 and 1.0.*/
   /* INPUT ARGUMENTS:
         _(INT16 ) i_sf: sub-frame index.
         _(INT16 ) rate_m: selected bit-rate.
         _(INT16 ) SVS_flag: coding mode (0/1).
   /*
         _ (FLOAT64 []) pdcfq: quantized prediction coeff.
         _(INT16 ) lag: pitch lag.
         _(FLOAT64 ) lpcg: LPC gain.
10 /*---
   /* OUTPUT ARGUMENTS:
          _(FLOAT64 []) pdcfq_w: weighted quantized predic. coeff.*/
             _____*/
                                                          */
   /* INPUT/OUTPUT ARGUMENTS:
15 /*
         _ (FLOAT64 *) Stab: stationary signal evaluation
                    coefficient (0.0->1.0).
   /*
   /* RETURN ARGUMENTS:
             _ None.
   void FCS ST parameter (INT16 i sf, INT16 rate m, INT16 SVS_flag, FLOAT64 pdcfq[],
                                 INT16 lag, FLOAT64 *Stab, FLOAT64 pdcfq_w[],
                                         FLOAT64 lpcg)
25
           FLOAT64 y, val;
           INT16 i;
30
                           LPC gain
           if (i sf == 0)
35
                   y = fabs(lpcg-lpcg_m) / MAX(fabs(lpcg + lpcg_m), EPSI);
                   if ((SVS_flag != 1) && (abs(lag-lag_m) > 0.25*lag))
                                 y = 1.0;
40
```

```
(*Stab) = 0.25 * (*Stab) + 0.75*(1.0 - y);
                     if (rate_m != RATE4_0K)
 5
                            (*Stab) = 0;
                                   Update memory
10
                     lpcg_m = lpcg;
                     lag_m = lag;
15
                              Update the weighting coefficients
                     if (lpcg < 10.0)
                                     alpha = 0.0125;
20
                     else
                             if (lpcg < 20.0)
                                     alpha = 0.025;
                             clsc
                                     alpha = 0.05;
25
                    }
30
            val = 1.0;
             for (i = 0; i < NP; i++)
                     val *= alpha;
                     pdcfq_w[i] = pdcfq[i] * val;
35
                     }
40
             return;
```

```
/* FUNCTION : FCS_Search_pastcorr ().
 10 /* PURPOSE : This function calculate the autocorrelation of */
                the past weighted quantized speech.
    /* INPUT ARGUMENTS:
           _(FLOAT64 []) ext: excitation signal.
15 /*
           _(FLOAT64 []) pdcfq: quantized prediction coeff.
          _ (FLOAT64 ) Stab: stationary signal evaluation
                         coefficient (0.0->1.0).
          _(INT16 ) pitch: pitch value.
          _(FLOAT64 ) ltpg: LTP gain.
20 /*
          _(INT16
                    ) SVS_flag: coding mode (0/1).
    /*
          _(INT16 ) i_sf: sub-frame index.
          _(INT16 ) l_sf: sub-frame size.
   /* OUTPUT ARGUMENTS:
25 /*
          _ (FLOAT64 []) Rw:
                                autocorrelation values.
          _ (FLOAT64 []) T_Rw: index of autocorrelation values. */
          _ (FLOAT64 *) N_Rw: number of autocorrelation values.*/
   /* INPUT/OUTPUT ARGUMENTS:
30 /*
             None.
                                              */
   /* RETURN ARGUMENTS:
             _ None.
35
   void FCS_Scarch_pastcorr (FLOAT64 ext[], FLOAT64 pdcfq[], FLOAT64 Stab,
                                         INT16 pitch, FLOAT64 ltpg, INT16 rate m,
                                                 INT16 SVS_flag, INT16 i_sf, INT16 1 sf,
                                                        FLOAT64 Rw[], INT16 T_Rw[], INT16 *N_Rw)
40
          {
```

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```
329
            FLOAT64 \ x, \ y, \ pdcfq\_w[NP], \ wsp[L\_WSP], \ tmpmcm[NP], \ val;
            INT16 i, k, m;
 5
                            Weighting filtering
10
            val = 1.0;
            for (i = 0; i < NP; i++)
                    val *= 0.85;
                    pdcfq_w[i] = pdcfq[i] * val;
15
                    }
            ini_dvcctor (tmpmem, 0, NP-1, 0.0);
           . if (SVS_flag != 1)
                    FLT_allsyn(cxt+MAX_LAG-L_WSP, L_WSP, pdcfq_w, NP, wsp, tmpmem);
20
            elsc
                    {
                    if (i_sf==0)
                            cpy dvector (ext+MAX_LAG-L_WSP, wsp_m, 0, L_WSP-1);
                     FLT allsyn(wsp_m, L_WSP, pdcfq_w, NP, wsp, tmpmem);
25
                               Correlation
30
             dot_dvector (wsp+L_WSP-L_CORR, wsp+L_WSP-L_CORR, &x, 0, L_CORR-1);
             (*N_Rw) = 0;
35
             ini_dvector(Rw, 0, 1_sf-1, 0.0);
             for (k = 3; k < 5; k++)
                     i = (INT16)(pitch / k + 0.5);
40
```

```
330
                     if ((i < l_sf) && (i > min_pit))
                                    for (m = 1; m < 6; m++)
                                            if (m*i < l_sf && m*i < pitch-6)
  5
                                                     dot_dvector (wsp+L_WSP-L_CORR,
                                                                                   wsp+L_WSP-L_CORR-
    m*i,
                                                                                    &Rw[*N_Rw], 0,
 10 L_CORR-1);
                                                    dot_dvector (wsp+L_WSP-L_CORR-m*i,
                                                                                   wsp+L_WSP-L_CORR-
    m*i, &y,
 15
                                                                                           0, L_CORR-1);
                                                    Rw [*N_Rw] = MAX(MIN(Rw[*N_Rw] /
                                                                                   MAX(0.5*(x+y), 0.001),
                                                                                           1.0), 0.0);
20
                                                    T_Rw[*N_Rw] = m*i;
                                                    (*N_Rw)++;
                            }
25
                    }
                             Reduction factor
                                                          */
30
            if ((SVS_flag == 1) && (SVS_flag_m == 1) && (rate_m == RATE4_0K))
                   {
                   for (i = 0; i < *N_Rw; i++)
35
                           Rw[i] *= Stab * 0.5;
                   for (i = 0; i < *N_Rw; i++)
                           Rw[i] = MIN(ltpg, 1.0);
                   }
           clsc
40
                   {
```

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```
331
                for (i = 0; i < *N_Rw; i++)
                       Rw[i] *= Stab*0.25;
                for (i = 0; i < *N_Rw; i++)
                       Rw[i] *= MIN(ltpg, 0.75);
                }
5
          if (i_sf == 0)
                SVS_flag_m = SVS_flag;
10
          return;
15
/* FUNCTION : FCS_cdbk_search_13b_sub54 ().
   /* PURPOSE : This function estimate the optimal fixed
         excitation for 4.0 kbps mode 1.
25 /*-----
   /* INPUT ARGUMENTS:
         _(FLOAT64 []) ext: excitation signal.
         _(FLOAT64 []) pdcfq: quantized prediction coeff.
          _(FLOAT64 []) target: target signal.
                             "ideal excitation" signal after */
          _(FLOAT64 []) res:
· 30 /*
                       LTP contribution.
    /*
                              W(z)/A(z) impulse response.
          _(FLOAT64 []) hh:
                   ) l_sf: sub-frame size.
          _(INT16
          _(INT16 ) i_sf: sub-frame index.
                           pitch lag.
                   ) lag:
          _(INT16
 35 /*
                   ) VUV: frame class.
          _(INT16
          _(FLOAT64 ) pgain: pitch preprocessing quantized */
                        pitch gain.
          _(FLOAT64 ) NSR: noise to signal ratio.
          _(FLOAT64 ) Rp: pitch preprocessing pitch
  40 /*
```

```
332
   /*
                         correlation.
          _ (INT16
                     ) rate_m: sclected bit-rate.
          _(FLOAT64 ) lpcg: LPC gain.
                                                      */
 5 /* OUTPUT ARGUMENTS:
   /*
          _ (FLOAT64 []) unfcod: unfiltered excitation signal. */
          _ (PARAMETER *) chan: output data structure.
   /* INPUT/OUTPUT ARGUMENTS:
                                                           */
10 /*
          None.
                                              */
   /* RETURN ARGUMENTS:
          None.
15
   void FCS_cdbk_search_13b_sub54 (FLOAT64 ext [], FLOAT64 pdcfq[], FLOAT64 target[],
                                  FLOAT64 res [], FLOAT64 hh[], FLOAT64 unfcod[],
                                  INT16 l_sf, INT16 i_sf, INT16 lag,
                                         INT16 VUV, FLOAT64 pgain, FLOAT64 NSR, FLOAT64 Rp,
20
                                                 INT16 rate m, FLOAT64 lpcg, PARAMETER *chan)
           {
25
           INT16 mode:
           INT16 T_Rw[L_SF3], N Rw, n;
           INT16 i, lopt;
           INT16 IdxPuls[MAXPN]={0}, Sign[MAXPN]={0};
           INT16 SignTab[L_SF3];
30
           FLOAT64 Criter, Criter_m, Gp_m;
           FLOAT64 hh v[L SF3], ref[L SF3];
           FLOAT64 Rw[L SF3], tmpmcm[NP], pdcfq w[NP];
35
           cpy_dvector(hh, hh_v, 0, 1_sf-1);
           FCS_ST_parameter (i_sf, rate_m, 1, pdcfq, lag, &Stab_13b_enc,
40
                                         pdcfq_w, lpcg);
```

```
FCS_Search_pastcorr (ext, pdcfq, Stab_13b_enc, lag, pgain, rate_m,
                                                     1, i_sf, l_sf, Rw, T_Rw, &N_Rw);
            for (i = 0; i < N_Rw; i++)
 5
                   for (n = T_Rw[i]; n < l_sf; n++)
                           hh_v[n] += Rw[i] + hh[n-T_Rw[i]];
            ini_dvector (tmpmem, 0, NP-1, 0.0);
            FLT_allsyn (hh_v, l_sf, pdcfq_w, NP, hh_v, tmpmem);
10
            cpy_dvector (hh_v, ref, 0, 1_sf-1);
            for (i = 1; i < L HF; i++)
                   for (n = i; n < l_sf; n++)
                           hh_v[n] += hh_hf[i]*ref[n-i];
15
            FCS_Calc_pre_search (hh_v, hh_v, lag, pgain, &Gp_m, target, ref,
                                                    SignTab, res, 0.0, 1, 1_sf);
20
          Iopt = 0;
            Criter=-1;
            /*____*/
               2 pulses CB: 2pulses x 5bits/pulse + 2 signs = 12 bits */
25
            FCS_Full_Scarch_CPCB (0, Sign, 1.0, 5, 2, SignTab, ref, &Criter,
                                                    unfcod, 1 sf, IdxPuls);
30
            mode = 3;
            Criter_m = Criter;
            /* 3 pulses CB: 3 pulses x (2 or 1)bits/pulse + 3 signs + 3bits */
                            center = 12 bits
35
                         Determine the center
40
```

```
for (i = 0; i < SEG_NUM_M1; i++)
                      FCS_Full_Scarch_CPCB (i, Sign, 1.0, 2, 3, SignTab, ref, &Criter,
  5
                                                                                  unfcod, l_sf, IdxPuls);
                      if (Criter > Criter_m)
                              {
                               lopt = i;
 10
                               mode = 2;
                               Criter_m = Criter;
                     }
15
                            Save the Index of the cdbk
             for (i = 0; i < MAXPN; i++)
20
                     chan->idx_cpcb[i_sf][i] = IdxPuls[i];
             for (i = 0; i < MAXPN; i++)
                     chan->idx_cpcbsign[i_sf][i] = (Sign[i]+1)/2;
25
             chan->idx_center[i_sf] = Iopt;
             if (mode == 3)
                     chan-idx_subcpcb[i_sf][0] = 1;
             elsc
30
                     chan-idx_subcpcb[i_sf][0] = 0;
                           Harmonic weighting on unfcod
35
            for (i = lag; i < l_sf; i++)
                             unfcod[i] += Gp_m * unfcod[i-lag];
40
            cpy_dvector (unfcod, hh_v, 0, 1_sf-1);
```

```
335
```

```
for (i = 0; i < N_Rw; i++)
                  for (n = T_Rw[i]; n < l_sf; n++)
                         unfcod[n] += Rw[i] * hh_v[n-T_Rw[i]];
5 .
           ini dvector (tmpmem, 0, NP-1, 0.0);
           FLT allsyn(unfcod, 1 sf, pdcfq_w, NP, unfcod, tmpmem);
           cpy_dvector (unfcod, ref, 0, 1_sf-1);
           for (i = 1; i < L_HF; i++)
10
                 for (n = i; n < l_sf; n++)
                         unfcod[n] += hh_hf[i]*rcf[n-i];
15
           return;
   /* FUNCTION : FCS_cdbk_decod_13b_sub54 ().
                                                            */
   /*-----*/
25 /* PURPOSE : This function decode the optimal fixed
             excitation for 4.0 kbps mode 1.
   /* INPUT ARGUMENTS:
         (FLOAT64 []) ext: excitation signal.
   /*
30 /*
         _(FLOAT64 []) pdcfq: quantized prediction coeff.
         _(INT16 ) l_sf: sub-frame size.
   /*
         _(INT16 ) i_sf: sub-frame index.
                            pitch lag.
         _(INT16 ) lag:
         _(FLOAT64 ) pgain: dccoded pitch gain.
35 /*
         (INT16 ) rate_m: current fixed bit-rate.
          (FLOAT64 ) lpcg: decoded LPC gain.
   /* OUTPUT ARGUMENTS:
          (FLOAT64 []) unfcod: unfiltered excitation signal. */
```

```
336
    /* INPUT/OUTPUT ARGUMENTS:
                                                        */
          _(PARAMETER *) chan: output data structure.
    /* RETURN ARGUMENTS:
 5 /*
          None.
   void FCS_cdbk_decod_13b_sub54 (FLOAT64 ext [], FLOAT64 pdcfq [],
                                       FLOAT64 unfcod [], INT16 1_sf, INT16 i_sf,
10
                                INT16 lag, FLOAT64 pgain,
                                       INT16 rate_m, FLOAT64 lpcg,
                                              PARAMETER *chan)
           {
15
           INT16 T_Rw[L_SF3], N_Rw, n;
           INT16 i, lopt, IdxPuls[MAXPN], Sign[MAXPN];
           FLOAT64 Gp_m;
           FLOAT64 Rw[L_SF3], hh_v[L_SF3], tmpmcm[NP], pdcfq_w[NP];
20
           for (i = 0; i < MAXPN; i++)
                 IdxPuls[i] = chan->idx_cpcb[i_sf][i];
25
           for (i = 0; i < MAXPN; i++)
                 Sign[i] = 2*chan->idx_cpcbsign[i_sf][i]-1;
           if (chan-idx\_subcpcb[i\_sf][0] == 1)
                 {
30
                  /* 2 pulses CB: 2pulses x 5bits/pulse + 2 signs = 12 bits */
                 FCS_Decod_CPCB (0, Sign, 1.0, 5, 2, unfcod, 1 sf, IdxPuls);
35
                 }
          else
                 { -
                 /* 3 pulses CB: 3 pulses x (2 or 1)bits/pulse +
40
                           3 signs + 3bits center = 11 bits
```

```
337
                     lopt = chan->idx_center[i_sf];
                     FCS Decod CPCB (Iopt, Sign, 1.0, 2, 3, unfcod, l_sf, ldxPuls);
 5
                               Pitch enhancement
10
            Gp m = MIN(pgain, PAST_PGAIN_MAX);
             Gp m = MAX(PAST_PGAIN_MIN, Gp_m);
             for (i = lag; i < l_sf; i++)
                     unfcod[i] = unfcod[i] + Gp_m*unfcod[i-lag];
15
             FCS ST_parameter (i_sf, rate_m, 1, pdcfq, lag, &Stab_13b_dcc,
                                              pdcfq_w, lpcg);
             FCS_Search_pastcorr (ext, pdcfq, Stab_13b_dec, lag, pgain, rate_m, 1,
20
                                              i sf, 1_sf, Rw, T_Rw, &N_Rw);
             cpy_dvector (unfcod, hh_v, 0, l_sf-1);
             for (i = 0; i < N_Rw; i++)
25
                     for (n = T_Rw[i]; n < l_sf; n++)
                             unfcod[n] += Rw[i]*hh_v[n-T_Rw[i]];
             ini dvector (tmpmem, 0, NP-1, 0.0);
             FLT allsyn (unfcod, l_sf, pdcfq_w, NP, unfcod, tmpmem);
30
             cpy dvector (unfcod, hh_v, 0, l_sf-1);
             for (i = 1; i < L_HF; i \leftrightarrow)
                     for (n = i; n < l_sf; n++)
35
                              unfcod[n] += hh_hf[i] + hh_v[n-i];
             return;
40
```

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```
338
     /* FUNCTION : FCS_cdbk_search_15b_sub80 ().
                                                                 */
     /* PURPOSE
                   : This function estimate the optimal fixed
 10 /*
                 excitation for 4.0 kbps mode 0.
     /* INPUT ARGUMENTS :
     /*
            _ (FLOAT64 []) ext:
                                 excitation signal.
     /*
            _ (FLOAT64 []) pdcfq: quantized prediction coeff.
 15 /*
            _ (FLOAT64 []) target: target signal.
    /*
            _ (FLOAT64 []) res:
                                 "ideal excitation" signal after */
    /*
                         LTP contribution.
                                                   */
            _ (FLOAT64 | | ) hh:
                                  W(z)/A(z) impulse response.
           _(INT16
                       ) l_sf:
                               sub-frame size.
 20 /*
           _ (INT16
                       ) i_sf:
                               sub-frame index.
           _ (INT16
                       ) lag:
                               pitch lag.
           _ (INT16
                       ) VUV:
                                 frame class.
           _(FLOAT64 ) pgain: quantized pitch gain.
           _(FLOAT64 ) NSR:
                                  noise to signal ratio.
25 /*
           _(FLOAT64 ) Rp:
                                 pitch preprocessing pitch
                         correlation.
    /* OUTPUT ARGUMENTS:
    /*
           _ (FLOAT64 []) unfcod: unfiltered excitation signal.
30 /*
           _ (PARAMETER *) chan: output data structure.
   /* INPUT/OUTPUT ARGUMENTS:
           None.
35 /* RETURN ARGUMENTS:
           Nonc.
   void FCS_cdbk_scarch_15b_sub80 (FLOAT64 ext[], FLOAT64 pdcfq[], FLOAT64 target[],
40
                                   FLOAT64 res[], FLOAT64 hh[], FLOAT64 unfcod[],
```

```
339
                                   INT16 1_sf, INT16 i_sf, INT16 lag,
                                   INT16 VUV, FLOAT64 pgain, FLOAT64 NSR, FLOAT64 Rp,
                                   INT16 rate_m, FLOAT64 lpcg, PARAMETER *chan)
5
           INT16 mode;
           INT16 T_Rw[L_SF], N_Rw;
           INT16 IdxPuls[MAXPN] = \{0\}, Sign[MAXPN] = \{0\};
10
           INT16 i, n, lopt;
           INT16 IdxPuls_tmp[MAXPN],
                   Sign_tmp[MAXPN], SignTab[L_SF];
           FLOAT64
                           P1_SHP, Criter, Criter_m, Criter_tmp, Gp_m, gp;
           FLOAT64 hh_v[L_SF], ref[L_SF];
           FLOAT64 Rw[L_SF], tmpmem[NP], pdcfq_w[NP];
15
            cpy_dvcctor (hh, hh_v, 0, l_sf-1);
           FCS_ST_parameter (i_sf, rate_m, l, pdcfq, lag, &Stab_15b_cnc,
20
                                            pdcfq_w, lpcg);
            FCS_Search_pastcorr(ext, pdcfq, Stab_15b_cnc, lag, pgain, rate_m, 0,
                                                    i_sf, l_sf, Rw, T_Rw, &N_Rw);
25
            for (i = 0; i < N_Rw; i++)
                   for (n = T_Rw[i]; n < l_sf; n++)
                           hh_v[n] += Rw[i] + hh[n-T_Rw[i]];
30
            ini dvector (tmpmem, 0, NP-1, 0.0);
            FLT_allsyn(hh_v, l_sf, pdcfq_w, NP, hh_v, tmpmem);
            cpy_dvcctor (hh_v, ref, 0, 1_sf-1);
35
            for (i = 1; i < L_HF; i++)
                   for (n = i; n < l_sf; n++)
                           hh_v[n] += hh_h[i]*ref[n-i];
            /* 2 pulses CB: 2 pulses x 6 bits/pulse + 2 signs = 14 bits
40
```

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```
FCS_Calc_prc_search (hh_v, hh_v, lag, pgain, &Gp_m, target, ref,
                                                         SignTab, res, 0.0, 0, 1_sf);
 5
             Criter = -1;
             gp = 0.75;
10
          FCS_Simp_Search_4kCB (0, 1, 1, Sign, 1.0, 7, 2, SignTab, ref, &Criter,
                                               unfcod, l_sf, IdxPuls, lag, gp);
             mode = 3;
             Criter_m = Criter;
15
                                Sign check
                                                               */
20
          if (IdxPuls[0]<=IdxPuls[1])</pre>
            {
                      if (Sign[0]!=Sign[1])
                     FCS_PulseExchange(IdxPuls, Sign, IdxPuls+1, Sign+1);
25
          else
            --{
                      if(Sign[0] == Sign[1])
                              FCS_PulseExchange(IdxPuls, Sign, IdxPuls+1, Sign+1);
30
         IdxPuls[0] = IdxPuls[0]*L_SF + IdxPuls[1];
             /* 3 pulses CB : 3 pulses x 2 bits/pulse + 3 signs + 4bits center = */
35
                          13 bits
                                                            */
                               Reference signal
                                                                */
40
```

```
FCS_Calc_pre_search (hh_v, hh_v, lag, 0.25, &Gp_m, target, ref,
                                                      SignTab, res, 0.0, 1, l_sf);
5
                            Determine the center
             Iopt = 0;
             Criter = -1;
10
             Criter_tmp = Criter;
             for (i = 0; i < SEG_NUM_M0; i++)
                     FCS_Simp_Search_CPCB(0, 0, i, Sign_tmp, 1.0, 2, 3, SignTab, ref,
                                               &Criter, hh_v, l_sf, IdxPuls_tmp);
15
                     if(Criter > Criter_tmp)
                             Iopt = i;
20
                     Criter_tmp = Criter;
                              Determine the shape
25
             Criter = Criter_m;
             if ((lag < 35) && (Rp < 0.6))
                     Criter*=MIN(0.6+0.4*(lag-MIN_LAG)/(35.0-MIN_LAG), 1.0);
30
             Criter_tmp = Criter;
             FCS_Full_Search_CPCB(Iopt, Sign, 1.0, 2, 3, SignTab, rcf, &Criter,
                                                        unfcod, l_sf, IdxPuls);
35
              if (Criter > Criter_tmp)
                      modc = 2;
              elsc
                      Criter = Criter_m;
 40
```

/*-----*/

```
Perceptual Wheighted decison
                  */
 5
            P1_SHP = PPP_sharpness (l_sf, res);
            if ((VUV \ge 3) && (Rp < 0.75))
                   Criter *= MIN(MAX(1.5-4.0*P1_SHP, 0.5), 1.0);
10
            if (VUV == 2)
                   Criter = MIN(MAX(1.5-5.0*P1_SHP, 0.25), 1.0);
            if (VUV \le 1)
                   Criter *= 0.75*MIN(MAX(1.5-5.0*P1_SHP, 0.0), 1.0);
           if ((VUV == 1) && (mode == 3) && (lag < 50))
15
                   Criter = 0.5;
           if (VUV \ge 2)
                   Criter *= 1.0 - 0.5*NSR*MIN(P1_SHP+0.5, 1.0);
           Criter_m = Criter;
20
                         Gaussian codebook, 13 bits
                                                            */
25
           GCB_gauss_cb_itu4k (target, rcs, hh, &Criter, unfcod, ldxPuls, Sign,
                                                                                 N GAUSS 13b,
   N_GAUSS_13b);
           if (Criter > Criter_m)
30
                   mode = 1;
                   IdxPuls[0] = IdxPuls[0]*N_GAUSS_13b + IdxPuls[1];
35
                            Index of the cdbk
           for (i = 0; i \leq MAXPN; i++)
40
                  chan->idx_cpcb[i_sf][i] = IdxPuls[i];
```

```
343
             for (i = 0; i < MAXPN; i++)
                    chan-idx_cpcbsign[i_sf][i] = (Sign[i]+1)/2;
             chan->idx_center[i_sf] = lopt;
 5
             if (mode == 3)
                     chan-idx_subcpcb[i_sf][0] = 1;
             elsc
10
                     chan-idx_subcpcb[i_sf][0] = 0;
                     if (mode == 2)
                              chan-idx_subcpcb[i_sf][1] = 1;
                     clse
                              chan-idx_subcpcb[i_sf][1] = 0;
15
                     }
                            Harmonic weighting on unfcod
20
             if (mode \geq = 2)
                     {
                      if (mode == 2)
                              for (i = lag; i < l_sf; i++)
                                       unfcod[i] += Gp_m * unfcod[i-lag];
25
                      cpy_dvector (unfcod, hh_v, 0, 1_sf-1);
                      for (i = 0; i < N_Rw; i++)
                              for (n = T_Rw[i]; n < l_sf; n++)
30
                                       unfcod[n] += Rw[i]*hh_v[n-T_Rw[i]];
                      ini_dvector (tmpmem, 0, NP-1, 0.0);
                      FLT_allsyn(unfcod, l_sf, pdcfq_w, NP, unfcod, tmpmem);
                      cpy_dvector (unfcod, ref, 0, l_sf-1);
35
                      for (i = 1; i < L_HF; i++)
                              for (n = i; n < l_sf; n++)
                                       unfcod[n] += hh_hf[i]*ref[n-i];
                      }
```

```
344
           rcturn;
  5
10
   /* FUNCTION : FCS_cdbk_decod_15b_sub80 ().
                                                   */
   /* PURPOSE : This function decode the fixed excitation vector */
15 /*
             for 4.0 kbps mode 0.
   /* INPUT ARGUMENTS:
   /*
         _(FLOAT64 []) ext: excitation signal.
         _ (FLOAT64 []) pdcfq: quantized prediction coeff.
20 /*
        _(INT16 ) l_sf: sub-frame size.
        _(INT16 ) i_sf: sub-frame index.
   /*
        _(INT16 ) lag:
                        pitch lag.
   /*
        _(FLOAT64 ) pgain: decoded pitch gain.
   /*
        _(INT16 ) rate_m: decoded fix bit-rate.
25 /*
        _(FLOAT64 ) lpcg: decoded LPC gain.
  /* OUTPUT ARGUMENTS:
        _ (FLOAT64 []) unfcod: unfiltered excitation signal. */
30 /* INPUT/OUTPUT ARGUMENTS:
        _(PARAMETER *) chan: output data structure.
  /* RETURN ARGUMENTS:
        None.
                                      */
FCS_cdbk_dccod_15b_sub80 (FLOAT64 ext[], FLOAT64 pdcfq[],
  void
                                  FLOAT64 unfcod[], INT16 1_sf, INT16 i_sf,
                                  INT16 lag, FLOAT64 pgain,
40
                                  INT16 rate_m, FLOAT64 lpcg, PARAMETER *chan)
```

```
345
           {
           INT16 mode;
5
           INT16 T_Rw[L_SF], N_Rw;
           INT16 i, n, lopt, IdxPuls[MAXPN], Sign[MAXPN], IdxGauss[2];
           FLOAT64 gp, tmp[L_SF];
           FLOAT64 Rw[L_SF],tmpmcm[NP], pdcfq_w[NP];
10
           for (i = 0; i < MAXPN; i++)
                   IdxPuls[i] = chan->idx_cpcb[i_sf][i];
15
           for (i = 0; i < MAXPN; i++)
                   Sign[i] = 2*chan->idx_cpcbsign[i_sf][i] - 1;
           if (chan-idx_subcpcb[i_sf][0] == 1)
20
                   /* 2 pulses CB: 2pulses x 6bits/pulse + 2 signs = 14 bits */
                   /*____*/
                   mode = 3;
25
                   gp = 0.75;
                   i = IdxPuls[0]/L_SF;
             IdxPuls[1] = IdxPuls[0] - i*L_SF;
             IdxPuls[0] = i;
30
             /*
                            Sign check
                                                     */
             if (IdxPuls[0] <= IdxPuls[1])</pre>
35
                   Sign[1] = Sign[0];
             else
                   Sign[1] = -Sign[0];
             FCS_Decod_PitCB (1, Sign, 1.0, 7, 2, unfcod, 1_sf, IdxPuls,
40
```

```
346
                                                      lag, gp);
                     }
             else
  5
                      if (chan-idx\_subcpcb[i\_sf][1] == 1)
                             {
                              /* 3 pulses CB: 3 pulses x 2 bits/pulse + 3 signs +
                                         4bits center = 13 bits
 10
                              modc = 2;
                              lopt = chan->idx_center[i_sf];
                              FCS_Decod_CPCB (Iopt, Sign, 1.0, 2, 3, unfcod, 1_sf,
 15
                                                                      IdxPuls);
                             }
                     else
20
                                         Gaussian codebook, 13 bits
                             mode = 1;
25 #ifdef VERBOSE
                             if (l_sf!=40+40)
                                     nrerror("ERROR: dimension mismatch decoding gaussian excitation\n");
   #endif
30
                                  Decode the 11 bits index into the two 45-entry */
                                             codebooks
35
                             IdxGauss[0] = IdxPuls[0]/N_GAUSS_13b;
                             IdxGauss[1] = IdxPuls[0] - N_GAUSS_13b*IdxGauss[0];
                             GCB_decode_gauss_cb_itu4k(N_GAUSS_13b, 40, IdxGauss[0],
            Sign[0], unfcod);
40
                             GCB_dccode_gauss_cb_itu4k(N_GAUSS_13b, 40, IdxGauss[1],
```

```
Sign[1], unfcod+1);
                     }
 5
            FCS_ST_parameter (i_sf, rate_m, 1, pdcfq, lag, &Stab_15b_dec,
                                               pdcfq_w, lpcg);
            FCS_Search_pastcorr(ext, pdcfq, Stab_15b_dec, lag, pgain, rate_m, 0,
                                                        i_sf, I_sf, Rw, T_Rw, &N_Rw);
10
            if (mode \geq = 2)
                     {
                     if (mode == 2)
                             for (i = lag; i < l_sf; i++)
15
                                      unfcod[i] = unfcod[i] + 0.25*unfcod[i-lag];
                     cpy_dvector (unfcod, tmp, 0, l_sf-1);
                      for (i = 0; i < N_Rw; i++)
                              for (n = T_Rw[i]; n < l_sf; n++)
20
                                      unfcod[n] += Rw[i]*tmp[n-T_Rw[i]];
                      ini_dvector (tmpmem, 0, NP-1, 0.0);
                      FLT_allsyn (unfcod, l_sf, pdcfq_w, NP, unfcod, tmpmem);
25
                      cpy_dvcctor (unfcod, tmp, 0, l_sf-1);
                      for (i = 1; i < L_HF; i++)
                              for (n = i; n < l\_sf; n++)
                                       unfcod[n] += hh_hf[i]*tmp[n-i];
30
                     }
             return;
35
 40
```

```
/* FUNCTION : FCS_cdbk_scarch_22b_sub40 ().
                                                            */
     /* PURPOSE : This function estimate the optimal fixed
                excitation for 8.5 kbps mode 0.
     /* INPUT ARGUMENTS :
     /*
           _ (FLOAT64 []) ext:
                               excitation signal.
           _(FLOAT64 []) pdcfq: quantized prediction coeff.
  10 /*
           _ (FLOAT64 []) target: target signal.
           _ (FLOAT64 []) res:
                               "ideal excitation" signal after */
                         LTP contribution.
                                                */
           _ (FLOAT64 []) hh:
                                W(z)/A(z) impulse response.
           _ (INT16
                      ) 1_sf: sub-frame size.
                                                   */
 15 /*
           _ (INT16
                     ) i_sf: sub-frame index.
    /*
           _ (INT16
                     ) lag:
                             pitch lag.
    /*
           _(INT16
                     ) VUV:
                              frame class.
    /*
           _ (FLOAT64 ) pgain: quantized pitch gain.
    /*
           _ (FLOAT64 ) NSR:
                                noise to signal ratio.
 20 /*
           _(FLOAT64 ) Rp:
                               pitch preprocessing pitch
                       correlation.
                                            */
    /* OUTPUT ARGUMENTS:
          _(FLOAT64 ||) unfcod: unfiltered excitation signal. */
25 /*
          _ (PARAMETER *) clian: output data structure.
   /* INPUT/OUTPUT ARGUMENTS:
                                            */
30 /* RETURN ARGUMENTS:
          _ None.
   void FCS_cdbk_scarch_22b_sub40 (FLOAT64 ext [], FLOAT64 pdcfq [],
35
                         FLOAT64 target [], FLOAT64 res [], FLOAT64 hh [],
                                FLOAT64 unfcod [], INT16 i_sf, INT16 i_sf,
                                       INT16 lag, INT16 VUV, FLOAT64 pgain,
                                              FLOAT64 NSR, FLOAT64 Rp, PARAMETER *chan)
40
```

```
INT16 i, mode;
           INT16\ IdxPuls[MAXPN],\ Sign[MAXPN],\ SignTab[L\_SF4];
           FLOAT64 P1_SHP, Criter, Criter_m, Criter_m3, Criter_m2, Gp_m, Gp;
5
           FLOAT64 hh v[L_SF4], ref[L_SF4];
                    */
           if ((lag < 20) && (pgain > 0.5))
10
                   Gp = MIN(pgain, 1.0)*0.75;
           else
                   Gp = pgain;
            Gp = MIN(Gp, PAST_PGAIN_MAX2);
15
                           Reference signal
            FCS_Calc_pre_search (hh, hh_v, lag, Gp, &Gp_m, target, ref, SignTab,
20
                                                   res, 0.5, 1, 1_sf);
            /*_------*/
            /* 5 pulses CB : 3p \times 4b + 2p \times 3b + 3b \text{ signs} = 21 \text{ bits}
25
            Criter=-1;
            FCS_Simp_Search_CPCB (0, 1, 0, Sign, 1.0, 4, 5, SignTab, ref, &Criter,
                                                   unfcod, l_sf, IdxPuls);
            modc = 3;
30
            Criter_m = Criter;
            Criter_m3 = Criter;
            /* 5 pulses CB : 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
35
            FCS_Simp_Search_CPCB(0, 0, 2, Sign, 1.0, 3, 5, SignTab, ref, &Criter,
                                                            unfcod, 1_sf, IdxPuls);
            if (Criter > Criter_m)
 40
```

```
350
                   mode = 2;
                   Criter_m2 = Criter;
 5
                         Perceptual Wheighted decison
10
           P1_SHP = PPP_sharpness(l_sf, res);
           if (VUV \le 0)
                   Criter *= 0.9*MIN(MAX(1.5-5.0*P1_SHP, 0.7), 1.0);
           if (VUV == 1)
                   Criter *= 0.9;
15
           if (VUV \ge 1)
                  Criter *= 1.0-0.25*NSR*MIN(P1_SHP+0.5, 1.0);
           Criter_m = Criter;
           /* 5 pulses CB : 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
20
           FCS_Simp_Search_CPCB(0, 1, 1, Sign, 1.0, 3, 5, SignTab, ref, &Criter,
                                                 unfcod, l_sf, IdxPuls);
25
           if (Criter > Criter_m)
                  mode = 1;
30
                            Fine serach
35
           switch (mode)
                   /*____*/
                   /* 5 pulses CB : 3p \times 4b + 2p \times 3b + 3b \text{ signs} = 21 \text{ bits } */
40
```

```
351
                      case 3: Criter = Criter_m3;
                                FCS_Simp_Scarch_CPCB(2, 2, 0, Sign, 1.0, 4, 5, SignTab,
                                                           ref, &Criter, unfcod, l_sf, IdxPuls);
 5
                                                  Sign check on the same track
                                        for (i = 1; i < 3; i++)
10
                                                  if (IdxPuls[i] <= IdxPuls[i+2])
                                                           {
                                                           if (Sign[i] != Sign[i+2])
                                                                    FCS_PulseExchange (IdxPuls+i, Sign+i,
                                                                              IdxPuls+i+2, Sign+i+2);
15
                                                           }
                                                  else
                                                           if (Sign[i] == Sign[i+2])
                                                                    FCS_PulscExchange (ldxPuls+i, Sign+i,
20
                                                                              IdxPuls+i+2, Sign+i+2);
                                                  }
                               break:
25
                          5 pulses CB: 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
30
                       case 2: Criter = Criter_m2;
                                 FCS_Simp_Search_CPCB(1, 1, 2, Sign, 1.0, 3, 5, SignTab,
                                                           ref, &Criter, unfcod, l_sf, IdxPuls);
                                break;
35
                            5 pulses CB: 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
```

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```
case 1: FCS_Simp_Scarch_CPCB(2, 2, 1, Sign, 1.0, 3, 5, SignTab,
                                                                ref, &Criter, unfcod, l_sf, IdxPuls);
                             brcak;
 5
                      default: nrerror("Wrong mode !!");
                             break;
                     }
10
                               Index of the cdbk
             for (i = 0; i < MAXPN; i++)
15
                     chan->idx_cpcb[i_sf][i] = IdxPuls[i];
             for (i = 0; i < MAXPN; i++)
                     chan-idx_cpcbsign[i_sf][i] = (Sign[i]+1)/2;
20
            if (modc == 3)
                     chan->idx_subcpcb[i_sf][0] = 1;
             clsc
25
                     chan-idx_subcpcb[i_sf][0] = 0;
                     if (modc == 2)
                             chan->idx_subcpcb[i_sf][1] = 1;
                     else
                            -chan->idx_subcpcb[i_sf][1] = 0;
30
                     }
                           Harmonic weighting on unfcod
35
             for (i = lag; i < l_sf; i++)
                     unfcod[i] += Gp_m * unfcod[i-lag];
40
```

353 return; 5 ||| /* FUNCTION : FCS_cdbk_decod_22b_sub40 (). 10 /*----*/ /* PURPOSE : This function decode the fixed excitation vector */ for 8.5kbps mode 0. /* INPUT ARGUMENTS: _(INT16) l_sf: sub-frame size. _(INT16) i_sf: sub-frame index. _(INT16) lag: pitch lag. _(FLOAT64) pgain: decoded pitch gain. _(INT16) rate_m: decoded fix bit-rate. _(FLOAT64) lpcg: decoded LPC gain. 20 /* /* OUTPUT ARGUMENTS : _(FLOAT64 []) unfcod: unfiltered excitation signal. */ 25 /* INPUT/OUTPUT ARGUMENTS: _ (PARAMETER *) chan: output data structure. /* RETURN ARGUMENTS: Nonc. void FCS_cdbk_dccod_22b_sub40(FLOAT64 unfcod [], INT16 I_sf, INT16 i_sf, INT16 lag, FLOAT64 pgain, PARAMETER *chan) 35 INT16 i, IdxPuls[MAXPN], Sign[MAXPN]; FLOAT64 Gp_m; 40

```
for (i = 0; i < MAXPN; i++)
                    IdxPuls[i]=chan->idx_cpcb[i_sf][i];
            for (i = 0; i < MAXPN; i++)
 5
                 Sign[i] = 2*chan->idx_cpcbsign[i_sf][i]-1;
            if (chan-idx_subcpcb[i_sf][0] == 1)
10
                    /* 5 pulses CB : 3p \times 4b + 2p \times 3b + 3b \text{ signs} = 21 \text{ bits}
                    15
                    for (i = 1; i < 3; i++)
                             if(IdxPuls[i] \leq IdxPuls[i+2])
                                    Sign[i+2] = Sign[i];
                            else
20
                                    Sign[i+2] = -Sign[i];
                            }
                    FCS_Decod_CPCB(0, Sign, 1.0, 4, 5, unfcod, 1_sf, IdxPuls);
25
            else
                    if (chan->idx_subcpcb[i_sf][1]==1)
30
                            /* 5 pulses CB : 3p \times 4b + 2p \times 3b + 3b \text{ signs} = 21 \text{ bits } */
                            FCS Decod CPCB(2, Sign, 1.0, 3, 5, unfcod, 1 sf, IdxPuls);
                            }
35
                    clse
                             /*-----*/
                             /* 5 pulses CB : 5p x 3b + 5b signs = 20 bits
40
```

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```
355
                         FCS Decod CPCB(1, Sign, 1.0, 3, 5, unfcod, 1_sf, IdxPuls);
                 }
 5
                         Pitch enhancement
          if ((lag < 20) && (pgain > 0.5))
                 Gp_m = MIN(pgain, 1.0)*0.75;
10
          else
                 Gp_m = pgain;
          Gp_m = MIN(Gp_m, PAST_PGAIN_MAX2);
          Gp_m = MAX(PAST_PGAIN_MIN, Gp_m);
15
          for (i = lag; i < l_sf; i++)
                unfcod[i] = unfcod[i] + Gp_m*unfcod[i-lag];
20
          return;
25
/* FUNCTION : FCS cdbk_scarch_30b_sub40 ().
                                                         */
   /* PURPOSE : This function estimate the optimal fixed
              excitation for 8.5kbps mode 1.
   /* INPUT ARGUMENTS:
         _(FLOAT64 []) ext: excitation signal.
   /*
         _(FLOAT64 []) pdcfq: quantized prediction coeff.
   /*
         _(FLOAT64 []) target: target signal.
   /*
         _ (FLOAT64 []) res:
                             "ideal excitation" signal after */
40 /*
```

```
LTP contribution.
           (FLOAT64 []) hh:
                                 W(z)/A(z) impulse response.
           (INT16
                      ) | sf:
                              sub-frame size.
           _ (INT16
                      ) i sf:
                              sub-frame index.
  5 /*
           _(INT16
                              pitch lag.
                      ) lag:
           _ (INT 16
                      ) VUV:
                                frame class.
           _(FLOAT64) pgain: quantized pitch gain.
           _(FLOAT64 ) NSR:
                                 noise to signal ratio.
           _(FLOAT64 ) Rp:
                                pitch preprocessing pitch
10 /*
                        correlation.
                                              */
   /* OUTPUT ARGUMENTS:
          _(FLOAT64 []) unfcod: unfiltered excitation signal.
          _(PARAMETER *) chan: output data structure.
   /* INPUT/OUTPUT ARGUMENTS:
          _ None.
   /* RETURN ARGUMENTS:
          None.
   void FCS_cdbk_search_30b_sub40(FLOAT64 ext [], FLOAT64 pdcfq [], FLOAT64 target [],
                                                  FLOAT64 res[], FLOAT64 hh [], FLOAT64 unfcod [],
25
                                                         INT16 1_sf, INT16 i_sf, INT16 lag, INT16 VUV,
                                                         FLOAT64 pgain, FLOAT64 NSR, FLOAT64 Rp.
                                                                PARAMETER *chan)
30
           INT16 IdxPuls[MAXPN], Sign[MAXPN], SignTab[L_SF4];
           FLOAT64 Criter, Gp_m, Gp;
           FLOAT64 hh_v[L_SF4], rcf[L_SF4];
35
           if ((lag < 20) && (pgain > 0.5))
                   Gp = MIN(pgain, 1.0)*0.75;
40
           else
```

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```
Gp = pgain;
             FCS_Calc_pre_search (hh, hh_v, lag, Gp, &Gp_m, target, ref, SignTab,
 5
                                                         res, 0.5, 1, 1_sf);
             /* 8 pulses CB : 6p \times 3b + 2p \times 4b + 4b \text{ signs} = 30 \text{ bits}
10
             Criter = -1;
             FCS_Simp_Search_CPCB(0, 0, 0, Sign, 1.0, 4, 8, SignTab, ref, &Criter,
                                                                  unfcod, l_sf, IdxPuls);
15
             FCS Simp Scarch_CPCB(1, 1, 0, Sign, 1.0, 4, 8, SignTab, ref, &Criter,
                                                                  unfcod, I_sf, IdxPuls);
                            Sign check on the same track
20
             for (i = 0; i < 4; i++)
                      if(IdxPuls[i] \le IdxPuls[i+4])
25
                               if (Sign[i] != Sign[i+4])
                                       FCS PulseExchangc(IdxPuls+i, Sign+i, IdxPuls+i+4,
                                                                                    Sign+i+4);
30
                               }
                               clsc if (Sign[i] == Sign[i+4])
                                       FCS_PulseExchange(IdxPuls+i, Sign+i, IdxPuls+i+4,
                                                                                    Sign+i+4);
                      }
35
                                Index of the cdbk
                                                                  */
40
```

```
for (i = 0; i < MAXPN; i++)
               chan->idx_cpcb[i_sf][i] = IdxPuls[i];
         for (i = 0; i < MAXPN; i++)
               chan->idx_cpcbsign[i_sf][i] = (Sign[i]+1)/2;
5
         chan-idx_subcpcb[i_sf][0] = 1;
                     Harmonic weighting on unfcod
10
         for (i = lag; i < 1 sf; i++)
               unfcod[i] += Gp_m * unfcod[i-lag];
15
         return;
20
/* FUNCTION : FCS_cdbk_decod_30b_sub40 ().
      */
   /* PURPOSE : This function decode the fixed excitation vector */
            for 8.5 kbps mode 1.
   /* INPUT ARGUMENTS:
         _(INT16 ) l_sf: sub-frame size.
   /*
         _(INT16 ) i_sf: sub-frame index.
         _(INT16 ) lag: pitch lag.
   /*
         _(FLOAT64 ) pgain: decoded pitch gain.
35 /*
         _(INT16 ) rate_m: decoded fix bit-rate.
         _(FLOAT64 ) lpcg: decoded LPC gain.
   /* OUTPUT ARGUMENTS:
         (FLOAT64 []) unfcod: unfiltered excitation signal. */
```

```
359
   /* INPUT/OUTPUT ARGUMENTS:
         (PARAMETER *) chan: output data structure.
   /*____*/
5 /* RETURN ARGUMENTS:
         None.
   void FCS_cdbk_dccod_30b_sub40 (FLOAT64 unfcod [], INT16 l_sf,
                                       INT16 i_sf, INT16 lag, FLOAT64 pgain,
10
                                               PARAMETER *chan)
          INT16 i, IdxPuls[MAXPN], Sign[MAXPN];
15
          FLOAT64 Gp_m;
          for (i = 0; i < MAXPN; i++)
20
                 IdxPuls[i] = chan->idx_cpcb[i_sf][i];
          for (i = 0; i < MAXPN; i++)
                  Sign[i] = 2*chan->idx_cpcbsign[i_sf][i]-1;
25
           /+-----*/
           /* 8 pulses CB : 6p \times 3b + 2p \times 4b + 4b \text{ signs} = 30 \text{ bits}
           for (i = 0; i < 4; i++)
30
                  if (IdxPuls[i] <= IdxPuls[i+4])
                         Sign[i+4] = Sign[i];
                  else
                         Sign[i+4] = -Sign[i];
35
                  }
           FCS_Decod_CPCB(0, Sign, 1.0, 4, 8, unfcod, 1_sf, IdxPuls);
40
```

```
360
                          Pitch enhancement
          if ((lag < 20) && (pgain > 0.5))
5
                 Gp_m = MIN(pgain, 1.0)*0.75;
          else
                  Gp_m = pgain;
           Gp_m = MIN(Gp_m, PAST_PGAIN_MAX);
           Gp m = MAX(PAST_PGAIN_MIN, Gp_m);
10
           for (i = lag; i < l_sf; i++)
                  unfcod[i] = unfcod[i] + Gp_m*unfcod[i-lag];
15
           return;
20
25 /* FUNCTION : FCS_ChangeSign ().
   /* PURPOSE : This function modify the sign of the excitation */
               gain.
30 /* INPUT ARGUMENTS:
          _ (PARAMETER *) chan: output data structure.
   /*
          _(INT16 ) i_sf: sub-frame index.
    /*
          _(INT16 ) l_sf: sub-frame size.
          _(FLOAT64 []) unfcod: unfiltered excitation signal. */
    /*
          _(FLOAT64 []) fcod: filtered excitation signal. */
35 /*
    /* OUTPUT ARGUMENTS:
          None.
 40 /* INPUT/OUTPUT ARGUMENTS:
```

```
361
         (FLOAT64 *) gain: unquntized excitation gain. */
   /* RETURN ARGUMENTS:
          None.
   void FCS_ChangeSign (PARAMETER *chan, INT16 i_sf, INT16 l_sf,
          FLOAT64 unfcod [], FLOAT64 fcod [], FLOAT64 *gain)
10
           INT16 i;
15
           for (i = 0; i < MAXPN; i++)
                   chan->idx_cpcbsign[i_sf][i] *= -1;
           for (i = 0; i < MAXPN; i++)
                   chan->idx_cpcbsign[i_sf][i] += 1;
20
           for (i = 0; i < l_sf; i++)
                   unfcod[i] = -1.0;
           for (i = 0; i < l_sf; i++)
                   fcod[i] *= -1.0;
25
            (*gain) = -(*gain);
            return;
30
35
        ---- END ---
40
```

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```
363
  /* Conexant System Inc.
5 /* 4311 Jamborce Road
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  */
15 /* PROTOTYPE FILE : lib_fcs.h
  /*____*/
  /*-----*/
  void FCS_init_lib
                        (void);
  void FCS_Init_CPCB
                             (void);
25
                             (INT16, INT16, INT16, INT16);
  void FCS_Set_CPCB
                        (FLOAT64 [], INT16);
  void FCS_Init_HF_Noise
30
                        (FLOAT64, INT16, FLOAT64, INT16, FLOAT64 [],
  void FCS_DetermPulsLoc
                                  INT16 [], INT16 *, INT16);
35
                        (INT16, INT16 [], FLOAT64 [], FLOAT64 [], INT16 [],
  void FCS_One_Search_CPCB
                                  INT16 [], FLOAT64 *, FLOAT64 *, FLOAT64 *);
```

void FCS_One_Search_4kCB

364 (INT16 [], INT16, FLOAT64, INT16, INT16 [], FLOAT64 [], FLOAT64 [], INT16 [], INT16 [], FLOAT64 *, FLOAT64 *, FLOAT64 *, INT16);

5 void FCS_Full_Scarch_CPCB

(INT16, INT16 [], FLOAT64, INT16, INT16, INT16 [], FLOAT64 [], FLOAT64 *, FLOAT64 [], INT16, INT16 []);

10 void FCS_Simp_Search_CPCB (INT16, INT16, INT16, INT16 [], FLOAT64, INT16, INT16, INT16 [], FLOAT64 [], FLOAT64 *, FLOAT64 [], INT16, INT16 []);

15 void FCS_Simp_Scarch_4kCB (INT16, INT16, INT16, INT16 [], FLOAT64, INT16, INT16, INT16 [], FLOAT64 [], FLOAT64 *, FLOAT64 [], INT16, INT16 [], INT16, FLOAT64);

20 void FCS_Dccod_CPCB (INT16, INT16 [], FLOAT64, INT16, INT16, FLOAT64 [],

INT16, INT16 []);

25 void FCS_Dccod_PitCB (INT16, INT16 [], FLOAT64, INT16, INT16, FLOAT64 [], INT16,

INT16 [],

INT16, FLOAT64);

30 void FCS_Calc_pre_search(FLOAT64 [], FLOAT64 [], INT16, FLOAT64, FLOAT64 *,

FLOAT64 [], FLOAT64 [], INT16 [], FLOAT64 [], FLOAT64,

INT16, INT16);

35 void FCS_cdbk_scarch_13b_sub54 (FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16, INT16, INT16, INT16, FLOAT64, FLOAT64, FLOAT64, FLOAT64, PARAMETER

*****);

365
void FCS_cdbk_decod_13b_sub54 (FLOAT64 [], FLOAT64 [], FLOAT64 [],
INT16, INT16, INT16, FLOAT64, INT16,
FLOAT64, PARAMETER *);

5 void FCS_cdbk_search_15b_sub80 (FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16, INT16, INT16, INT16, FLOAT64, FLOAT64, FLOAT64, FLOAT64, FLOAT64, FLOAT64, PARAMETER *);

10 void FCS_cdbk_dccod_15b_sub80 (FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16, INT16, INT16, FLOAT64,
INT16, FLOAT64, PARAMETER *);

void FCS_cdbk_scarch_22b_sub40 (FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16, INT16, INT16, INT16, INT16, FLOAT64, FLOAT64, FLOAT64,

PARAMETER *);

20 void FCS_cdbk_decod_22b_sub40 (FLOAT64 [], INT16, INT16,

INT16, FLOAT64, PARAMETER *);

void FCS_cdbk_search_30b_sub40 (FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16, INT16, INT16, INT16, INT16, FLOAT64, FLOAT64, FLOAT64,

FLOAT64,

PARAMETER *);

30 void FCS_cdbk_decod_30b_sub40 (FLOAT64 [], INT16, INT16, INT16, INT16, FLOAT64, PARAMETER *chan);

void FCS_PulseExchange(INT16 *, INT16 *, INT16 *, INT16 *);

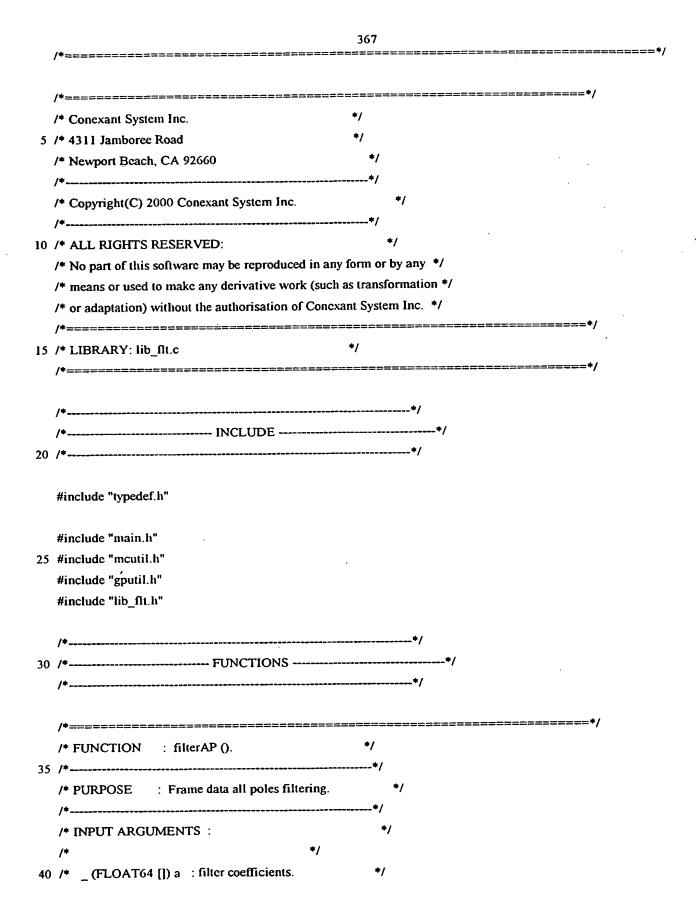
void FCS_ChangeSign (PARAMETER *, INT16, INT16, FLOAT64 [], FLOAT64 *);

40 void FCS_Search_pastcorr (FLOAT64 [], FLOAT64 [], FLOAT64, INT16, FLOAT64, INT16,

366 INT16, INT16, INT16, FLOAT64 [], INT16 [], INT16 *);

5	void FCS_ST_parameter (INT16, INT16, INT16, FLOAT64 [], INT16, FLOAT64 *, FLOAT64 [], FLOAT64);	
	/*=====================================	
0	/**/ /*=============================	

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```
368
   /* _ (FLOAT64 []) x: input signal frame.
   /* _(INT16) P : filter order.
   /* _ (INT16) N : number of input samples.
 5 /* OUTPUT ARGUMENTS:
   /*
                                             */
   /* _(FLOAT64 []) y : output signal frame.
   /* INPUT/OUTPUT ARGUMENTS:
10 /*
   /* _ (FLOAT64 []) buf : input/output memory array.
   /* RETURN ARGUMENTS : _ None.
15
           filterAP (FLOAT64 a [], FLOAT64 x [], FLOAT64 y [], FLOAT64 buf [],
   void
                                                                                                    INT16
   P, INT16 N)
20
            INT16 i, j;
25
            for (i = 0; i < N; i ++)
                    buf [0] = x [i];
                    for (j = P; j > 0; j --)
30
                            buf [0] = (a [j] * buf [j]);
                            buf[j] = buf[j-1];
35
                     y[i] = buf[0];
                    }
40
```

369 return; 5 /* FUNCTION : filterAZ (). 10 /*-----/* PURPOSE : Frame data all zeros filtering. /+_____ /* INPUT ARGUMENTS: 15 /* _(FLOAT64 []) b : filter coefficients. /* _(FLOAT64 []) x: input signal frame. /* _(INT16) P : filter order. /* _ (INT16) N: number of input samples. /*-----20 /* OUTPUT ARGUMENTS: /* _(FLOAT64 []) y : output signal frame. /*_____ /* INPUT/OUTPUT ARGUMENTS: 25 /* /* _(FLOAT64 []) buf : input/output memory array. /+_____*/ /* RETURN ARGUMENTS : _ None. 30 filterAZ (FLOAT64 b [], FLOAT64 x [], FLOAT64 y [], FLOAT64 buf [], void INT16 P, INT16 N) 35 INT16 i, j;

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```
370
           for (i = 0; i < N; i ++)
                  buf [0] =
                                 x [i];
                          [i] = 0.0;
5
                   for (j = P; j > 0; j --)
                                  [i] += buf [j] * b [j];
                          buf [j] = buf [j-1];
10
                   y[i] += (buf[0] * b[0]);
15
            return;
20
    25 /* FUNCTION : filterPZ ().
    /* PURPOSE : Frame data all poles-zeros filtering.
    /* INPUT ARGUMENTS:
 30 /*
    /* _(FLOAT64 []) b : numerator filter coefficients.
    /* _(FLOAT64 []) a : denominator filter coefficients.
    /* _(FLOAT64 []) x: input signal frame.
    /* _(INT16) P : filter order.
 35 /* _(INT16) N : number of input samples.
    /* OUTPUT ARGUMENTS:
                                            */
    /*
    /* _(FLOAT64 []) y : output signal frame.
```

```
371
   /* INPUT/OUTPUT ARGUMENTS:
   /* _ (FLOAT64 []) buf : input/output memory array.
5 /* RETURN ARGUMENTS : _ None.
           filterPZ (FLOAT64 b [], FLOAT64 a [], FLOAT64 x [], FLOAT64 y [], \
   void
                                                                             FLOAT64 buf [], INT16 P, INT16
10 N)
            INT16 i, j;
15
            for (i = 0; i < N; i ++)
20
                    buf [0] = x [i];
                    y[i] = 0.0;
                    for (j = P; j > 0; j --)
25
                             y[i] += (buf[j] * b[j]);
                             buf [0] = (a [j] * buf [j]);
                             buf[j] = buf[j-1];
                            }
30
                    y[i] += (buf[0] * b[0]);
35
             return;
```

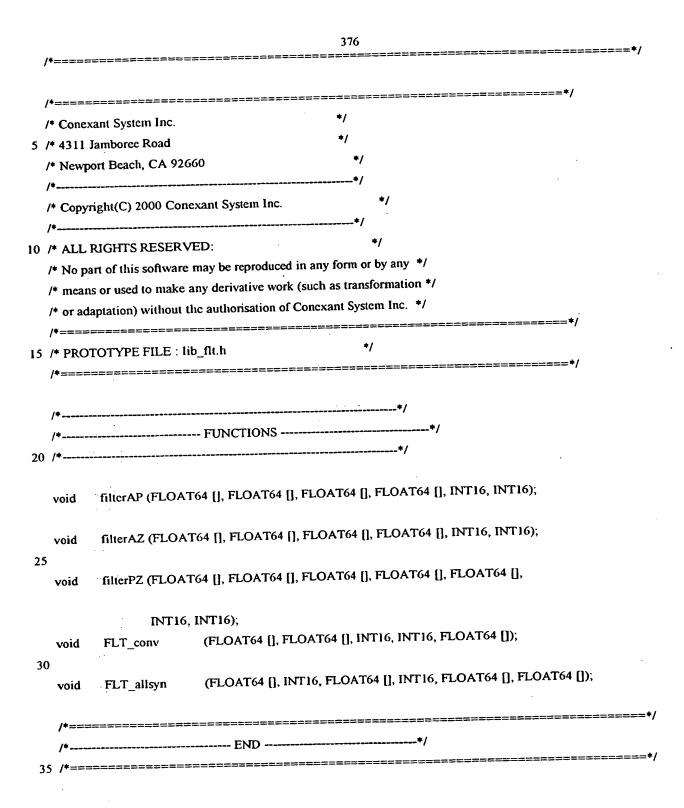
```
/* FUNCTION : FLT_conv ().
5 /* PURPOSE : This function perform a filtering operation
  /*
       The input array, x, is assumed to include past input values */
  /*
       (NCOFS-1 values) for the filter memory, as well as new input */
       values. The array is set up as follows:
   /*
10 /*
                                   Xin(N+NCOFS-1)
              Xin(NCOFS)
   /#
                                    1
                     new data
          I-----II-----
          old |
        Xin(1) Xin(NCOFS-1)
15 /*
       The first NCOFS-1 points of array Xin are assumed to be the */
        "warm-up" points for the first output point, Xout(1).
        _____
20 /* INPUT ARGUMENTS:
   /* (FLOAT64 []) x: input signal frame.
   /* _(FLOAT64 []) a : denominator filter coefficients.
   /* _(INT16) P : filter order.
                 N : number of input samples to be filtered. */
25 /* _(INT16)
   /* OUTPUT ARGUMENTS:
   /* _(FLOAT64 []) y : output signal frame.
30 /* INPUT/OUTPUT ARGUMENTS:
    /* _(FLOAT64 []) buf : input/output memory array.
    /* RETURN ARGUMENTS:
    /* _ None.
    void FLT_conv (FLOAT64 x[], FLOAT64 a[], INT16 P, INT16 N, FLOAT64 y[])
```

```
INT16 i, l, k;
         FLOAT64 sum;
5
         for (i = 0; i < N; i++)
                sum = 0.0;
                1 = P + i;
10
                for (k = 0; k < P; k++)
                      1 = 1;
                      sum += a[k] * x[1];
15
               y[i] = sum;
20
         return;
25
/* FUNCTION : FLT_allsyn ().
  /* PURPOSE : This function filter a signal using an all-pole */
  /* . filter
  /* INPUT ARGUMENTS:
  /* _ (FLOAT64 []) x : input signal frame.
  /* _(FLOAT64 []) a : denominator filter coefficients.
40 /* _(INT16) P : filter order.
```

```
/* _(INT16) N : number of input samples to be filtered. */
   /* OUTPUT ARGUMENTS:
   /* _(FLOAT64 []) y : output signal frame.
   /* INPUT/OUTPUT ARGUMENTS:
   /* _(FLOAT64 []) buf : input/output memory array.
   /* RETURN ARGUMENTS:
10 /* _ None.
   void FLT_allsyn (FLOAT64 x[], INT16 N, FLOAT64 a[], INT16 P, FLOAT64 y[],
                   FLOAT64 buff[])
15
            INT16 i, k;
            FLOAT64 sum;
20
            for (i = 0; i < N; i++)
25
                    sum = x[i];
                    for (k = P-1; k \ge 1; k-)
                            sum = sum + a[k] * buff[k];
30
                            buff(k) = buff(k-1);
                    if (P > 0)
                    sum = sum + a[0]*buff[0];
 35
                    buff[0] = sum;
                    y[i] = sum;
 40
```

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	375
	/**/
	return;
	·
5	/**/
	}
	/**/
10	/*====================================
	/**_*/
	/*
	/**



======			
*======	:======================================		=======================================
Conexant Sys	stem Inc.	*/	
* 4311 Jamboro	ce Road	*/	
* Newport Bead		*/	
+		*/	•
* Copyright(C)	2000 Conexant System Inc.	*/	
/+		*/	
/* ALL RIGHTS	S RESERVED:	*/	
/* No part of thi	is software may be reproduced	in any form or by any */	
* means or use	d to make any derivative worl	k (such as transformation */	
) without the authorisation of		•
/*=======	:======================================		=======================================
/* LIBRARY: li		*/	
¹ ========	:======================================	=0=====================================	=======*/
	INCLUDE		
/*		*/	
#include "typed	ef.h"		
#include "main	.h"		
#include "const	h"		
#include "gputi	1.h"		
#include "mcuti	il.h"	•	
#include "ext_v	/ar.h"		
#include "lib_g	cb.h"		
#include "lib_g #include "lib_fl			
	lt.h"		
#include "lib_fl	lt.h"		
#include "lib_fl #include "gauss	lt.h"	*/	
#include "gauss	lt.h" s_bv.tab"		

	#define a	25173				
	#define c	13849			•	
5	#define m	65536				
		_VAR_NUM	12			
		S_VEC_SIZE				
	#define MAX			2		
10						
••	/*				*/	
	•				*/	
						·
	, ·					
	/ *					=======================================
13					*/	
		1 : GCB_init			•	
	· ·					
,		: This functi		*/	ariable of 7	
		the library GCB.		•	*/	
20						
	/* INPUT AR				*/	
		_ None.		*/		
	•					
	/* OUTPUT A	RGUMENTS:			*/	
25	5 /*	_		*/		
	/*					
	/* INPUT/OU	TPUT ARGUMI	ENTS:		*/	
	/*	_		*/		
	/*				*/	
30	/* RETURN	ARGUMENTS:			*/	
	/ *	_ None.		*/		
	/*=======	=========				=============
	void GCB_in	it lib (void)				
3						
	•				*/	
	/*			_gauss_cb_itu4l		
	, /*		_			
	,					
	0 cee	d exc = 21845.				•

	/*	 */
	/* GCB_gauss_excit_dec	*/
	/*	*/
5		
	Zl_gcb=0;	
	/*	
	/* GCB_gauss_excit	*/
10	/*	
	ref_eng_gcb = 0;	
	rate_mem = RATE8_5K;	•
	- · · · · · · · · · · · · · · · · · · ·	
15	/*	·*/
	/* GCB_gauss_excit_dec	*/
	/*	*/
	$sced_dcc = 0;$	
20		
	/*	
	return;	
25	/*	**/
23	}	,
	. ,	
/	/*	*/
30 /	/+	=======================================
	/* FUNCTION : GCB_dccode_gauss_cb_itu	
•	/*	
1	/* PURPOSE : This function extract the gau	
·	/* the codebook.	*/
	/*	*/
	/* INPUT ARGUMENTS : /* _ (INT16) N_gauss: gaussian code	·
-	/* (INT16) L_vec: gaussian vector	
,	- '	· · ·
,	/* _ (INT16) index: codebook index	t. */

```
380
   /* OUTPUT ARGUMENTS:
           _(FLOAT64 []) unfcod: unfiltered excitation
   /*
                          vector.
5 /*-----
   /* INPUT/OUTPUT ARGUMENTS :
                                              */
                _ None.
   /* RETURN ARGUMENTS:
                                              */
10 /*
                None.
   void GCB_dccode_gauss_cb_itu4k(INT16 N_gauss, INT16 L_vec, INT16 index,
                                                                                        INT16 sign,
15 FLOAT64 unfcod [])
           INT16 k, delay, table_entry;
           FLOAT64 x, *unfcod_p;
20
            if (index >= N_gauss)
25
                    index = (INT16)(N_gauss* GCB_quickuran(&secd_exc));
                    x = GCB_quickuran(&seed_exc);
                    if (x < 0.5)
                           sign = 1;
                    clsc
30
                           sign = -1;
                    }
35
            if (index < N_gauss)
                    delay = (INT16)(index*INV_TABLE_SIZE);
                    table_cntry = (INT16)(index-delay*TABLE_SIZE);
```

```
381
                    if (sign == 1)
                            unfcod_p = unfcod - 2*dclay;
                            for (k = delay; k < L_vec; k++)
                                    unfcod_p[2*k] = bv[table_entry][k];
                            unfcod_p = unfcod + 2*(L_vec - delay);
                            for(k = 0; k < delay; k++)
                                    unfcod_p[2*k] = bv[table_entry][k];
                            }
10
                    else
                             unfcod_p = unfcod - 2*delay;
                             for(k = delay; k < L_vec; k++)
                                    unfcod_p[2*k] = -bv[table_entry][k];
15
                             unfcod_p = unfcod + 2*(L_vcc - delay);
                             for(k = 0; k < delay; k++)
                                    unfcod_p[2*k] = -bv[table_entry][k];
                            }
20
                    }
            elsc
    #ifdef VERBOSE
                     printf("FATAL ERROR: cb index (%d) out of range\n",index);
                     exit(0);
25
    #endif
30
             return;
                                                                */
                      : GCB_gauss_cb_itu4k ().
    /* FUNCTION
 40 /* PURPOSE : This function select the MAX_VEC_NUM gaussian */
```

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```
382
  /*
             vectors in the codebooks.
  /* INPUT ARGUMENTS:
         _(FLOAT64 []) target: target vector.
                                              */
          _(FLOAT64 []) res:
                             residual vector.
          _(INT16 ) i_sf: sub-frame index.
         _(INT16 ) l_sf: sub-frame index size.
         _ (FLOAT64 []) hh:
                             impulse response.
          _(FLOAT64 *) Criter: maximazed criterium.
          _(INT16 ) N_gauss_1: 1st gaussian codebook size. */
10 /*
          _(INT16 ) N_gauss_2: 2nd gaussian codebook size. */
                                                */
  /* OUTPUT ARGUMENTS:
          _ (FLOAT64 []) unfcod: unfiltered excitation
15 /*
                       vector.
                                      */
  /*
          _(INT16 []) index: selected codebook indexes. */
          _(INT16 []) sign: selected vectors sign.
  /* INPUT/OUTPUT ARGUMENTS:
              None.
  /* RETURN ARGUMENTS:
              _ Nonc.
25
  void GCB gauss cb itu4k (FLOAT64 target[], FLOAT64 res[],
                                    FLOAT64 hh [], FLOAT64 *Criter,
                                           FLOAT64 unfcod [], INT16 *index, INT16 sign [],.
                                                 INT16 N_gauss_1, INT16 N_gauss_2)
30
          INT16 index1_buf [NUM_PRESELECT], sign1_buf[NUM_PRESELECT];
          INT16 index2_buf [NUM_PRESELECT], sign2_buf[NUM_PRESELECT];
          INT16 index_tmp[MAX_VEC_NUM], sign_tmp[MAX_VEC_NUM];
35
          FLOAT64 match:
          /*_____*/
          40
```

	/*		*/	
	/*	First basis vector	*/	
	,			
	GCB_basi	s_preselect_itu4k (res, N_gaus	s_1, GAUSS_VEC_	
NUM	PRESELECT	n:		sign1_bu
	-			•
			*/	
	/* /*	Second basis vector	•	
	,			•
	GCB basi	is_preselect_itu4k(res+1, N_ga	uss 2, GAUSS_VEO	C_SIZE, index2_buf,
			_	
	sign2_buf,	NUM_PRESELECT);		
	/*		*/	
	/*=====		Fine search =====	*/
	/*		+/	
	match = 0	GCB_fine_search_itu4k(target,	hh, index1_buf, sign	n1_buf,
				GAUSS_VEC_SIZE, N_gauss_1,
index	2_buf,			
				sign2_buf, GAUSS_VEC_SIZE, N gauss_2, NUM_PRESELECT,
index	tmp.			11_5auss_2, 110112_1100000001,
				sign_tmp);
	•			*/
				,
	,			
	if(match	> (*Criter))		
	. {			
		(*Criter) = match;		,
		index[0] = index_tmp[0]; index[1] = index_tmp[1];		
		sign[0] = sign_tmp[0];		
	•	0(-10		

```
sign[1] = sign_tmp[1];
                 GCB_decode_gauss_cb_itu4k (N_gauss_1, GAUSS_VEC_SIZE,
                                                                    index[0], sign[0],
5
          unfcod);
                 GCB_decode_gauss_cb_itu4k (N_gauss_2, GAUSS_VEC_SIZE,
                                                                    index[1], sign[1], unfcod+1);
                 }
10
          return;
15
                                             _____
                                                        */
   /* FUNCTION : GCB_basis_preselect_itu4k ().
   /* PURPOSE : This function pre-select the 2 gaussian vectors */
               in the codebooks.
   /*
   /* INPUT ARGUMENTS:
           _ (FLOAT64 []) res : residual vector.
25 /*
           _(INT16 ) N_gauss: gaussian codebook size. */
   /*
                                 gaussian vector size. */
           _(INT16 ) L_vec:
           _(INT16 ) skip_fac: skip factor.
                     ) num_preselect: number of pre-selected */
           _(INT16
                                            */
                           candidates.
30 /*
    /* OUTPUT ARGUMENTS:
           _(INT16 []) index_buf: codebook index buffer. */
    /*
           _ (INT16 []) sign_buf:
                                  codebook sign buffer. */
    /*
    /* INPUT/OUTPUT ARGUMENTS:
                                            */
                _ None.
    /* RETURN ARGUMENTS:
                                            */
 40 /*
                None.
```

```
void GCB_basis_preselect_itu4k (FLOAT64 res[], INT16 N_gauss, INT16 L_vec,
                                                              INT16 index_buf [], INT16 sign_buf [],
                                                                      INT16 num_preselect)
 5
           INT16 i, j, k, table_entry, delay, sign;
                            numer[NUM_PRESELECT], tstnum, x, *res_p;
10
           FLOAT64
            for (i = 0; i < num\_preselect; i++)
15
                    {
                                             = -1.0;
                    numer[i]
                    index buf[i]
                                    = 0;
                    sign_buf[i]
                                    = 1;
20
                         Preselection of the basis vectors
            for (i = 0; i < N_{gauss}, i++)
25
                    {
                     delay = (INT16)(i*INV_TABLE_SIZE);
                     table_entry = (INT16)(i-delay*TABLE_SIZE);
30
                    /* Correlation between target and codebook vector
                     res_p = res - 2*delay;
35
                     for(tstnum=0.0, j=delay; j<L_vec; j++)
                             tstnum += res_p[2*j]*bv[table_entry][j];
                     res_p = res + 2*(L_vec - delay);
                     for(j=0; j<delay; j++)
40
```

```
386
                             tstnum += res_p[2*j]*bv[table_entry][j];
                    if (tstnum \geq 0.0)
 5
                             sign = 1;
                    else
                              sign = -1;
                              tstnum = -tstnum;
10
                              }
                           NOTE: see AMR fixed-point code for low complexity */
                         (and bit-exact) approachCorrelation between target and */
15
                                      codebook vector
                      if (tstnum > numer[0])
                              {
20
                               numer[0] = tstnum;
                               index_buf[0] = i;
                               sign_buf[0] = sign;
 25
                               for (j=0; j<num_preselect-1; j++)
                                        if (numer[j] > numer[j+1])
 30
                                                 x = numer[j];
                                                 numer[j] = numer[j+1];
                                                 numer[j+1] = x;
                                                 k = index_buf[j];
                                                 index_buf[j] = index_buf[j+1];
 35
                                                 index_buf[j+1] = k;
                                                 k = sign_buf[j];
                                                 sign_buf[j] = sign_buf[j+1];
                                                 -sign_buf[j+1] = k;
                                                 }
  40
```

```
387
 5
           rcturn;
10
                  : GCB fine_search_itu4k ().
   /* FUNCTION
                   : This function pre-select the 2 gaussian vectors */
   /* PURPOSE
   /*
                in the codebooks.
   /* INPUT ARGUMENTS:
            (FLOAT64 []) target:
                                    taget vector.
                                    impulse response.
   /*
            (FLOAT64 []) hh:
                                    1st codebook vector
   /*
            _ (INT16 []) index1:
                             indexes.
25 /*
   /*
                                   1st codebook vector signs.*/
            _(INT16 []) sign1:
            _(INT16 )11:
                                 1st gaussian vector size. */
                                  1st gaussian codebook */
            (INT16 ) NI:
                             sizc.
                                    2nd codebook vector
30 /*
            __(INT16 []) index2:
                                               */
                             indexes.
                                   2nd codebook vector signs.*/
            _(INT16 []) sign2:
            _(INT16 ) 12:
                                 2nd gaussian vector size. */
                                  2nd gaussian codebook */
            _(INT16 ) N2:
                             size.
35 /*
            _(INT16 ) num_preselect: number of pre-selected */
                             vectors.
   /* OUTPUT ARGUMENTS:
            _ (INT16 []) index_tmp: selected codebook vector */
40 /*
```

```
388
                       indexes.
  /*
          _(INT16 []) sign_tmp: selected codebook vector
                       signs.
5 /* INPUT/OUTPUT ARGUMENTS:
                                         */
  /*
              _ None.
  /* RETURN ARGUMENTS:
          _(FLOAT64 ) tmax :
                               maximized criterion.
FLOAT64 GCB_fine_search_itu4k(FLOAT64 target [], FLOAT64 hh [],
                          INT16 *index1, INT16 *sign1, INT16 II, INT16 N1,
                          INT16 *index2, INT16 *sign2, INT16 12, INT16 N2,
15
                           INT16 num_preselect, INT16 *index_tmp,
                           INT16 *sign_tmp)
20
           INT16 i1, i2, l_sf;
          FLOAT64 unfcod_tmp[L_SF], fcod_tmp[L_SF], tstden, tstnum, x, tmax;
           FLOAT64
25
           1 \text{ sf} = 11 + 12
30
           buf = dvector (0, 1_sf-1);
35
           tmax = -1.0;
            for (i1 = 0; i1 < num_preselect; i1++)
                  GCB_decode_gauss_cb_itu4k (N1, 11, index1[i1], sign1[i1],
```

```
unfcod_tmp);
                     for(i2=0; i2<num_preselect; i2++)
5
                             GCB_dccode_gauss_cb_itu4k (N2, 12, index2[i2], sign2[i2],
                    unfcod_tmp+1);
10
                                       Compute the filtered random vector
                             ini_dvector (buf, 0, 1_sf-1, 0.0);
                             filterAZ (hh, unfcod_tmp, fcod_tmp, buf, (INT16)(l_sf-1),
15
                                      l_sf);
                                                                         */
                                            Compute the error
20
                                                      fcod_tmp, &tstnum, 0, 1_sf-1);
                             dot dvector (target,
                             dot_dvector (fcod_tmp, fcod_tmp, &tstden, 0, 1_sf-1);
                             tstden += EPSI;
25
                             x = tstnum*tstnum/tstden;
                                         Maximisation of the criterium
30
                             if (x > tmax)
                                      {
35
                                      tmax = x;
                                      index_tmp[0] = index1[i1];
                                      index_tmp[1] = index2[i2];
                                      sign_tmp[0] = sign1[i1];
                                      sign_tmp[1] = sign2[i2];
40
                                      }
```

	}		
	/*		*/
	<i>,</i> }		
•	/*		* /
	free_dvector (buf, 0, 1_s	af-1);	
	/*	·	*/
	return tmax;		
	/*		+/
	}		
/*			*/
/* FUN	CTION : GCB_quick	curan ().	*/
•	POSE : This function		
	with mean zero an		*/ _*/
•	JT ARGUMENTS :		*/
	_ None.	*/	·-*/
	PUT ARGUMENTS :		*/
/ *	_ None.	*/	**
•	JT/OUTPUT ARGUMEN		* / */
/*		random generator se	ed. */
	URN ARGUMENTS:	random gaussian val	*/ uc */
/*		Ittiliaetii Baaceiaii tai	

	391
	{ /**/
	FLOAT64 val;
5	/**/
	seed = (a (*seed) + c) % m;
0	val = (FLOAT64) (*seed) / (FLOAT64) m;
	/* <u></u> */
_	return val;
5	/**/
	} /**/
0	/*/*
,	/*====================================
	/* PURPOSE : This function generate Gaussian random values */
	/**/
	/* INPUT ARGUMENTS : */ /* _ Nonc. */
	/**/ /* OUTPUT ARGUMENTS : */
	/*None. */ /**/
	/* INPUT/OUTPUT ARGUMENTS : */ /* _ (INT64 *) seed : random generator seed. */
	/**/ /* RETURN ARGUMENTS : */
	/* _ (INT64 *) val: random gaussian value. */ /*==================================
1 0	FLOAT64 GCB_gauss_noise (INT64 *seed)

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	392
	{ /**/
	/ *
	INT16 i;
5	FLOAT64 sum, val;
	**
	/ * */
	sum = 0.0;
10	for $(i = 0; i < UNIF_VAR_NUM; i++)$
	sum += GCB_quickuran (seed);
	*/
	/**/
15	val = sum - (FLOAT64)UNIF_VAR_NUM / 2.0;
	/**/
20	return val;
20	/**/
	}
	/**/
25	/*====================================
	/* FUNCTION : GCB_gauss_excit_dec (). */
	/**/
	/* PURPOSE : This function generate Gaussian noise for the */
30	/* decoder at very low bit-rate coding. */ /**/
	/* INPUT ARGUMENTS : */
	/* _ (INT16) rate : fixed bit-rate. */
	/**/
35	/* OUTPUT ARGUMENTS : */
	/* _ (FLOAT64 []) ext : gaussian excitation signal. */
	/**/ /* INPUT/OUTPUT ARGUMENTS : */
	/* INPUT/OUTPUT ARGUMENTS. /*(INT64 *) sced : random generator seed. */
40	/**/

```
393
  /* RETURN ARGUMENTS:
             None.
5 void GCB_gauss_excit_dec (INT64 *seed, INT16 rate, FLOAT64 ext[])
         INT16 i;
         FLOAT64 C;
10
         C = 0.0;
         for (i = 0; i < L_FRM; i++)
15
                cxt[i] = GCB_gauss_noise(seed) + C*Zl_gcb;
                Z1_gcb = cxt[i];
                }
20
         return;
25
/* FUNCTION : GCB_gauss_excit ().
   /* PURPOSE : This function generate Gaussian noise for the */
   /* encoder at very low bit-rate coding.
35 /*-----
   /* INPUT ARGUMENTS:
         _ (INT16 ) rate: fixed bit-rate.
         _(INT16 ) rate_m : past fixed bit-rate.
         _ (FLOAT64 ) NSR: estimated noise to signal ratio.*/
         (FLOAT64 []) ResEng: estimated residual energy.
40 /*
```

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```
394
   /* OUTPUT ARGUMENTS:
                                 gaussian excitation signal. */
           _ (FLOAT64 []) ext :
           _ (FLOAT64 []) gc :
                                 gain scaling factor.
   /* INPUT/OUTPUT ARGUMENTS:
           (INT64 *) seed: random generator seed.
   /* RETURN ARGUMENTS:
10 /*
                None.
   void GCB_gauss_excit (INT64 *seed, INT16 rate, INT16 rate_m, FLOAT64 NSR,
                                  FLOAT64 ResEng[], FLOAT64 ext[], FLOAT64 gc[])
15
           FLOAT64 x;
20
           GCB_gauss_excit_dec(seed, rate, ext);
           if (rate == RATE2_0K)
25
                   {
                   ref_eng_gcb = ResEng[0];
                   dot_dvector(ext, ext, &x, 0, L_FRM/2-1);
                   gc[0] = 0.7*sqrt(MAX(ref_eng_gcb-80*4, 0) / MAX(x, 0.0001));
30
                    ref_eng_gcb = ResEng[1];
                    dot_dvector(ext+L_FRM/2, ext+L_FRM/2, &x, 0, L_FRM/2-1);
35
                    gc[2]=0.7*sqrt(MAX(ref_eng_gcb-80*4, 0) / MAX(x, 0.0001));
                   }
            else
                    if ((rate_mcm != RATE0_8K) || (rate_m != RATE0_8K))
40
```

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```
395
                           ref_eng_gcb = ResEng[0] + ResEng[1];
                   else
                           rcf_eng_gcb = 0.5*rcf_eng_gcb + 0.5*(ResEng[0]+ResEng[1]);
5
                    dot_dvector (ext, ext, &x, 0, L_FRM-1);
                    gc[0] = 0.7*sqrt(MAX(ref_eng_gcb-160*6, 0) / MAX(x, 0.0001));
10
                            Memory Up-date
            rate_mem = rate_m;
15
            return;
20
```

```
396
  */
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  /* Newport Beach, CA 92660
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  15 /* PROTOTYPE FILE : lib_gcb.h
                                    */
  /+____=
  /+_____*/
  /*-----*/
20 /*----*/
                                (void);
       GCB_init_lib
  void
                           (INT16, INT16, INT16, FLOAT64 []);
  void
       GCB_decode_gauss_cb_itu4k
25
       GCB_gauss_cb_itu4k
                                (FLOAT64 [], FLOAT64 [], FLOAT64 [],
  void
                                          FLOAT64 *, FLOAT64 [], INT16 *,
                                               INT16 [], INT16, INT16);
30
                           (FLOAT64 [], INT16, INT16, INT16 [],
       GCB_basis_preselect_itu4k
  void
                                          INT16 [], INT16);
            GCB_fine_search_itu4k
                                (FLOAT64 [], FLOAT64 [],
35 FLOAT64
                                          INT16 *, INT16 *, INT16, INT16, INT16
                                               INT16 *, INT16, INT16, INT16,
                                                    INT16 *, INT16 *);
```

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	FLOAT	64	GCB_quickuran	(INT64 *);
5	FLOAT	64	GCB_gauss_noise	(INT64 *);
	void	GCB_g	auss_excit_dec	(INT64 *, INT16, FLOAT64 []);
10	void	GCB_g	auss_excit	(INT64 *, INT16, INT16, FLOAT64, FLOAT64 [], FLOAT64 []);
15			END	

398 */ /* Conexant System Inc. 5 /* 4311 Jamboree Road /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ /*----*/ 15 /* LIBRARY: lib_geq.c /+____*/ /*----*/ #include "typcdef.h" #include "main.h" 25 #include "const.h" #include "gputil.h" #include "ext_var.h" 30 #include "lib_geq.h" ----*/ 35 /* FUNCTION : GEQ_init_lib (). : This function initilise the global variables of */ /* PURPOSE the GEQ library. 40 /*

```
399
  /* INPUT ARGUMENTS:
                                             */
  /*
                    _ None.
5 /* OUTPUT ARGUMENTS:
                                             */
                    _ None.
   /* INPUT/OUTPUT ARGUMENTS:
                    None.
10 /*-
   /* RETURN ARGUMENTS:
                    _ Nonc.
15 void GEQ_init_lib(void)
        INT16 i.j;
20
           ini_dvector(past_energyq_2d, 0, GVQ_VEC_SIZE_2D-1, -20.0);
           ini_dvector(past_energyq_3d, 0, GVQ_VEC_SIZE_3D-1, -20.0);
           ini_dvector(past_energyq_4d, 0, GVQ_VEC_SIZE_4D-1, -20.0);
25
           ini_dvector(gp_buf, 0, GP_BUF_SIZE-1, 0.0);
                                  = 0.0;
           past_fixed_energy
                                         = PAST PGAIN_MIN;
           pgain_past
30
                                  = PAST_PGAIN_MIN;
           pgain_past_dcc
           ini_dvector(Prev_Beta_Pitch, 0, BETA_BUF_SIZE-1, 0.0);
35
           GainNormDeci = 0;
            GainModiDeci = 0;
            energy_pred_coeff_1[0] = 0.6;
40
```

```
energy_pred_coeff_1[1] = 0.3;
             energy_pred_coeff_1[2] = 0.1;
             cnergy_pred_coeff_2[0] = 0.40;
  5
             cnergy_pred_coeff_2[1] = 0.25;
             energy_pred_coeff_2[2] = 0.10;
             energy_pred_coeff_3[0] = 0.300;
             energy_pred_coeff_3[1] = 0.150;
 10
             cnergy\_pred\_cocff\_3[2] = 0.075;
             energy_pred_coeff4d_1[0] = 0.7;
             energy_pred_coeff4d_1[1] = 0.6;
             cncrgy_pred_coeff4d_1[2] = 0.4;
15
             cnergy_pred_coeff4d_1[3] = 0.2;
             energy_pred_coeff4d_2[0] = 0.40;
             cnergy_pred_coeff4d_2[1] = 0.20;
             energy_pred_coeff4d_2[2] = 0.10;
20
             energy_pred_coeff4d_2[3] = 0.05;
             energy_pred_coeff4d_3[0] = 0.300;
             energy_pred_coeff4d_3[1] = 0.20;
             energy_pred_coeff4d_3[2] = 0.075;
25
             energy_pred_coeff4d_3[3] = 0.025;
            energy_pred_coeff4d_4[0] = 0.20;
            energy_pred_coeff4d_4[1] = 0.075;
            energy_pred_coeff4d_4[2] = 0.025;
30
            energy_pred_coeff4d_4[3] = 0.0;
                             VQ Tables
                                                         */
35
            for (i = 0; i < MSMAX_2_128; i++)
                    for (j = 0; j < GVQ_VEC_SIZE 2D; j++)
                gain_cb_2_128[i][j] = gainVQ_2_128[i][j];
                gain_cb_2_128_8_5[i][j] = gainVQ_2_128_8_5[i][j];
40
```

	,	
	/**/	
	return;	
	/**/	
	}	•
4	/**/	
	/*====================================	=====
	/* FUNCTION : GEQ_gainVQMA_2 (). */ /**/	•
	/* PURPOSE : This function performs 2-D VQ of Gp and Gc with */	
	/* 7 bits. */	
	/**/	
	/* INPUT ARGUMENTS : */	
	/* _(FLOAT64 []) Tgs: target signal. */	
	/* _(FLOAT64 []) unfcod: adaptive and fixed codebook */	
	/* excitation. */	
	/*(FLOAT64 []) fcod: filtered adaptive and fixed */	
	/* codebook excitation. */	
	/* _ (INT16) 1_sf: length of subframe. */	
	/**/	
	/* OUTPUT ARGUMENTS : */	
	/* _ (FLOAT64 []) gainQ: Quantized adaptive and */	
	/* fixed codebook gains. */	
	/* _ (INT16 *) idx: codebook index. */	
	/*	
	/* INPUT/OUTPUT ARGUMENTS : */	
	/* _ None. */	
	/**/	
	/* RETURN ARGUMENTS : */	
į		

```
402
           FLOAT64 energy, pred_gain, dist, dist_min, g_pitch, g_cdbk,
                   corr[CORR_SIZE];
 5
           INT16 i;
           INT16 num_candidate;
           FLOAT64 Renergy;
           FLOAT64 unq_Renergy;
           FLOAT64 pred_energy, err_energy;
10
        FLOAT64 **ptr;
           15
           /* Note: compute the cdbk energy with mean removed (i.e., -30dB) */
20
           if (rate == RATE4 0K)
                  {
                  dot_dvector (past_energyq_2d, energy_pred_coeff_1,
                                               &pred_energy, 0, GVQ_VEC_SIZE_2D-1);
                  ptr = gain_cb_2_128;
25
                  }
           else
                  dot_dvector (past_energyq_4d, energy_pred_coeff4d_1,
                                               &prcd_energy, 0, GVQ_VEC_SIZE_4D-1);
30
                  ptr = gain_cb_2_128_8_5;
                  }
           prcd_energy = MAX (pred_energy, -20.0);
35
           dot_dvector(unfcod[1], unfcod[1], &energy, 0, 1_sf-1);
           Renergy = energy;
           energy = 30.0 - 10.0 \cdot \log 10 (energy/(FLOAT64)) \cdot sf+EPS1);
40
```

```
403
                         Compute the predicted energy
           energy += pred_energy;
5
                         compute the predicted gain
           pred_gain = pow(10.0, energy/20.0);
10
           err_energy = -cncrgy + 20*log10(fabs(gainQ[1]) + EPSI);
           err energy = pow(10.0, err_energy/20.0);
           unq_Renergy = 10.*log10(sqr(gainQ[1])*Renergy+EPSI);
15
            dist_min = MAXFLT;
20
            num_candidate = 128;
            dot_dvector (fcod[0], fcod[0], &corr[0], 0, 1_sf-1);
            dot dvector (fcod[0], Tgs, &corr[1], 0, 1_sf-1);
            corr[1] *= -2.0;
25
            dot_dvector(fcod[1], fcod[1], &corr[2], 0, 1_sf-1);
            dot_dvector(fcod[1], Tgs, &corr[3], 0, l_sf-1);
            corr[3] *= -2.0;
30
            dot_dvector(fcod[0], fcod[1], &corr[4], 0, l_sf-1);
            corr[4] *= 2.0;
35
            for (i = 0; i < num\_candidate; i++)
                    g_{in} = ptr[i][0];
                    g_cdbk = pred_gain*ptr[i][1];
                    dist = sqr(g_pitch)*corr[0] + g_pitch*corr[1] +
                                            sqr(g_cdbk)*corr[2] + g_cdbk*corr[3] +
 40
```

404 g_pitch*g_cdbk*corr[4];

```
if (dist < dist_min)
  5
                               dist_min = dist;
                               (*idx) = i;
                      }
 10
                              get the quantized gains
 15
               gainQ[0] = ptr[*idx][0];
               gainQ[1] = prcd_gain*ptr[*idx][1];
                           update past quantized energies
20
             if( rate == RATE4_0K)
                     for (i = GVQ_VEC_SIZE_2D-1; i > 0; i--)
25
                             past_energyq_2d[i] = past_energyq_2d[i-1];
                     past_energyq_2d[0] = 20.0*log10(gainVQ_2_128[*idx][1]);
                     } .
30
             else
                     for (i = GVQ\_VEC\_SIZE\_4D-1; i > 0; i-)
                             past_energyq_4d[i] = past_energyq_4d[i-1];
35
                     past_energyq_4d[0] = 20.0*log10(gainVQ_2_128_8_5[*idx][1]);
40
             return;
```

	/**/
	}
5	/**/
	/**/
	/* FUNCTION : GEQ_dec_gains_2_7 (). */
	/ ** /
10	/* PURPOSE : This function decodes the 7 bit 2-D VQ of Gp */
	/* and Gc. */
	/**/
	/* INPUT ARGUMENTS : */ /* (INT16) Lag: current pitch lag. */
	/* _ (INT16) Lag: current pitch lag. */ /* _ (INT16) index: codebook index. */
	/* _(INT16) N_bfi: duration of bad frame */
	/* seq. (0=good frame) */
*	/* _(FLOAT64 []) unfcod: adaptive and fixed */
	/* codebook excitation. */
20	/* _ (INT16) i_sf: subframe number. */
	/* _(INT16) l_sf: length of subframe. */
	/**/ /* OUTPUT ARGUMENTS : */
	/* _ (FLOAT64 []) gainQ: Quantized adaptive and */
25	
	/*
	/* INPUT/OUTPUT ARGUMENTS : */
	/* _ None. */
	/**/
30	/* RETURN ARGUMENTS : */
	/*None. */ /*==================================
	, · <u> </u>
	void GEQ_dcc_gains_2_7(INT16 rate, INT16 Lag, INT16 index, FLOAT64 gainQ [],
35	INT16 N_bfi, FLOAT64 unfcod [], INT16 i_sf, INT16 l_sf)
	(
	/**/
	DITTIC : www. counts
40	INT16 i, num_count;
÷۷	

```
406
            FLOAT64 current_fixed_energy;
            FLOAT64 energy, pred_gain;
            FLOAT64 pdown, cdown;
            FLOAT64 val;
 5
            FLOAT64 min_erg_27;
                  Compute the cdbk energy with mean removed (i.e., -30dB) */
10
            dot_dvector (unfcod, unfcod, &energy, 0, 1_sf-1);
            current_fixed_cncrgy = energy;
            energy /= (FLOAT64)l_sf;
15
            energy = 30.0 - 10.0*log10(energy+EPSI);
                           Compute the predicted energy
                                                                */
20
         if (rate == RATE4_0K)
              dot_dvector (past_energyq_2d, energy_pred_coeff_1, &val, 0,
                       GVQ_VEC_SIZE_2D-1);
         cise
25
           dot_dvector (past_energyq_4d, energy_pred_coeff4d_1, &val, 0,
                GVQ_VEC_SIZE_4D-1);
            val = MAX (val, -20.0);
30
            energy += val;
                            Compute the predicted gain
35
            pred gain = pow(10.0, (energy / 20.0));
            if (N bfi == 0)
40
```

```
407
                if (rate == RATE4_0K)
5
                      {
                       gainQ[0] = gainVQ_2_128[index][0];
                       gainQ[1] = prcd_gain*gainVQ_2_128[index][1];
                               update past quantized energies
10
                       for (i = GVQ\_VEC\_SIZE\_2D-1; i > 0; i--)
                             past_energyq_2d[i] = past_energyq_2d[i-1];
15
                       past\_energyq\_2d[0] = 20.0*log10(gainVQ\_2\_128[index][1]);
                       }
                clse
20
                       gainQ[0] = gainVQ_2_128_8_5[index][0];
                       gainQ[1] = pred_gain*gainVQ_2_128_8_5[index][1];
                       for (i = GVQ\_VEC\_SIZE\_4D-1; i > 0; i--)
                             past_energyq_4d[i] = past_energyq_4d[i-1];
25
                       past_energyq_4d[0] = 20.0*log10(gainVQ_2_128_8_5[index][1]);
                }
30
          else
                 35
                    set down-scaling according to number of bad frames */
40
```

```
switch (N_bfi)
                         case 1:
                                        pdown = 0.95;
5
                                        cdown = 0.75;
                                        break;
                          case 2:
                                        pdown = 0.95;
                                         cdown = 0.75;
10
                                         break;
                          case 3:
                                         pdown = 0.90;
                                         cdown = 0.75;
                                         break;
15
                          case 4:
                                         pdown = 0.90;
                                         cdown = 0.40;
                                         break;
                          case 5:
20
                                         pdown = 0.90;
                                         cdown = 0.30;
                                         break;
                           case 6:
                                         pdown = 0.90;
25
                                         cdown = 0.20;
                                         break;
                           dcfault:
                                          pdown = 0.70;
 30
                                          cdown = 0.20;
                                          break;
                           }
 35
                    if (N_bfi == 1)
 40
```

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```
409
                            if (i_sf == 0)
                                     if (Lag <= 40)
                                             gp_mcan = 0.5*(gp_buf[6]+gp_buf[7]);
                                     elsc if (Lag <= 80)
 5
                                              gp_mean = 0.0;
                                              num_count = 0;
                                              for (i = 4; i < 8; i++)
10
                                                      if (gp_buf[i] \ge 0.5)
                                                              gp_mean += gp_buf[i];
                                                              num_count++;
15
                                                      }
                                              if (num_count > 0)
                                                     gp_mean /= (FLOAT64)num_count;
                                              else
                                                      gp_mcan = MAX(MAX(gp_buf[6], gp_buf[7]),
20
            gp_buf[5]);
                                     else if (Lag <= 120)
25
                                              gp_mcan = 0.0;
                                              num_count = 0;
                                              for (i = 2; i < 8; i++)
                                                      if (gp\_buf[i] \ge 0.5)
30
                                                               gp_mean += gp_buf[i];
                                                               num_count++;
                                                      }
35
                                              if (num_count > 0)
                                                      gp_mean /= (FLOAT64)num_count;
                                              else
                                                       gp_mean = MAX(MAX(MAX(gp_buf[6], gp_buf[7]),
                                                                                       gp_buf[5]), gp_buf[4]);
40
```

```
410
                                           }
                                    else
                                            gp_mean =0.0;
                                            num_count =0;
5
                                            for (i = 0; i < 8; i++)
                                                     if (gp\_buf[i] \ge 0.5)
                                                             gp_mean += gp_buf[i];
10
                                                             num_count++;
                                                    }
                                             if (num_count > 0)
15
                                                     gp_mean /= (FLOAT64)num_count;
                                             else
                                                     gp\_mcan = MAX(MAX(MAX(MAX(gp\_buf[6], gp\_buf[7]),
                                                                             gp_buf[5]), gp_buf[4]),
20 gp_buf[3]);
                                             }
    #ifdef ALGO_BUG_FIX
                                     if (rate == RATE4_0K)
 25
                                              if (MAX(Prev_Beta_Pitch[0], Prev_Beta_Pitch[1]) > 0.70)
                                                     gainQ[0] = 0.95;
                                              else
                                                      gainQ[0] = MIN(0.95,gp_mcan);
                                              }
 30
                                      else
                                              if (MAX(Prev_Beta_Pitch[0], MAX(Prev_Beta_Pitch[1],
                                                               MAX(Prev_Beta_Pitch[2],
                                                                       Prev_Beta_Pitch[3]))) > 0.70)
  35
                                                                               gainQ[0] = 0.95;
                                               else
                                                      gainQ[0] = MIN(0.95,gp_mean);
                                              }
  40 #else
```

```
if (MAX(Prev_Beta_Pitch[0], Prev_Beta_Pitch[1]) > 0.70)
                                            {
                                             gainQ[0] = 0.95;
                                             cdown = 0.0;
 5
                                    else
                                            gainQ[0] = MIN(0.95,gp_mcan);
   #endif
                                    }
10
                            else
                                    gainQ[0] = gp_fec;
                            }
                    else
15
                            gainQ[0] = gp_fec;
                            gainQ[0] *= pdown;
                    gp_fec = gainQ[0];
20
                    gainQ[1] = sqrt(past_fixed_energy/current_fixed_energy);
                    gainQ[1] *= cdown;
                               update past quantized energies
25
                    if (rate == RATE4_0K)
                             min_erg_27 = MIN(past_energyq_2d[0], past_energyq_2d[1]);
30
                             for (i = GVQ_VEC_SIZE_2D-1; i > 0; i-)
                                     past_energyq_2d[i] = past_energyq_2d[i-1];
                             past_energyq_2d[0] = MIN(min_erg_27, -energy +
35
                                             20*log10(fabs(0.6*gainQ[1]) + EPSI));
                            }
                     clse
                             min_erg_27 = MIN(past_energyq_4d[0], past_energyq_4d[1]);
 40
```

```
min_erg_27 = MIN(min_erg_27, past_energyq_4d[2]);
                             min_erg_27 = MIN(min_erg_27, past_energyq_4d[3]);
                             for (i = GVQ\_VEC\_SIZE\_4D-1; i > 0; i--)
  5 -
                                     past_energyq_4d[i] = past_energyq_4d[i-1];
                             past_energyq_4d[0] = MIN(min_erg_27, -energy +
                                                    20*log10(fabs(0.6*gainQ[1]) + EPSI));
                            }
 10
 15
                        Update the past fixed codebook energy
             past_fixed_energy = current_fixed_energy*sqr(gainQ[1]);
20
            return;
25
   /* FUNCTION : GEQ_Quant_PitchGain_3D ().
35 /* PURPOSE : This function performs 3-D VQ of Ga1, Ga2, and */
                Ga3 with 4 bits.
   /* INPUT ARGUMENTS:
                     _ None.
```

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```
413
   /* OUTPUT ARGUMENTS:
         (INT16) idx: codebook index.
   /* INPUT/OUTPUT ARGUMENTS:
                            Gp: unquantized/quantized pitch
         _ (FLOAT64 [])
   /* RETURN ARGUMENTS:
   void GEQ_Quant_PitchGain_3D(FLOAT64 *Gp, INT16 *idx)
15
           INT16 i;
           FLOAT64 min_dist;
           FLOAT64 dist;
           FLOAT64 em[GVQ_VEC_SIZE_3D];
20
           min_dist = MAXFLT;
           for(i = 0; i < TAB_SIZE_GVQ_3D; i++)
25
                  dif_dvector (Gp, gp3_tab[i], err, 0, GVQ_VEC_SIZE_3D-1);
                  dot_dvector (err, err, &dist, 0, GVQ_VEC_SIZE_3D-1);
30
                  if (dist < min_dist)
                          {
                          min_dist = dist;
                          (*idx) = i;
                          }
35
                  }
           for (i = 0; i < GVQ_VEC_SIZE_3D; i++)
                  Gp[i] = gp3_tab[(*idx)][i];
40
```

	retum;			
	/*			*/
	} .			
			+/	
/*				
				=======================================
	CTION : Dec_PitchGa			1
•	POSE : This function			, */
	Ga2, and Ga3.		*/	
	·		+/	
	T ARGUMENTS :		*/	
/*	_ (INT16) N_bfi:			
/*		(0=good frame)		
	_(INT16) i_sf:			
	_ (INT16) index:			
	TO AD CLID SENERC		*/	
	PUT ARGUMENTS: _(FLOAT64 []) gain	O Ouantized a	daptive */	
17	(LTOYIOA ()) Sun	.4.	•	
/*	code	ebook gains.	*/	
/* /*		ebook gains.	*/	
/* /*5 /* INP	code T ARGUMENTS: Nonc.	ebook gains. 	*/ */ */	
/* /*5 /* INP	code	ebook gains. 	*/ */ */	
/* /* 5 /* INP /* /* /* /* RE1	code UT ARGUMENTS : _ Nonc. TURN ARGUMENTS :	*/	*/ */ */ */ */	
/* /* 5 /* INP /* /* /* /* RET	code UT ARGUMENTS : _ None. TURN ARGUMENTS :	*/	*/ */ */ */	·
/* /* 5 /* INP /* /* /* /* RET	code UT ARGUMENTS : _ None. TURN ARGUMENTS :	*/	*/ */ */ */	· ·===================================
/* /* 5 /* INP /* /* RE1 /* 0 /*===	code UT ARGUMENTS : _ Nonc. TURN ARGUMENTS : _ Nonc.	*/	*/ */ */ */	
/* /* /* RET /* 0 /*====	code UT ARGUMENTS : _ None. TURN ARGUMENTS : _ None. ===================================	*/	*/ */ */ */	=* index, FLOAT64 *gainQ)
/* /* /* RE7 /* 0 /*===:	code UT ARGUMENTS: _ Nonc. TURN ARGUMENTS: _ Nonc. ===================================	*/	*/*/*/ */*/ */*/ 16 i_sf, INT16	index, FLOAT64 *gainQ)
/* /* /* INP /* /* /* RE1 /* 0 /*===:	code UT ARGUMENTS : _ None. TURN ARGUMENTS : _ None. ===================================	*/	*/*/*/ */*/ */*/ 16 i_sf, INT16	index, FLOAT64 *gainQ)
/* /* /* INP /* /* /* RET /* 0 /*===:	code UT ARGUMENTS: _ Nonc. TURN ARGUMENTS: _ Nonc. ===================================	*/	*/*/*/ */*/ */*/ 16 i_sf, INT16	index, FLOAT64 *gainQ)
/* /* /* INP /* /* /* RET /* 0 /*===:	code UT ARGUMENTS : None None	*/	*/*/*/ */*/ */*/ 16 i_sf, INT16	index, FLOAT64 *gainQ)
/* /* /* INP /* /* /* RET /* 0 /*===	code UT ARGUMENTS : None None	*/ ====================================	*/	index, FLOAT64 *gainQ)
/* /* /* INP /* /* /* RET /* to /*=== void G	code UT ARGUMENTS : Nonc. TURN ARGUMENTS : Nonc. SEQ_Dec_PitchGain_3D (*/ ====================================	*/	index, FLOAT64 *gainQ)

```
415
          5
          gainQ[0] = gp3_tab[index][i_sf];
      else
10
          15
            set down-scaling according to number of bad frames */
          switch (N_bfi)
20
             {
             case 1:
                   pdown = 0.99;
                   break;
25
             case 2:
                   pdown = 0.98;
                   break;
             case 3:
                   pdown = 0.96;
30
                   break;
             case 4:
                   pdown = 0.96;
                   break;
             case 5:
35
                   pdown = 0.96;
                   break;
             case 6:
                   pdown = 0.96;
                   break;
             default:
40
```

```
416
                   pdown = 0.70;
                   break;
            }
5
         if (N_bfi == 1)
10
           gainQ[0] = 0.95;
              clsc
                    gainQ[0] = gp_buf[7];
                    gainQ[0] *= pdown;
15
           }
        }
20
      return;
25
                                              */
30 /* FUNCTION : GEQ_Quant_PitchGain_4D ().
  /* PURPOSE : This function performs 4-D VQ of Ga1, Ga2, Ga3 */
     and Ga4 with 6 bits.
   /*-----
35 /* INPUT ARGUMENTS:
                _ None.
   /* OUTPUT ARGUMENTS:
   /* _ (INT16) idx: codebook index.
```

```
417
   /* INPUT/OUTPUT ARGUMENTS:
                            Gp: unquantized/quantized pitch */
          _(FLOAT64 [])
   /*
                        gains.
 5 /* RETURN ARGUMENTS:
                    _ None.
                                              */
   /*
   void GEQ Quant_PitchGain_4D(FLOAT64 *Gp, INT16 *idx)
10
           INT16 i;
           FLOAT64 min_dist;
15
           FLOAT64 dist;
           FLOAT64 err[GVQ_VEC_SIZE_4D];
           min_dist = MAXFLT;
20
           for(i = 0; i < TAB SIZE GVQ_4D; i++)
                   {
                   dif_dvcctor (Gp, gp4_tab[i], err, 0, GVQ_VEC_SIZE_4D-1);
                   dot_dvector (err, err, &dist, 0, GVQ_VEC_SIZE_4D-1);
25
                   if (dist < min_dist)
                           min_dist = dist;
30
                           (*idx) = i;
                   }
           for (i = 0; i < GVQ\_VEC\_SIZE\_4D; i++)
35
                  Gp[i] = gp4_tab[(*idx)][i];
           rcturn;
40
```

```
5
  /* FUNCTION : GEQ_Dec_PitchGain_4D ().
  /* PURPOSE : This function decodes the 6 bit 4-D VQ of Gal, */
        Ga2, Ga3, and Ga4.
10 /*
  /* INPUT ARGUMENTS:
                    N_bfi: duration of bad frame */
       _(INT 16)
                     seq. (0=good frame)
  /*
                    i_sf: subframe number.
15 /*
       _(INT16)
        _(INT16) index: codebook index.
   /* OUTPUT ARGUMENTS :
        _(FLOAT64 []) gainQ: Quantized adaptive
                       codebook gains.
20 /*
   /* INPUT ARGUMENTS:
                 None.
25 /* RETURN ARGUMENTS:
   /*
                 None.
   void GEQ_Dec_PitchGain_4D (INT16 N_bfi, INT16 i_sf, INT16 index, FLOAT64 *gainQ)
30
       FLOAT64 pdown;
 35
        if (N bfi == 0)
           40
```

```
419
            gainQ[0] = gp4_tab[index][i_sf];
 5
        else
10
            set down-scaling according to number of bad frames */
15
           switch (N_bfi)
               {
               case 1:
20
                       pdown = 0.99;
                       break;
               case 2:
                       pdown = 0.98;
                       break;
25
               case 3:
                       pdown = 0.96;
                       break;
               case 4:
                       pdown = 0.96;
30
                       break;
               case 5:
                       pdown = 0.96;
                       brcak;
               case 6:
35
                       pdown = 0.96;
                       break;
               default:
                       pdown = 0.70;
                       break;
40
               }
```

```
5
               if (N bfi == 1)
            gainQ[0] = 0.95;
               else
10
                      gainQ[0] = gp_buf[7];
                      gainQ[0] *= pdown;
           }
15
         }
      return;
20
      }
25
                       -----
  /* FUNCTION : GEQ_gainVQMA_3 ().
  /* PURPOSE : This function performs 3-D VQ of Gc1, Gc2, and */
30 /*
             Gc3 with 8 bits.
  /* INPUT ARGUMENTS:
       (FLOAT64 [])
                       Tgs: target signal.
        _ (FLOAT64 [])
                        unfcod: fixed codebook excitation.*/
  /*
35 /*
        _(FLOAT64 [])
                        fcod: filtered fixed codebook */
                       excitation.
                                     */ .
  /*
  /* OUTPUT ARGUMENTS:
                        gainQ: Quantized fixed codebook */
        (FLOAT64 [])
  /*
                                     */
                       gains.
40 /*
```

```
(INT16)
                       idx: codebook index.
  /* INPUT/OUTPUT ARGUMENTS:
                  _ None.
  /* RETURN ARGUMENTS:
                  _ None.
                                        */
10 void GEQ_gainVQMA_3 (FLOAT64 Tgs [], FLOAT64 **unfcod, FLOAT64 **fcod,
                       FLOAT64 **gainQ, INT16 *idx)
15
          INT16 i;
          INT16 num_candidate;
          INT16 i_s, i_sf;
          INT16 LL_SF(N_SF3)={L_SF0, L_SF0, L_SF3};
20
          FLOAT64 erg_ratio[N_SF3];
          FLOAT64 pred_energy[N_SF3];
          FLOAT64 err_energy[N_SF3];
          FLOAT64 lin_err_energy[N_SF3];
          FLOAT64 energy[N_SF3], pred_gain[N_SF3], dist, dist_min,
25
                 corr[CORR_SIZE][N_SF3];
          FLOAT64 val;
       FLOAT64 corr_coeff_a[3], corr_coeff_b[3], corr_coeff_c;
30
          35
                         MA Prediction
          dot_dvector(past_energyq_3d, energy_pred_coeff_1,
           &pred_energy[0], 0, GVQ_VEC_SIZE_3D-1);
40
```

```
pred_energy[0] = MAX(pred_energy[0], -20.0);
          dot_dvector(past_energyq_3d, energy_pred_coeff_2,
            &pred_energy[1], 0, GVQ_VEC_SIZE_3D-1);
5
           pred_energy[1] = MAX(pred_energy[1], -20.0);
           dot_dvcctor(past_cnergyq_3d, energy_pred_coeff_3,
            &pred_energy[2], 0, GVQ_VEC_SIZE_3D-1);
10
           pred_energy[2] = MAX(pred_energy[2], -20.0);
           15
           i_s = 0;
           for (i_sf = 0; i_sf < N_SF3; i_sf++)
20
                   /* compute the cdbk energy with mean removed (i.e., -34dB) */
                   dot\_dvector\ (unfcod[1]+i\_s,\ unfcod[1]+i\_s,\ \&energy[i\_sf],
25
           0, LL_SF[i_sf]-1);
                   cnergy[i_sf] = 34.0 - 10.0*log10(energy[i_sf] /
30
   (FLOAT64)LL_SF[i_sf]+EPSI);
                             compute the predicted energy
35
                   energy[i_sf] += pred_energy[i_sf];
```

```
423
                                 compute the predicted gain
                    pred_gain[i_sf] = pow(10.0, energy[i_sf]/20.0);
 5
                    err_energy[i\_sf] = -energy[i\_sf] + 20*log10(fabs(gainQ[1][i\_sf])
                    + EPSI);
10
                    lin_err_energy[i_sf] = pow(10.0, err_energy[i_sf]/20.0);
                    i_s += LL_SF[i_sf];
15
               dist_min = MAXFLT;
20
            num_candidate = 256;
            i_s = 0;
            for (i_sf = 0; i_sf < N_SF3; i_sf++)
25
                     dot\_dvector\ (fcod[0]+i\_s,\ fcod[0]+i\_s,\ \&val,\ 0,\ LL\_SF[i\_sf]-l);
                     corr[0][i_sf] = val;
                     dot_dvcctor (fcod[0]+i_s, Tgs+i_s, &val, 0, LL_SF[i_sf]-1);
                     corr[1][i_sf] = -2.0 * val;
30
                     dot_dvector (fcod[1]+i_s, fcod[1]+i_s, &val, 0, LL_SF[i_sf]-1);
                     corr[2][i_sf] = val;
                     dot dvector(fcod[1]+i_s, Tgs+i_s, &val, 0, LL_SF[i_sf]-1);
35
                     corr[3][i_sf] = -2.0*val;
                     dot\_dvector(fcod[0]+i\_s, fcod[1]+i\_s,&val, 0, LL\_SF[i\_sf]-1);
                     corr[4][i_sf] = 2.0*val;
```

```
dot_dvector(Tgs+i_s, Tgs+i_s, &val, 0, LL_SF[i_sf]-1);
                      corr[5][i_sf] = val;
                      i_s += LL_SF[i_sf];
5
             erg_ratio[0] = corr[5][0] / (corr[5][0] + corr[5][1] + corr[5][2] + EPSI);
             erg_ratio[1] = corr[5][1] / (corr[5][0] + corr[5][1] + corr[5][2] + EPSI);
             erg_{a}[5][2] = corr[5][2] / (corr[5][0] + corr[5][1] + corr[5][2] + EPSI);
10
         corr_coeff_a[0] = erg_ratio[0] * corr[2][0] * pred_gain[0] * pred_gain[0];
         corr\_coeff\_b[0] = erg\_ratio[0] * (corr[3][0] + gainQ[0][0] * corr[4][0]) * pred\_gain[0];
         corr_coeff_a[1] = crg_ratio[1]*corr[2][1]*pred_gain[1]*pred_gain[1];
         corr\_coeff\_b[1] = crg\_ratio[1]*(corr[3][1]+gainQ[0][1]*corr[4][1])*pred\_gain[1];
15
          corr_coeff_a[2] = erg_ratio[2]*corr[2][2]*pred_gain[2]*pred_gain[2];
          corr\_coeff\_b[2] = erg\_ratio[2]*(corr[3][2]+gainQ[0][2]*corr[4][2])*pred\_gain[2];
          20
    \label{eq:corr_sol} \begin{split} & \text{erg\_ratio}[1]*(\text{corr}[5][1]+\text{sqr}(\text{gain}Q[0][1])*\text{corr}[0][1]+\text{gain}Q[0][1]*\text{corr}[1][1])+ \end{split}
    erg\_ratio[2]*(corr[5][2]+sqr(gainQ[0][2])*corr[0][2]+gainQ[0][2]*corr[1][2]);\\
              for (i = 0; i < num\_candidate; i++)
25
               dist = corr_coeff_a[0]*sqr(gainVQ_3_256[i][0])+
                   corr coeff_b[0]*gainVQ_3_256[i][0] +
                   corr_coeff_a[1]*sqr(gainVQ_3_256[i][1])+
                   corr coeff_b[1]*gainVQ_3_256[i][1] +
                   corr_coeff_a[2]*sqr(gainVQ_3_256[i][2])+
 30
                    corr_coeff_b[2]*gainVQ_3_256[i][2] + corr_coeff_c;
                      if (dist < dist_min)
                                dist_min = dist;
 35
                                (*idx) = i;
                                }
                       }
```

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```
425
                           Get the quantized gains
           gainQ[1][0] = pred_gain[0]*gainVQ_3_256[*idx][0];
5
           gainQ[1][1] = pred_gain[1]*gainVQ_3_256[*idx][1];
           gainQ[1][2] = pred_gain[2]*gainVQ_3_256[*idx][2];
10
                         Update past quantized energies
           for (i_sf = 0; i_sf < N_SF3; i_sf++)
                   {
15
                   val = gainVQ_3_256[*idx][i_sf];
                   past_energyq_3d [N_SF3-1-i_sf] = 20.0* \log 10(val);
20
            return;
25
   /* FUNCTION : GEQ_dcc_gc_3_8 ().
   /* PURPOSE : This function decodes the 8 bit 3-D VQ of Gc1, */
              Gc2, and Gc3. Note that the gains are decoded */
                one-by-one on a subframe basis.
35 /*
    /* INPUT ARGUMENTS:
          _(INT16)
                          index: codebook index.
          _(INT16)
                           N_bfi: duration of bad frame */
                             seq. (0=good frame)
                                                    */
40 /*
```

```
_(FLOAT64 []) untow: nxca without excitation.*/
  /*
                       i_sf: subframe number.
         (INT16)
  /* OUTPUT ARGUMENTS :
                                  Quantized fixed codebook */
         (FLOAT64 *)
                         gainQ:
                                          */
                          gains.
   /* INPUT ARGUMENTS:
   /* RETURN ARGUMENTS:
15 void GEQ_dcc_gc_3_8(INT16 index, FLOAT64 *gainQ, INT16 N_bfi, FLOAT64 **unfcod,
                 INT16 i_sf)
                       */
20
          INT16 i_sf_local;
          INT16 LL_SF[N_SF3]={L_SF0, L_SF0, L_SF3};
           FLOAT64 cdown, current_fixed_energy, energy, pred_gain;
        FLOAT64 min_erg_38;
25
           /* Note: the gains are decoded one-by-one on a subframe basis
30
                Compute the cdbk energy with mean removed (i.e., -34dB) */
                 Predict energy for all subframes at first subframe
35
           if (i sf == 0)
                  dot_dvector(past_energyq_3d, energy_pred_coeff_1,
40
```

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```
427
                     pred_energy_d38, 0, 3-1);
                  pred_energy_d38[0] = MAX (pred_energy_d38[0], -20.0);
                  dot_dvector(past_energyq_3d, energy_pred_coeff_2,
                     pred_energy_d38+1, 0, 3-1);
                  prcd_energy_d38[1] = MAX (pred_energy_d38[1], -20.0);
                  dot_dvector(past_energyq_3d, energy_pred_coeff_3,
                     pred_energy_d38+2, 0, 3-1);
                  pred_energy_d38[2] = MAX (pred_energy_d38[2], -20.0);
10
                  }
           dot\_dvcctor\ (unfcod[1],\ unfcod[1],\ \&energy,\ 0,\ LL\_SF[i\_sf]-1);
           current_fixed_energy = energy;
15
           energy = 34.0 - 10.0*log10(energy/(FLOAT64)LL_SF[i_sf]+EPSI);
                        Compute the predicted energy
20
           cnergy += pred_energy_d38[i_sf];
                         Compute the predicted gain
25
           pred_gain = pow(10.0, energy/20.0);
           if (N bfi == 0)
30
                   35
                                Get the quantized gain
                   (*gainQ) = pred_gain*gainVQ_3_256[index][i_sf];
40
```

```
past_fixed_energy = current_fixed_energy*sqr((*gainQ));
                 if (i_sf == N_sF3-1)
5
                                 Update past quantized energies
                         for (i_sf_local= 0; i_sf_local < N_SF3; i_sf_local++)
10
                                {
                                past_energyq_3d[N_SF3-1-i_sf_local] =
                                               20.0*log10(gainVQ_3_256[index][i_sf_local]);
                                }
                         }
15
                 }
          else
                  20
                      set down-scaling according to number of bad frames */
25
                  switch (N_bfi)
                          case 1:
                                               cdown = 0.75;
30
                                               break;
                          case 2:
                                               cdown = 0.75;
                                               break;
                          case 3:
 35
                                               cdown = 0.75;
                                                break;
                          case 4:
                                                cdown = 0.40;
                                                break;
 40
```

```
case 5:
                                              cdown = 0.30;
                                              break;
5
                         case 6:
                                               cdown = 0.20;
                                               break;
                         dcfault:
                                               cdown = 0.20;
                                               break;
10
                         }
                   15
                   (*gainQ) = sqrt(past_fixed_energy/current_fixed_energy);
                   (*gainQ) *= cdown;
                   past_fixed_energy = current_fixed_energy*sqr((*gainQ));
20
                   /* Buffer data for update after the last subframe
25
                   min_erg_38 = MIN(past_energyq_3d[0], past_energyq_3d[1]);
                   min_erg_38 = MIN(min_erg_38, past_energyq_3d[2]);
                   past energyq_3d[GVQ_VEC_SIZE_3D-1-i_sf] = MIN(min_erg_38,
                                               -energy + 20*log10(fabs(0.6*(*gainQ)) + EPSI));
30
                           Set GC to zero for this mode under FER
35
                   (*gainQ) = 0.0;
40
```

```
return;
 5
   /* FUNCTION : GEQ_gainVQMA_4 ().
   /* PURPOSE : This function performs 4-D VQ of Gc1, Gc2, Gc3 */
              and Gc4 with 10 bits.
   /* INPUT ARGUMENTS:
         _(FLOAT64 []) Tgs: target signal.
15 /*
        _ (FLOAT64 []) unfcod: fixed codebook excitation.*/
   /*
   /*
         (FLOAT64 [])
                          fcod: filtered fixed codebook */
   /*
                                          */
                          excitation.
20 /* OUTPUT ARGUMENTS :
   /*
         _ (FLOAT64 []) gainQ: Quantized fixed codebook */
                          gains.
         (INT16)
                        idx: codebook index.
25 /* INPUT/OUTPUT ARGUMENTS:
   /*
                   _ None.
   /* RETURN ARGUMENTS:
                   None.
                                           */
   void GEQ_gainVQMA_4 (FLOAT64 Tgs [], FLOAT64 **unfcod, FLOAT64 **fcod,
                        FLOAT64 **gainQ, INT16 *idx)
35
          INT16 i;
           INT16 num_candidate;
          INT16 i_s, i_sf;
40
```

```
FLOAT64 pred_energy[N_SF4];
         FLOAT64 err_energy[N_SF4];
         FLOAT64 lin_err_energy[N_SF4];
          FLOAT64 energy[N_SF4], pred_gain[N_SF4], dist, dist_min,
5
                corr[CORR_SIZE][N_SF4];
          FLOAT64 val;
       FLOAT64 corr_coeff_a[4], corr_coeff_b[4], corr_coeff_c;
10
          15
                        MA Prediction
          dot_dvector(past_energyq_4d, energy_pred_coeff4d_1,
           &pred_energy[0], 0, GVQ_VEC_SIZE_4D-1);
20
          pred_energy[0] = MAX(pred_energy[0], -20.0);
          dot_dvector(past_energyq_4d, energy_pred_coeff4d_2,
           &pred_energy[1], 0, GVQ_VEC_SIZE_4D-1);
25
          pred_energy[1] = MAX(pred_energy[1], -20.0);
          dot_dvector(past_energyq_4d, energy_pred_coeff4d_3,
           &pred_energy[2], 0, GVQ_VEC_SIZE_4D-1);
30
          pred energy[2] = MAX(pred_energy[2], -20.0);
          dot_dvcctor(past_energyq_4d, energy_pred_coeff4d_4,
           &pred_energy[3], 0, GVQ_VEC_SIZE_4D-1);
35
          pred_energy[3] = MAX(pred_energy[3], -20.0);
           40
```

```
432
           i_s = 0;
           for (i_sf = 0; i_sf < N_SF4; i_sf++)
 5
                   /* compute the cdbk energy with mean removed (i.e., -34dB) */
10
                   dot_dvector (unfcod[1]+i_s, unfcod[1]+i_s, &encrgy[i_sf],
           0, L_SF4-1);
                   energy[i_sf] = 34.0 - 10.0*log10(energy[i_sf] /
15
   (FLOAT64)L_SF4+EPSI);
                            compute the predicted energy
20
                   energy[i_sf] += pred_energy[i_sf];
25
                            compute the predicted gain
                   pred_gain[i_sf] = pow(10.0, energy[i_sf]/20.0);
                   err_energy[i_sf] = -energy[i_sf] + 20*log10(fabs(gainQ[1][i_sf])
30
                   + EPSI);
                   \lim_{x \to \infty} \lim_{x \to \infty} [i_sf] = pow(10.0, crr_energy[i_sf]/20.0);
35
                   i s += L SF4;
40
```

```
433
            dist_min = MAXFLT;
            num_candidate = 1024;
5
            i_s = 0;
            for (i_sf = 0; i_sf < N_SF4; i_sf++)
                     dot\_dvector\ (fcod[0]+i\_s,\ fcod[0]+i\_s,\ \&val,\ 0,\ L\_SF4-l);
10
                     corr[0][i_sf] = val;
                     dot_dvector (fcod[0]+i_s, Tgs+i_s, &val, 0, L_SF4-1);
                     corr[1][i_sf] = -2.0 * val;
                     dot_dvector(fcod[1]+i_s, fcod[1]+i_s, &val, 0, L_SF4-1);
15
                     corr[2][i_sf] = val;
                      dot_dvector(fcod[1]+i_s, Tgs+i_s, &val, 0, L_SF4-1);
                      corr[3][i_sf] = -2.0*val;
20
                      dot_dvector(fcod[0]+i_s, fcod[1]+i_s,&val, 0, L_SF4-1);
                      corr[4][i_sf] = 2.0*val;
                      dot_dvector(Tgs+i_s, Tgs+i_s, &val, 0, L_SF4-1);
                      corr[5][i_sf] = val;
25
                      i_s += L_SF4;
         corr_cocff_a[0] = corr[2][0]*pred_gain[0]*pred_gain[0];
30
        corr\_coeff\_b[0] = (corr[3][0]+gainQ[0][0]*corr[4][0])*pred\_gain[0];
        corr_cocff_a[1] = corr[2][1]*pred_gain[1]*pred_gain[1];
        corr coeff_b[1] = (corr[3][1]+gainQ[0][1]*corr[4][1])*pred_gain[1];
35
        corr_coeff_a[2] = corr[2][2]*pred_gain[2]*pred_gain[2];
        corr_cocff_b[2] = (corr[3][2]+gainQ[0][2]*corr[4][2])*pred_gain[2];
        corr_coeff_a[3] = corr[2][3]*pred_gain[3]*pred_gain[3];
        corr_coeff_b[3] = (corr[3][3]+gainQ[0][3]*corr[4][3])*pred_gain[3];
40
```

```
corr_coeff_c = corr[5][0] + sqr(gainQ[0][0]) + corr[0][0] + gainQ[0][0] + corr[1][0] +
   corr[5][1]+sqr(gainQ[0][1])*corr[0][1]+gainQ[0][1]*corr[1][1]+
         corr[5][2]+sqr(gainQ[0][2])*corr[0][2]+gainQ[0][2]*corr[1][2]+
 5 corr[5][3]+sqr(gainQ[0][3])*corr[0][3]+gainQ[0][3]*corr[1][3];
             for (i = 0; i < num\_candidate; i++)
             dist =
10 corr_coeff_a[0]*sqr(gainVQ_4_1024[i][0])+corr_coeff_b[0]*gainVQ_4_1024[i][0]
   corr\_coeff\_a[1]*sqr(gainVQ\_4\_1024[i][1]) + corr\_coeff\_b[1]*gainVQ\_4\_1024[i][1]
15
   corr_coeff_a[2]*sqr(gainVQ_4_1024[i][2])+corr_coeff_b[2]*gainVQ_4_1024[i][2]
   corr_coeff_a[3]*sqr(gainVQ_4_1024[i][3])+corr_coeff_b[3]*gainVQ_4_1024[i][3]
20 +
          corr_coeff_c;
                     if (dist < dist_min)
25
                              dist_min = dist;
                              (*idx) = i;
                     }
30
                              Get the quantized gains
35
        gainQ[1][0] = pred_gain[0]*gainVQ_4_1024[*idx][0];
        gainQ[1][1] = prcd_gain[1]*gainVQ_4_1024[*idx][1];
        gainQ[1][2] = pred_gain[2]*gainVQ_4_1024[*idx][2];
        gainQ[1][3] = pred_gain[3]*gainVQ_4_1024[*idx][3];
```

```
435
                     Update past quantized energies
         for (i_sf = 0; i_sf < N_SF4; i_sf++)
5
                val = gainVQ_4_1024[*idx][i_sf];
                past_energyq_4d [N_SF4-1-i_sf] = 20.0* log10(val);
10
         return;
15
20
  /* FUNCTION : GEQ_dec_gc_4_10 ().
                : This function decodes the 10 bit 4-D VQ of Gc1, */
  /* PURPOSE
25 /* Gc2, Gc3, and Gc4. Note that the gains are
                                                */
  /* decoded one-by-one on a subframe basis.
                                              */
  /* INPUT ARGUMENTS:
        _(INT16)
                      index: codebook index.
        _(INT16)
                      N bfi: duration of bad frame */
                       seq. (0=good frame)
  /*
        _(FLOAT64 []) unfcod: fixed codebook excitation.*/
                            subframe number.
                      i sf:
        _ (INT16)
35 /* OUTPUT ARGUMENTS:
                        gainQ: Quantized fixed codebook */
         _(FLOAT64 *)
                                      */
   /*
                        gains.
                                              */
   /* INPUT ARGUMENTS:
           _ None.
40 /*
```

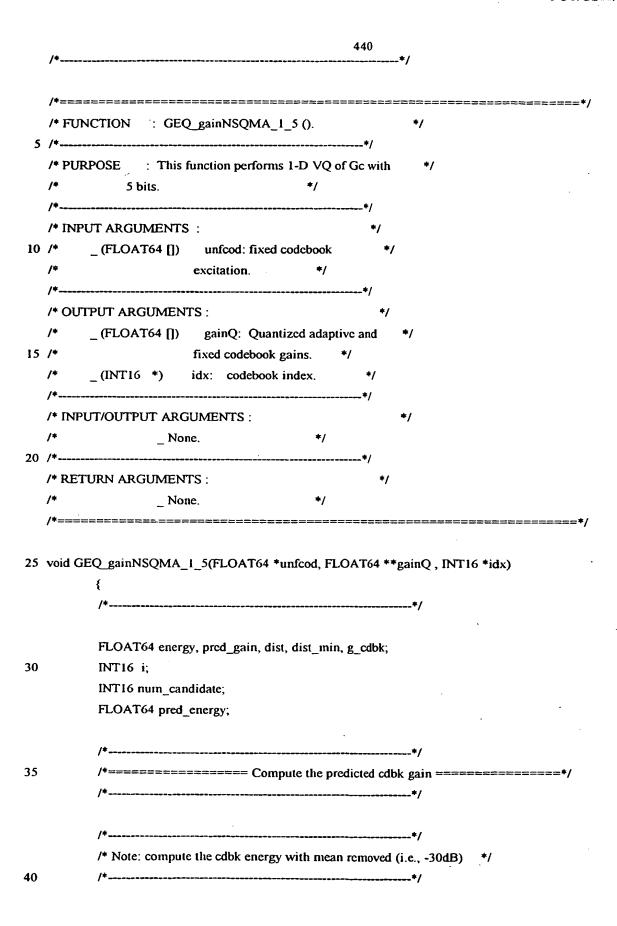
PCT/US00/25182 WO 01/22402

```
436
  /* RETURN ARGUMENTS :
                    _ None.
5
  void GEQ_dcc_gc_4_10(INT16 index, FLOAT64 *gainQ, INT16 N_bfi, FLOAT64 **unfcod,
                   INT16 i_sf)
10
            INT16 i_sf_local;
            FLOAT64 cdown, current_fixed_energy, energy, pred_gain;
         FLOAT64 min_erg_410;
15
             /* Note: the gains are decoded one-by-one on a subframe basis
 20
                   Compute the cdbk energy with mean removed (i.e., -34dB) */
 25
                     Predict energy for all subframes at first subframe
              if(i_sf == 0)
  30
                       dot_dvector(past_energyq_4d, energy_pred_coeff4d_1,
                           pred_energy_d410, 0, 4-1);
                       pred_energy_d410[0] = MAX (pred_energy_d410[0], -20.0);
   35
                       dot_dvector(past_energyq_4d, energy_pred_coeff4d_2,
                           pred_energy_d410+1, 0, 4-1);
                        pred_energy_d410[1] = MAX (pred_energy_d410[1], -20.0);
                        dot_dvector(past_cnergyq_4d, energy_pred_coeff4d_3,
    40
```

```
437
                      pred_cncrgy_d410+2, 0, 4-1);
                  prcd_energy_d410[2] = MAX (pred_energy_d410[2], -20.0);
                  dot_dvector(past_energyq_4d, energy_pred_coeff4d_4,
                      pred_energy_d410+3, 0, 4-1);
5
                  pred_energy_d410[3] = MAX (pred_energy_d410[3], -20.0);
           dot_dvector (unfcod[1], unfcod[1], &energy, 0, L_SF4-1);
10
           current_fixed_energy = energy;
           energy = 34.0 - 10.0*log10(energy/(FLOAT64)L_SF4+EPSI);
15
                        Compute the predicted energy
           energy += prcd_energy_d410[i_sf];
20
                         Compute the predicted gain
           pred gain = pow(10.0, cncrgy/20.0);
25
           if (N_bfi == 0)
                   30
                                Get the quantized gain
35
                   (*gainQ) = pred_gain*gainVQ_4_1024[index][i_sf];
                   past_fixed_energy = current_fixed_energy*sqr((*gainQ));
                 if (i_sf == N_SF4-1)
40
```

```
438
                          {
                                   Update past quantized energies
5
                          for (i_sf_local=0; i_sf_local < N_SF4; i_sf_local++)
                                 {
                                  past_energyq_4d[N_SF4-1-i_sf_local] =
                                                  20.0*log10(gainVQ\_4\_1024[index][i\_sf\_local]);\\
10
                          }
                   }
           elsc
15
                    set down-scaling according to number of bad frames */
20
                    switch (N_bfi)
                            case 1:
 25
                                                   cdown = 0.75;
                                                   break;
                            case 2:
                                                   cdown = 0.75;
                                                   break;
 30
                             case 3:
                                                   cdown = 0.75;
                                                   break;
                             case 4:
                                                    cdown = 0.40;
  35
                                                    brcak;
                             case 5:
                                                    cdown = 0.30;
                                                    break;
  40
```

```
439
                         case 6:
                                              cdown = 0.20;
                                               brcak;
                         dcfault:
                                               cdown = 0.20;
5
                                               break;
                        }
                  10
                  (*gainQ) = sqrt(past_fixed_energy/current_fixed_energy);
                   (*gainQ) *= cdown;
                   past_fixed_energy = current_fixed_energy*sqr((*gainQ));
15
                       Buffer data for update after the last subframe
20
            min erg 410 = MIN(past_energyq_4d[0], past_energyq_4d[1]);
            min_crg_410 = MIN(min_erg_410, past_energyq_4d[2]);
            min_erg_410 = MIN(min_erg_410, past_energyq_4d[3]);
            past_energyq_4d[GVQ_VEC_SIZE_4D-1-i_sf] = MIN(min_erg_410,
25
                                 -energy + 20*log10(fabs(0.6*(*gainQ)) + EPSI));
                           Set GC to zero for this mode under FER
30
                  (*gainQ) = 0.0;
                  }
35
           rcturn;
40
           }
```



```
dot_dvector (past_energyq_4d, energy_pred_coeff4d_1, &pred_energy, 0,
                   GVQ_VEC_SIZE_4D-1);
         pred_energy = MAX (pred_energy, -20.0);
5
         dot_dvector(unfcod, unfcod, &energy, 0, L_FRM-1);
         energy = 30.0 - 10.0*log10(cnergy/(FLOAT64)L_FRM+EPSI);
10
                     Compute the predicted energy
          energy += pred_energy;
15
                     compute the predicted gain
20
          pred_gain = pow(10.0, energy/20.0);
          /*____*/
25
          dist min = MAXFLT;
          num_candidate = 32;
          for (i = 0; i < num\_candidate; i++)
30
                 g cdbk = prcd_gain*gainSQ_1_32[i];
                 dist = sqr(g_cdbk - gainQ[1][0]);
35
                 if (dist < dist_min)
                       dist_min = dist;
                       (*idx) = i;
40
```

```
442
                     }
               }
5
                     get the quantized gains
         gainQ[1][0] = pred_gain*gainSQ_1_32[*idx];
10
                   update past quantized energies
         for (i = GVQ_VEC_SIZE_4D-1; i > 0; i--)
15
               past_energyq_4d[i] = past_energyq_4d[i-1];
         past energyq 4d[0] = 20.0*log10(gainSQ_1 32[*idx]);
20
         return;
  /* FUNCTION : GEQ_dec_gains_1_5 ().
  /*____*/
30 /* PURPOSE : This function decodes the 5 bit 1-D VQ of Gc */
  /*_____
  /* INPUT ARGUMENTS:
        _ (INT16)
                    index: codebook index.
                    N_bfi: duration of bad frame
        _ (INT16)
35 /*
                      scq. (0=good frame)
  /*
        _ (FLOAT64 [])
                      unfcod: adaptive and fixed
                      codebook excitation.
                                       */
  /* OUTPUT ARGUMENTS:
        _ (FLOAT64 [][]) gainQ: Quantized adaptive and */
```

```
fixed codebook gains.
   /* INPUT/OUTPUT ARGUMENTS:
                                          */
   /*
                 _ None.
   /* RETURN ARGUMENTS:
                                           */
                   _ None.
10 void GEQ_dec_gains_1_5 (INT16 index, FLOAT64 **gainQ, FLOAT64 *unfcod,
                                       INT16 N_bfi )
          {
          INT16 i;
15
           FLOAT64 current_fixed_energy;
           FLOAT64 energy, pred_gain;
           FLOAT64 cdown;
20
           FLOAT64 val;
           FLOAT64
                       min_crg_15;
           /*____*/
                Compute the cdbk energy with mean removed (i.e., -30dB) */
25
           dot dvector (unfcod, unfcod, &energy, 0, L_FRM-1);
           current fixed energy = energy;
           energy /= (FLOAT64)L_FRM;
30
           energy = 30.0 - 10.0*log10(energy+EPSI);
                        Compute the predicted energy
35
           dot_dvector (past_energyq_4d, energy_pred_coeff4d_1, &val, 0,
                     GVQ_VEC_SIZE_4D-1);
```

.

```
val = MAX (val, -20.0);
        energy += val;
5
                Compute the predicted gain
        pred_gain = pow(10.0, energy/20.0);
10
        if (N_bfi == 0)
             15
                    get the quantized gains
20
         gainQ[1][0] = pred_gain*gainSQ_1_32[index];
             /+____+/
             /* update past quantized energies */
25
             for (i = GVQ_VEC_SIZE_4D-1; i > 0; i-)
                  past_energyq_4d[i] = past_energyq_4d[i-1];
30
             past_energyq_4d[0] = 20.0*log10(gainSQ_1_32[index]);
            }
       else
35
             40
```

```
445
                         set down-scaling according to number of bad frames */
 5
                     switch (N_bfi)
                            {
                             case 1:
                                              cdown = 0.98;
                                              break;
10
                             case 2:
                                              cdown = 0.94;
                                              break;
                             case 3:
                                              cdown = 0.88;
                                              break;
15
                             case 4:
                                              cdown = 0.80;
                                              break;
                             case 5:
                                              cdown = 0.30;
20
                                              break;
                             case 6:
                                              cdown = 0.20;
                                              break;
25
                             dcfault:
                                              cdown = 0.20;
                                              break;
                             }
30
              gainQ[1][0] = sqrt(past_fixed_energy/current_fixed_energy);
              gainQ[1][0] *= cdown;
                                update past quantized energies
35
              min_crg_15 = MIN(past_energyq_4d[0], past_energyq_4d[1]);
              min_erg_15 = MIN(min_erg_15, past_energyq_4d[2]);
              min_erg_15 = MIN(min_crg_15, past_energyq_4d[3]);
40
```

```
for (i = GVQ\_VEC\_SIZE\_4D-1; i > 0; i--)
             past_energyq_4d[i] = past_energyq_4d[i-1];
 5
            past_energyq_4d[0] = MIN(min_erg_15, -energy +
                       20*log10(fabs(0.6*gainQ[1][0]) + EPSI));
10
                    Update the past fixed codebook energy
        past_fixed_energy = current_fixed_energy*sqr(gainQ[1][0]);
15
          return;
20
   /* FUNCTION : GEQ_gainNSQMA_1_6 ().
25 /* PURPOSE : This function performs 1-D VQ of Gc with
              6 bits.
                                       */
   /* INPUT ARGUMENTS:
   /*
         _ (FLOAT64 [])
                        unfcod: fixed codebook
30 /*
                       excitation.
                                        */
   /* OUTPUT ARGUMENTS:
   /*
         _ (FLOAT64 []) gainQ: Quantized adaptive and
                       fixed codebook gains.
35 /*
         _(INT16 *)
                       idx: codebook index.
                                                */
   /* INPUT/OUTPUT ARGUMENTS:
                                        */
                  Nonc.
40 /* RETURN ARGUMENTS:
```

/*	_ None. */
·	
void	GEQ_gainNSQMA_1_6(INT16 pos, FLOAT64 *unfcod, FLOAT64 **gainQ, INT16 *idx)
5	{
	/**/
	FLOAT64 energy, pred_gain, dist, dist_min, g_cdbk;
	INT16 i;
10	INT16 num_candidate;
	FLOAT64 pred_energy;
	/**/
	/*====================================
15	/**/
	/**/
	/* Note: compute the cdbk energy with mean removed (i.e., -30dB) */
20	/**/
20	dot_dvcctor (past_cncrgyq_2d, energy_pred_coeff_1, &pred_cncrgy, 0,
	GVQ_VEC_SIZE_2D-1);
	pred_energy = MAX (pred_energy, -20.0);
25	dot_dvector(unfcod+pos*40, unfcod+pos*40, &energy, 0, 80-1);
	encrgy = 30.0 - 10.0*log10(encrgy/(FLOAT64)80+EPSI);
	/* <u></u> */
30	/* Compute the predicted energy */
	/**/
	<pre>cncrgy += pred_energy;</pre>
35	/**/
	/* compute the predicted gain */
	/**/
	pred_gain = pow(10.0, energy/20.0);
40	

```
448
          5
          dist_min = MAXFLT;
          num_candidate = 64;
          for (i = 0; i < num\_candidate; i++)
10
                 g_cdbk = pred_gain*gainSQ_1_64[i];
                 dist = sqr(g_cdbk - gainQ[1][pos]);
15
                 if (dist < dist_min)
                        dist_min = dist;
                         (*idx) = i;
20
                 }
                        get the quantized gains
25
          gainQ[1][pos] = pred_gain*gainSQ_1_64[*idx];
30
                      update past quantized energics
          for (i = GVQ_VEC_SIZE_2D-1; i > 0; i-)
                 past_energyq_2d[i] = past_energyq_2d[i-1];
35
           past_energyq_2d[0] = 20.0*log10(gainSQ_1_64[*idx]);
40
           return;
```

```
/* FUNCTION : GEQ_dec_gains_1_6 ().
   /* PURPOSE : This function decodes the 6 bit 1-D VQ of Gc */
10 /+-----
   /* INPUT ARGUMENTS:
                       index: codebook index.
   /*
         _(INT16)
         _(INT16)
                       N_bfi: duration of bad frame
   /*
                          seq. (0=good frame)
                          unfcod: adaptive and fixed
         (FLOAT64 [])
15 /*
                          codebook excitation.
   /* OUTPUT ARGUMENTS:
         _(FLOAT64 [][]) gainQ: Quantized adaptive and */
                          fixed codebook gains. */
20 /*
                                                        */
   /* INPUT/OUTPUT ARGUMENTS:
                                           */
                   _ None.
25 /* RETURN ARGUMENTS:
   void GEQ dcc gains 1_6( INT16 pos, INT16 index, FLOAT64 **gainQ,
                                FLOAT64 *unfcod, INT16 N_bfi)
30
           INT16 i;
35
           FLOAT64 current_fixed_energy;
           FLOAT64 energy, pred_gain;
           FLOAT64 cdown;
           FLOAT64 val;
           FLOAT64
                         min_erg_16;
40
```

```
Compute the cdbk energy with mean removed (i.e., -30dB) */
  5
           dot_dvector (unfcod+pos*40, unfcod+pos*40, &energy, 0, 80-1);
           current_fixed_energy = energy;
           energy /= (FLOAT64)80;
10
           energy = 30.0 - 10.0*\log 10(energy+EPSI);
                        Compute the predicted energy
15
           dot_dvector (past_energyq_2d, energy_pred_coeff_1, &val, 0,
                     GVQ_VEC_SIZE_2D-1);
          val = MAX (val, -20.0);
20
           energy += val;
                        Compute the predicted gain
25
           pred_gain = pow(10.0, energy/20.0);
          if (N_bfi == 0)
30
                  35
                             get the quantized gains
                                                         */
            gainQ[1][pos] = prcd_gain*gainSQ_1_64[index];
40
```

```
451
                           update past quantized energies
                 for (i = GVQ\_VEC\_SIZE\_2D-1; i > 0; i--)
5
                        past_energyq_2d[i] = past_energyq_2d[i-1];
                  past\_energyq\_2d[0] = 20.0*log10(gainSQ\_1\_64[index]);
10
                 }
          else
15
                  set down-scaling according to number of bad frames */
20
                  switch (N_bfi)
                         case 1:
25
                                       cdown = 0.98;
                                       break;
                         case 2:
                                       cdown = 0.94;
                                       break;
30
                         case 3:
                                        cdown = 0.88;
                                        break;
                         case 4:
                                        cdown = 0.80;
35
                                        break;
                         case 5:
                                        cdown = 0.30;
                                        break;
40
                         case 6:
```

```
452
                                             cdown = 0.20;
                                             break;
                             dcfault:
 5
                                             cdown = 0.20;
                                             break;
                             }
              gainQ[1][pos] = sqrt(past_fixed_energy/current_fixed_energy);
10
              gainQ[1][pos] *= cdown;
                               update past quantized energies
15
              min_erg_16 = MIN(past_energyq_2d[0], past_energyq_2d[1]);
              for (i = GVQ_VEC_SIZE_2D-1; i > 0; i--)
               past_energyq_2d[i] = past_energyq_2d[i-1];
20
              past_energyq_2d[0] = MIN(min_crg_16, -energy +
                           20*log10(fabs(0.6*gainQ[1][pos]) + EPSI));
                    }
25
                        Update the past fixed codebook energy
         past_fixed_energy = current_fixed_energy*sqr(gainQ[1][pos]);
30
            return;
35
   /* FUNCTION : GEQ_gain_rcopt_2 ().
                                                             */
40 /* PURPOSE : This function jointly estimates Ga and Gc.
```

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```
453
   /* INPUT ARGUMENTS:
         _ (FLOAT64 [])
                            vec0: filtered adaptive codebook */
                                              */
   /+
                           excitation.
                            vec1: filtered fixed codebook */
          _ (FLOAT64 [])
 5 /*
                           excitation.
   /*
   /*
          _(FLOAT64 []) target: target signal.
                          1_sf: length of subframe.
   /*
          _ (INT16)
10 /* OUTPUT ARGUMENTS:
   /*
          _(FLOAT64)
                            gain0: adaptive codebook gain. */
          _ (FLOAT64 )
                            gain1: fixed codebook gain.
   /* INPUT ARGUMENTS:
15 /*
                    Nonc.
   /* RETURN ARGUMENTS:
20
   void GEQ_gain_reopt_2(FLOAT64 *vec0, FLOAT64 *vec1, FLOAT64 *target,
                                  FLOAT64 *gain0, FLOAT64 *gain1, INT16 1_sf)
           {
25
           FLOAT64 *Cp, *Tgs, *Cc;
           FLOAT64\ R\_Cp\_Tgs,\ R\_Cp\_Cp,\ R\_Cc\_Cc,\ R\_Cp\_Cc,\ R\_Cc\_Tgs;
           FLOAT64 den, num;
30
            Tgs = target;
            Cp = vec0;
            Cc = vec1;
35
                                  &R_Cp_Tgs,
                                                  0, l_sf-1);
            dot_dvcctor(Cp, Tgs,
                                  &R_Cp_Cp,
                                                  0, l_sf-1);
            dot_dvector(Cp, Cp,
            dot_dvector(Cc, Cc,
                                  &R_Cc_Cc,
                                                  0, l_sf-1);
                                  &R_Cp_Cc,
                                                  0, l_sf-1);
            dot_dvector(Cp, Cc,
            dot_dvector(Cc, Tgs,
                                   &R_Cc_Tgs,
                                                  0, 1_sf-1);
40
```

```
*/
                            Optimize all gains
 5
           num = R_Cp_Tgs*R_Cc_Cc - R_Cp_Cc*R_Cc_Tgs;
           den = R_Cp_Cp*R_Cc_Cc - R_Cp_Cc*R_Cp_Cc;
           if (den > 0)
10
                  den = MAX(0.001, den);
           else
                  den = MIN(-0.001, den);
15
           (*gain0) = num/den;
           (*gain0) = MAX(0.0, *gain0);
           (*gain0) = MIN(1.2, *gain0);
           (*gain1) = (R_Cc_Tgs - (*gain0)*R_Cp_Cc)/MAX(R_Cc_Cc, 0.00001);
20
           return;
25
30 /* FUNCTION : GEQ_energy_extrapolation ().
   /* PURPOSE : This function extrapolate the energy of the
             excitation when a frame erasure occurs.
35 /* INPUT ARGUMENTS:
                                                      */
   /* _ (INT16 ) i_s:
                           subframe starting sample.
                                                        */
   /* _(INT16) i_sf:
                                                      */
                           subframe number.
   /* (INT16) l_sf:
                           length of subframe.
                                                      */
   /* _ (FLOAT64 []) gainQ:
                               adaptive and fixed codebook gains. */
40 /* _ (FLOAT64 **) unfcod_dec: adaptive and fixed codebook excit. */
```

```
455
                          current frame Bad Frame Indicator. */
   /* _(INT16) bfi:
   /* _(INT16 []) lag:
                          subframe lags.
   /* _(FLOAT64 []) ForPitch_dec: decoded picth evolution.
   /* (PARAMETER) channel: decoded indexes.
   /* OUTPUT ARGUMENTS:
   /* _ (FLOAT64 []) ext_dec:
                                 final scaled excitation.
   /* _(FLOAT64 []) Prev_Bcta_Pitch: excitation scaling factor.
   /* _(FLOAT64 []) ET_buf:
                                  subframe energy factor.
10 /*-----
   /* INPUT ARGUMENTS:
                    None.
   /* RETURN ARGUMENTS:
15 /*
                    _ None.
   void GEQ_energy_extrapolation (INT16 i_s, INT16 i_sf, INT16 l_sf,
                                                  FLOAT64 gainQ [], FLOAT64 **qua_unfcod,
                                                  INT16 bfi, INT16 lag [], FLOAT64 ext_dec [],
20
                                                  FLOAT64 Prev_Beta_Pitch [], FLOAT64 ET_buf [],
                                                  FLOAT64 ForPitch_dec [], PARAMETER channel)
25
           INT16 i;
        FLOAT64 E1, E2, ET, FET, PFET;
        FLOAT64 *px, AA, BB;
        FLOAT64\ tmp\_vec1[L\_SF],\ tmp\_vec2[L\_SF],\ tmp\_vec3[L\_SF];
30
           for(i = 0; i < l_sf; i++)
35
                   tmp_vec1[i] = gainQ[1] * qua_unfcod[1][i+i_s];
                   tmp_{vec2[i]} = gainQ[0] * qua_unfcod[0][i+i_s];
                   tmp_vec3[i] = gainQ[0] * qua_unfcod[0][i+i_s] +
                                                  gainQ[1] * qua_unfcod[1][i+i_s];
40
```

```
dot_dvector(tmp_vec1, tmp_vec1, &E1, 0, 1_sf-1);
             dot_dvector(tmp_vec2, tmp_vec2, &E2, 0, 1_sf-1);
             dot_dvector(tmp_vec3, tmp_vec3, &ET, 0, 1_sf-1);
 5
            ET_buf[i_sf] = 10.0*log10(ET + EPSI);
            Prev_Beta_Pitch[i_sf] = E2 / (E1 + E2 + EPSI);
            px = ext_dec+MAX_LAG;
10
            dot_dvector(px, px, &FET, 0, 1_sf-1);
            FET += EPSI;
            if (bfi == 1)
15
                     if (gainQ[0] > 0.6)
                       if (channel.idx_SVS_deci == 0)
20
                                     BB = 0.90;
                            else
                                     BB = 0.95;
                     clse
                            BB = 0.8;
25
                     if (channel.idx_SVS_deci == 0)
                            {
30
                             if ((lag[i_sf] > 0) && (gainQ[0] >= 0.6))
                                     px = ext_dec+MAX_LAG-lag[i_sf];
                             else
                                     px = ext_dec+MAX LAG-l_sf,
                            }
35
                     else
                             if (lag[i\_sf] > 0)
                                     px = ext_dcc+MAX_LAG-(INT16)ForPitch_dcc[i_s];
                             else
40
                                     px = ext_dec+MAX_LAG-l_sf;
```

```
}
               dot_dvcctor(px, px, &PFET, 0, l_sf-1);
5
               AA = BB*sqrt(PFET/FET);
               px = ext_dec+MAX_LAG;
               sca_dvector(px, AA, px, 0, l_sf-1);
10
         return;
15
20
  */
  /* FUNCTION : GEQ_update_mem_4d_to_2d ().
   /* PURPOSE : This function update the cross memories
            for the gain quantizers for the transition
25 /*
            4 to 2 gains.
   /* INPUT ARGUMENTS:
                 _ None.
30 /*-----
   /* OUTPUT ARGUMENTS:
                 _ Nonc.
   /* INPUT/OUTPUT ARGUMENTS:
35 /*
                                      */
                 _ None.
   /* RETURN ARGUMENTS:
                 _ None.
40
```

```
458
   void GEQ_update_incm_4d_to_2d (void)
 5
         past_energyq_2d[0] = 0.5*(past_energyq_4d[0]+ past_energyq_4d[1]);
         past_energyq_2d[1] = 0.5*(past_energyq_4d[2]+ past_energyq_4d[3]);
10
         return;
  /* FUNCTION : GEQ_update_mcm_2d_to_4d ().
                                                   */
20 /* PURPOSE : This function update the cross memories
  /*
            for the gain quantizers for the transition
            2 to 4 gains.
  /* INPUT ARGUMENTS:
         None.
  /* OUTPUT ARGUMENTS:
                 None.
30 /* INPUT/OUTPUT ARGUMENTS:
  /*
  /* RETURN ARGUMENTS:
void GEQ_update_mem_2d_to_4d(void)
40
```

459 FLOAT64 val; $val = 0.5*(past energyq 2d[0] + past_energyq_2d[1]);$ 5 past_energyq_4d[0] = past_energyq_2d[0]; past_energyq_4d[1] = val; past_energyq_4d[2] = val; past_energyq_4d[3] = past_energyq_2d[1]; 10 return; 15 20 */ /* FUNCTION : GEQ_update_mem_3d_to_2d (). /*___*/ /* PURPOSE : This function update the cross memories 25 /* for the gain quantizers for the transition /* 3 to 2 gains. /*_____ /* INPUT ARGUMENTS: _ Nonc. 30 /*-----/* OUTPUT ARGUMENTS: None. /* INPUT/OUTPUT ARGUMENTS: 35 /* /* RETURN ARGUMENTS: None. 40

```
460
  void GEQ_update_mem_3d_to_2d(void)
         past_energyq_2d[0] = 0.5*(past_energyq_3d[0] + past_energyq_3d[1]);
5
         past\_energyq\_2d[1] = 0.5*(past\_energyq\_3d[1] + past\_energyq\_3d[2]);
10
         return;
  /* FUNCTION : GEQ_update_mem_2d_to_3d ().
20 /* PURPOSE : This function update the cross memories
            for the gain quantizers for the transition
  /*
                                       */
             2 to 3 gains.
   /* INPUT ARGUMENTS:
25 /*
                 _ None.
   /* OUTPUT ARGUMENTS:
                 _ None.
30 /* INPUT/OUTPUT ARGUMENTS:
   /*
                 None.
   /* RETURN ARGUMENTS:
                  _ Nonc.
void GEQ_update_mem_2d_to_3d(void)
40
```

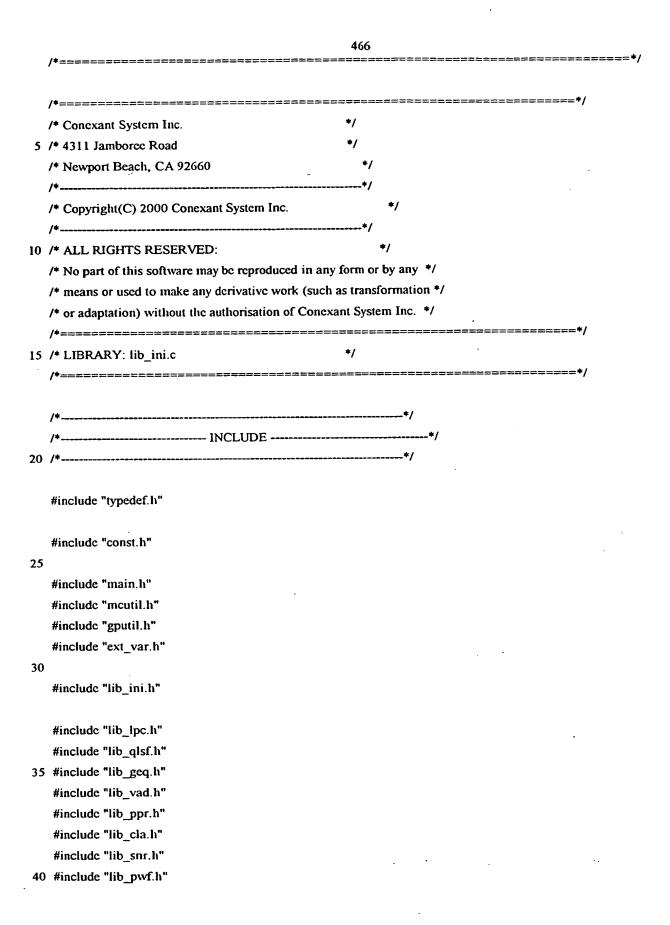
```
461
           past_energyq_3d[0] = past_energyq_2d[0];
           past_energyq_3d[1] = 0.5*(past_energyq_2d[0]+ past_energyq_2d[1]);
           past_cnergyq_3d[2] = past_cnergyq_2d[1];
5
           return;
10
15 /* FUNCTION : GEQ_update_mem_3d_to_4d ().
   /* PURPOSE : This function update the cross memories
              for the gain quantizers for the transition
              3 to 4 gains.
   /* INPUT ARGUMENTS:
                    _ None.
   /* OUTPUT ARGUMENTS:
25 /*
                    None.
   /* INPUT/OUTPUT ARGUMENTS:
30 /* RETURN ARGUMENTS:
                     _ None.
35 void GEQ_update_mem_3d_to_4d(void)
            past_energyq_4d[0] = past_energyq_3d[0];
            past_energyq_4d[1] = past_energyq_3d[1];
40
```

```
462
            past_energyq_4d[2] = past_energyq_3d[1];
            past_energyq_4d[3] = past_energyq_3d[2];
 5
            return;
10
   /* FUNCTION : GEQ_update_mcm_4d_to_3d ().
                                                              */
   /* PURPOSE : This function update the cross memories
   /*
               for the gain quantizers for the transition
               4 to 3 gains.
20 /* INPUT ARGUMENTS:
   /*
                    _ Nonc.
   /* OUTPUT ARGUMENTS:
                    _ None.
   /* INPUT/OUTPUT ARGUMENTS:
                    None.
   /* RETURN ARGUMENTS:
30 /*
   void GEQ_update_mem_4d_to_3d(void)
35
           past_energyq_3d[0] = past_energyq_4d[0];
            past_energyq_3d[1] = 0.5*(past_energyq_4d[1]+ past_energyq_4d[2]);
           past_energyq_3d[2] = past_energyq_4d[3];
40
```

	463
	/**/
	return;
5	/*
Э	
	}
	/**/
	/*====================================
W	
	/**/
	/*====================================

```
*/
  /* Conexant System Inc.
5 /* 4311 Jamboree Road
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  15 /* PROTOTYPE FILE: lib_geq.h
  /*_____/
  /*____*/
  /*----*/
                   (void);
  void GEQ_init_lib
                         (FLOAT64 [], FLOAT64 **, FLOAT64 **, FLOAT64 [], INT16 *,
  void GEQ_gainVQMA_2
                         INT16, INT16);
25
  void GEQ_dec_gains_2_7 (INT16, INT16, FLOAT64 [], INT16, FLOAT64 [],
                               INT16, INT16);
                         (FLOAT64 [], FLOAT64 **, FLOAT64 **, FLOAT64 [], INT16 *,
30 void GEQ_gainVQMA_2_8_5
                                INT16);
                         (INT16, INT16, FLOAT64 [], INT16, FLOAT64 [], INT16,
  void GEQ dec_gains_2_7_8_5
                                INT16);
35
                               (FLOAT64 *, INT16 *);
   void GEQ_Quant_PitchGain_3D
                               (FLOAT64 *, INT16 *);
   void GEQ_Quant_PitchGain_4D
40 void GEQ_Dec_PitchGain_3D
                               (INT16, INT16, INT16, FLOAT64 *);
```

```
465
                                       (INT16, INT16, INT16, FLOAT64 *);
   void GEQ_Dec_PitchGain_4D
                                (FLOAT64 [], FLOAT64 **, FLOAT64 **, FLOAT64 **, INT16 *);
   void GEQ_gainVQMA_3
                                (INT16, FLOAT64 *, INT16, FLOAT64 **, INT16);
   void GEQ_dec_gc_3_8
                                (FLOAT64 [], FLOAT64 **, FLOAT64 **, FLOAT64 **, INT16 *);
   void GEQ_gainVQMA_4
                                (INT16, FLOAT64 *, INT16, FLOAT64 **, INT16);
10 void GEQ_dec_gc_4_10
   void GEQ_gainNSQMA_1_5(FLOAT64 [], FLOAT64 **, INT16 []);
   void GEQ_dec_gains_1_5( INT16 , FLOAT64 **, FLOAT64 [], INT16 );
15
   void GEQ_gainNSQMA_1_6(INT16, FLOAT64 [], FLOAT64 **, INT16 []);
   void GEQ_dec_gains_1_6( INT16, INT16, FLOAT64 **, FLOAT64 [], INT16);
   void GEQ_gain_rcopt_2 (FLOAT64 [], FLOAT64 [], FLOAT64 *,
                                        FLOAT64 *, INT16);
20
   void GEQ_energy_extrapolation (INT16, INT16, INT16, FLOAT64 [], FLOAT64 **,
                                               INT16, INT16 [], FLOAT64 [], FLOAT64 [], FLOAT64 [],
                                               FLOAT64 [], PARAMETER);
25
   void GEQ update_mem_4d_to_2d (void);
   void GEQ update mem_2d_to_4d (void);
30 void GEQ_update_mem_3d_to_2d (void);
   void GEQ_update_mem_2d_to_3d (void);
   void GEQ_update_mcm_3d_to_4d (void);
   void GEQ_updatc_mem_4d_to_3d (void);
            ----- END -----
```



```
467
  #include "lib_pit.h"
  #include "lib ltp.h"
  #include "lib smo.h"
  #include "lib_prc.h"
5 #include "lib_bit.h"
  #include "lib_ppp.h"
  #include "lib_fcs.h"
  /*------
                              -----*/
10 /*-----*/
                                              */
  /* FUNCTION : INI_allocate_memory ().
15 /*-----*/
  /* PURPOSE : This function performs memory allocation.
  /*_____
  /* INPUT ARGUMENTS:
                                    */
                _ None.
  /* OUTPUT ARGUMENTS:
                                    */
                None.
  /* INPUT/OUTPUT ARGUMENTS:
25 /*
  /* RETURN ARGUMENTS:
30
        INI_allocate_memory (void)
  void
               Memory Allocation for Time-Domain Pre Filtering
35
         pre_flt_num = dvector (0, PPR_FLT_ORDER-1);
         pre_flt_den = dvector (0, PPR_FLT_ORDER-1);
         pre_flt_buf_z = dvector (0, PPR_FLT_ORDER-1);
         pre_fit_buf_p = dvector (0, PPR_FLT_ORDER-1);
40
```

```
ip flt num = dvector (0, LP_FLT_ORDER-1);
          lp flt buf = dvcctor (0, LP_FLT_ORDER-1);
5
          /* Memory Allocation for Silence Enhancement
          zeroed
                       = svector (0, SE MEM_SIZE-1);
                       = dvector (0, SE_MEM_SIZE-1);
10
          zero_rate
                       = dvector (0, SE_MEM_SIZE-1);
          low_rate
                       = dvector (0, SE_MEM_SIZE-1);
          high rate
                       = dvector (0, 1);
          low_neg
15
          low_pos
                       = dvector (0, 1);
          /* Memory Allocation for Tilt compansation Post Filtering
          /*____*/
20
          tc buff_exc = dvector (0, L_LPC-1);
                Memory Allocation for Time-Domain Post Filtering
25
          buff_LTpost = dvector (0, MAX_LAG+L_SF-1);
          PF_mem_syn = dvector (0, NP-1);
30
          /*____*/
                 Memory Allocation for Time-Domain Post Filtering
          pst_flt_num = dvector (0, PPR_FLT_ORDER-1);
35
          pst flt den = dvector (0, PPR FLT_ORDER-1);
          pst flt buf z = dvector (0, PPR_FLT_ORDER-1);
          pst flt buf p = dvector (0, PPR_FLT_ORDER-1);
          40
```

```
469
                   Memory Allocation for Linear Prediction Analysis
                                                                       */
            siglpc = dvector (0, L_LPC-1);
 5
            tilt_window = dvector (0, L_LPC-1);
            lpc_window = dvector (0, L_LPC-1);
            lpc windowl = dvector (0, L_LPC-1);
            lpc window2 = dvector (0, L_LPC-1);
10
            bwe factor
                            = dvcctor (0, NP);
            rxx
                           = dvector (0, NP);
            refl
                           = dmatrix (0, N_SF_MAX, 0, NP-1);
                           = dvector (0, NP-1);
            lsf_new
15
            lsf_old = dvcctor (0, NP-1);
            lsf_mid = dvcctor (0, NP-1);
            IntLSF_C
                           = dvector(0, N_SF4-1);
                           = dmatrix (0, N_SF_MAX, 0, NP-1);
20
            pdcf
                           = dmatrix (0, N_SF_MAX, 0, NP-1);
            pdcfq
                           = dmatrix (0, N_SF_MAX, 0, NP-1);
            pdcfq_dec
25
                     Memory Allocation for the LSF quantization
                                                                      */
        lsf cb 08k = d3tensor(0, MAXLTT_08k-1, 0, LMSMAX_08k-1, 0, MAXLNp-1);
         lsf cb 40k = d3tensor(0, MAXLTT_40k-1, 0, LMSMAX_40k-1, 0, MAXLNp-1);
         lsf_cb_85k = d3tensor(0, MAXLTT_85k-1, 0,LMSMAX_85k-1, 0, MAXLNp -1);
30
            stage_cand_08k = svector(0, 2);
            stage_cand_40k = svector(0, 3);
            stage_cand_85k = svector(0, 3);
35
            MS 08k = svector(0, LTT_08k-1);
            MS 40k = svector(0, LTT_40k-1);
            MS_85k = svector(0, LTT_85k-1);
40
```

```
470
                  = dmatrix (0, LQMA_40k-1, 0, NP-1);
           qes_dec = dmatrix (0, LQMA_40k-1, 0, NP-1);
           last_qlsf = dvector (0, NP-1);
           Isfq old = dvcctor (0, NP-1);
5
           lsfq old dec = dvector (0, NP-1);
                         = dvector (0, NP-1);
           Mcan :
           lsfq_mem_dec = dvector (0, NP-1);
10
           lsfq_mem_enc = dvector (0, NP-1);
                    Memory Allocation for Perceptual Weighting
15
                          = dvector (0, L_OLPIT-1);
           wspeech
           wspeech_mem = dvcctor (0, NP-1);
           wpdcf pole = dinatrix (0, N_SF_MAX, 0, NP-1);
                          = dmatrix (0, N_SF_MAX, 0, NP-1);
           wpdcf zero
                          = dvector (0, NP-1);
           tmp_ws_m
20
           ModiSig_m = dvector (0, NP-1);
           /*____*/
           /* Memory Allocation for Impulse Response of the synthesis filter */
           /*____*/
25
                    Memory Allocation for Signal Classification
30
            frame class mem = svector (0, CLA_MEM_SIZE-1);
            onstplsv_mem = svector (0, CLA_MEM_SIZE-1);
                                  = svector (0, CLA_MEM_SIZE-1);
            voiced_mem
                           = dvcctor (0, L_FRM+MEM_CLASS+L_LPCLHD-1);
            buffer_cla
35
                           = dvector (0, N_Lp-1);
            Lp_buffer
                           = dvector (0, LPC_WIN1-1);
            windowl
            P_{w} = dmatrix(0, 1, 0, SLOPE\_MAX\_SIZE-1);
40
```

```
buffer refi0 = dvector (0, CLA_MEM_SIZE-1);
           buffer wtilt = dvector (0, MAX_N_SF-1);
           buffer wmax = dvcctor (0, MAX_N_SF-1);
5
           buffer_wRp = dvector (0, MAX_N_SF-1);
           buffer max cla = dvector (0, MAX_N_SF-1);
                   Memory Allocation for Voice Activity Detection
                                                                     */
10
           lag buf = svector (0, LTP_BUFF_SIZE-1);
           pgain_buf = dvector (0, LTP_BUFF_SIZE-1);
15
            flag_vad_mem = svector (0, FLAG_VAD_MEM_SIZE-1);
                           = dmatrix (0, VAD_MEM_SIZE-1, 0, NP-1);
            vad Isf mem
                                    = dvector (0, VAD_MIN_MEM_SIZE-1);
            min energy mem
                                    = dvector (0, NP-1);
20
            mean_lsf
            norm_mean_lsf = dvector (0, NP-1);
            prev_cml_lsf_diff = dvector (0, VAD_MEM_SIZE-1);
                             = dvector (0, VAD_MEM_SIZE-1);
            prev_energy
25
                Memory Allocation for Smoothing parameters estimation
                                                                          */
30
            isf smooth = dvector (0, NP-1);
            N \text{ sub} = \text{svector}(0, N_SF_MAX-1);
            1sf old smo = dvector (0, NP-1);
            ma_lsf = dvector(0, NP-1);
35
            dSP_buf = dvector (0, DSP_BUFF_SIZE-1);;
            buffer smo = dvector (0, HI_LAG2+L_SF-1);
            buffer sum_smo = dvector (0, SMO_BUFF_SIZE-1);
            buffer_max_smo = dvector (0, SMO_BUFF_SIZE-1);
40
```

```
/*____*/
                  Memory Allocation for Open Loop Pitch Detection
5
           ol_lag = svcctor(0, N_SF_MAX);
                 = svector(0, N_SF_MAX);
           Rp_sub = dvector(0, N_SF_MAX-1);
10
           SincWindows = dvector (0, LEN_SINC_TAB-1);
           PlTmax0 = svector (0, NUM_MAX_SRCH-1);
           Rmax0 = dvector (0, NUM_MAX_SRCH-1);
15
           pitch f_mem = dvector (0, PIT_F_MEM-1);
           PitLagTab5b = dvector (0, LEN_PITCH_TAB_5BIT-1);
           PitLagTab7b = dvector (0, LEN_PITCH_TAB_7BIT-1);
           PitLagTab8b = dvector (0, LEN_PITCH_TAB_8BIT-1);
20
                  Memory Allocation for Open Pitch Pre-processing
25
           SincWindows_PP = dvector (0, LEN_SINC_TAB_PP-1);
           targ mem = dvector (0, MAX LAG-1);
30
                  Memory Allocation for Closed Loop Pitch Detection
           SincWindows_E = dvector (0, LEN_SINC_TAB_E-1);
35
           NewTg = dvector (0, L FRM+NP-1);
           lag f = dvector (0, N_SF_MAX-1);
           ext = dvector (0, MAX_LAG+L_SF-1);
40
```

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```
Memory Allocation for Short Term Algebraic Analysis
                   */
           MaxIdx = svector (0, MAXPN-1);
5
           p track_2_5_0 = \text{smatrix}(0, 2-1, 0, 32-1);
           p_{track_2_7_1} = smatrix(0, 2-1, 0, 80-1);
           p_track_3_2_80 = s3tensor(0, 16-1, 0, 3-1, 0, 4-1);
            p_{track_3_2_54} = s3tensor(0, 16-1, 0, 3-1, 0, 4-1);
10
            p track 5.4_0 = \text{smatrix}(0, 5-1, 0, 16-1);
            p_{track_5_3_1} = smatrix(0, 5-1, 0, 8-1);
            p_track_5_3_2 = smatrix(0, 5-1, 0, 8-1);
15
            p_{\text{track}_8_4_0} = \text{smatrix}(0, 8-1, 0, 16-1);
                    = dmatrix (0, L_SF-1, 0, L_SF-1);
            PHI
            unfcod = dmatrix (0, 1, 0, L_SF-1);
20
            fcod = dmatrix (0, 1, 0, L_SF-1);
            qua_unfcod = dmatrix (0, 1, 0, L_FRM-1);
            qua_fcod = dmatrix (0, 1, 0, L_FRM-1);
25
            unfcod dec = dmatrix (0, 1, 0, L_SF-1);
            wsp_m = dvector(0, L_WSP-1);
30
            hh_h = dvector(0, L_HF-1);
                     Memory Allocation for Gain Vector Quantisation
35
                       = dmatrix (0, 1, 0, N_SF_MAX-1);
         qua_gainQ
         past_energyq_2d = dvector (0, GVQ_VEC_SIZE_2D-1);
         past energyq_3d = dvector (0, GVQ_VEC_SIZE_3D-1);
40
```

```
past energyq 4d = dvector (0, GVQ_VEC_SIZE_4D-1);
         gp_buf = dvector (0, GP_BUF_SIZE-1);
 5
         pred_encrgy_d38 = dvector (0, GVQ_VEC_SIZE_3D-1);
         pred_cncrgy_d410 = dvector (0, GVQ_VEC_SIZE_4D-1);
        Prev_Beta_Pitch = dvector (0, BETA_BUF_SIZE-1);
10
         energy_pred_coeff_1 = dvector (0, GVQ_VEC_SIZE_3D-1);
         energy_pred_coeff_2 = dvector (0, GVQ_VEC_SIZE_3D-1);
        energy_pred_coeff_3 = dvector (0, GVQ_VEC_SIZE_3D-1);
15
        energy_pred_coeff4d_1 = dvector (0, GVQ_VEC_SIZE_4D-1);
        energy_pred_coeff4d_2 = dvector (0, GVQ_VEC_SIZE_4D-1);
        energy_pred_coeff4d_3 = dvector (0, GVQ_VEC_SIZE_4D-1);
        energy_pred_cocff4d_4 = dvcctor (0, GVQ_VEC_SIZE_4D-1);
20
         gain_cb_2_128
                         = dmatrix (0,MSMAX_2_128-1, 0, GVQ_VEC_SIZE_2D-1);
         gain_cb_2_128_8_5 = dmatrix (0, MSMAX_2_128-1, 0, GVQ_VEC_SIZE_2D-1);
25
                      Memory Deallocation for bit stream
            bitno0 = svector(0, 3-1);
30
            bitnol = svector(0, 3-1);
                  Memory Deallocation for Frame Erasure Concealenement
35
            cnrg buff = dvector(0, ERNG MEM SIZE-1);
            ext_dec_mem = dvector (0, MAX_LAG+L_SF-1);
40
```

```
Memory Allocation for Speech Synthesis
                                = dvector (0, NP-1);
           synth_mem
           synth_mcm_dec = dvcctor (0, NP-1);
 5
           dif_mem
                                = dvector (0, L_SF+NP-1);
                                = dvector (0, NP-1);
           target_mcm
           qua_synth_mcm = dvector (0, NP-1);
           qua dif mcm = dvector (0, L_SF+NP-1);
           qua_target_mem = dvector (0, NP-1);
10
           qua_ext = dvcctor (0, MAX_LAG+L_SF-1);
           sigsyn_dcc
                         = dvector (0, L_SF+NP-1);
15
           ext dec = dvector (0, MAX_LAG+L_SF-1);
20
           rcturn;
   /* FUNCTION : INI_dcallocate_memory ().
30 /* PURPOSE : This function performs memory deallocation.
   /*-----
   /* INPUT ARGUMENTS:
                    None.
35 /* OUTPUT ARGUMENTS:
                                            */
   /* INPUT/OUTPUT ARGUMENTS:
                                            */
```

```
476
   /* RETURN ARGUMENTS:
                                             */
   /*
                    Nonc.
   5 void
          INI_deallocate_memory ( void )
                  Memory Deallocation for Time-Domain Pre Filtering
10
           free dvector (pre flt num, 0, PPR_FLT_ORDER-1);
           free_dvector (pre_flt_den, 0, PPR_FLT_ORDER-1);
           free_dvector (pre_fit_buf_z, 0, PPR_FLT_ORDER-1);
           free_dvector (pre_flt_buf_p, 0, PPR_FLT_ORDER-1);
15
           free_dvcctor (lp_flt_num, 0, LP_FLT_ORDER-1);
           free_dvector (lp_flt_buf, 0, LP_FLT_ORDER-1);
20
                Memory Deallocation for Time-Domain Silence Enhancement */
           free_svector (zeroed,
                                  0, SE_MEM_SIZE-1);
           free dvcctor (zero rate, 0, SE MEM SIZE-1);
           free_dvcctor (low_rate, 0, SE_MEM_SIZE-1);
25
           free_dvector (high_rate, 0, SE_MEM_SIZE-1);
           free_dvector (low_neg, 0, 1);
           free_dvector (low_pos, 0, 1);
30
                Memory Deallocation for Tilt compansation Post Filtering
           free dvector (tc_buff exc, 0, L_LPC-1);
35
                 Memory Deallocation for Time-Domain Post Filtering
40
```

```
477
            free dvector (buff LTpost, 0, MAX_LAG+L_SF-1);
            free dvector (PF mem syn, 0, NP-1);
                   Memory Deallocation for Time-Domain Post Filtering
                                                                        */
5
                 */
            free_dvector (pst_flt_num, 0, PPR_FLT_ORDER-1);
            free dvector (pst_flt_den, 0, PPR_FLT_ORDER-1);
            free_dvcctor (pst_flt_buf_z, 0, PPR_FLT_ORDER-1);
10
            free dvector (pst_flt_buf_p, 0, PPR_FLT_ORDER-1);
                   Memory Deallocation for Linear Prediction Analysis
15
                                           0, L_LPC-1);
            free_dvector (siglpc,
            free dvector (tilt_window, 0, L_LPC-1);
            free_dvector (lpc_window,
                                           0, L_LPC-1);
20
            free_dvector (lpc_window1,
                                           0, L_LPC-1);
            free_dvector (lpc_window2, 0, L_LPC-1);
            free_dvector (bwe_factor, 0, NP);
            free dvector (rxx, 0, NP);
            free dmatrix (refl, 0, N_SF_MAX, 0, NP-1);
25
            free dvector (Isf new, 0, NP-1);
            free_dvector (lsf_old, 0, NP-1);
            free_dvector (lsf_mid, 0, NP-1);
30
            free dvector (IntLSF, C, 0, N_SF4-1);
            free dmatrix (pdcf, 0, N SF MAX, 0, NP-1);
            free dmatrix (pdcfq, 0, N_SF_MAX, 0, NP-1);
            free_dmatrix (pdcfq_dec, 0, N_SF_MAX, 0, NP-1);
35
                     Memory Allocation for the LSF quantization
```

```
478
              free_d3tensor (lsf_cb_08k, 0, MAXLTT_08k-1, 0,LMSMAX_08k-1,
                                               0, MAXLNp-1);
              frec_d3tensor (lsf_cb_40k, 0, MAXLTT_40k-1, 0,LMSMAX_40k-1,
                                               0, MAXLNp -1);
  5
              free_d3tensor (lsf_cb_85k, 0, MAXLTT_85k-1, 0,LMSMAX_85k-1,
                                               0, MAXLNp -1);
              free_svector (stage_cand_08k, 0, 2);
              free_svector (stage_cand_40k, 0, 3);
 10
              free_svector (stage_cand_85k, 0, 3);
              free_svector (MS_08k, 0, LTT_08k-1);
              free_svector (MS_40k, 0, LTT_40k-1);
              free_svector (MS_85k, 0, LTT_85k-1);
 15
              free dmatrix (ges,
                                               0, LQMA_40k-1, 0, NP-1);
              free_dmatrix (qes_dec, 0, LQMA_40k-1, 0, NP-1);
              free_dvector (last_qlsf, 0, NP-1);
 20
              free_dvector (lsfq_old, 0, NP-1);
              free_dvector (lsfq_old_dec, 0, NP-1);
              free dvector (Mean,
                                       0, NP-1);
              free_dvector (lsfq_mem_dec, 0, NP-1);
 25
              free_dvector (lsfq_mem_enc, 0, NP-1);
                        Memory Deallocation for Perceptual Weighting
                                                                          */
. 30
              free_dvector (wspeech,
                                               0, L_OLPIT-1);
              frec_dvcctor (wspeech_mem, 0, NP-1);
              free_dmatrix (wpdcf_pole, 0, N_SF_MAX, 0, NP-1);
              free_dmatrix (wpdcf_zero, 0, N_SF_MAX, 0, NP-1);
 35
              free_dvector (tmp_ws_m,
                                              0, NP-1);
              free_dvector (ModiSig_m,
                                               0, NP-1);
              /* Memory Deallocation for Impulse Response of the synthesis filter */
 40
```

```
*/
                   Memory Deallocation for Signal Classification
5
           free_svector (frame_class_mem, 0, CLA_MEM_SIZE-1);
                                          0, CLA_MEM_SIZE-1);
           free_svector (onstplsv_mem,
                                                  0, CLA_MEM_SIZE-1);
           free_svector (voiced_mem,
           free dvcctor (buffer cla, 0, L_FRM+MEM_CLASS+L_LPCLHD-1);
10
           free_dvector (Lp_buffer, 0, N_Lp-1);
           free_dvector (window1, 0, LPC_WIN1-1);
           free_dmatrix (P_w, 0, 1, 0, SLOPE_MAX_SIZE-1);
15
           free dvector (buffer refl0, 0, CLA_MEM_SIZE-1);
           free dvector (buffer wtilt,
                                          0, MAX_N_SF-1);
20
           free_dvcctor (buffer_wmax,
                                                  0, MAX N_SF-1);
                                                  0, MAX_N_SF-1);
           free_dvector (buffer_wRp,
           free dvector (buffer_max_cla,
                                          0, MAX_N_SF-1);
                   Memory Deallocation for Voice Activity Detection
25
            free svector (lag_buf,
                                   0, LTP_BUFF_SIZE-1);
            free_dvector (pgain_buf, 0, LTP_BUFF_SIZE-1);
30
                                           0, FLAG_VAD_MEM_SIZE-1);
           free_svector (flag_vad_mem,
                                                   0, VAD_MEM_SIZE-1, 0, NP-1);
            free_dmatrix (vad_lsf_mem,
            free_dvector (min_energy_mcm, 0, VAD_MIN_MEM_SIZE-1);
                                           0, NP-1);
            free_dvector (mean_lsf,
35
            free_dvector (norm_mean_lsf,
                                           0, NP-1);
            free_dvector (prev_cml_lsf_diff, 0, VAD_MEM_SIZE-1);
                                                   0, VAD MEM SIZE-1);
            free_dvector (prev_energy,
40
```

```
480
                Memory Deallocation for Smoothing parameters estimation
            frec_dvector (lsf_smooth, 0, NP-1);
 5
            free_svector (N_sub, 0, N_SF_MAX-1);
            free_dvector (lsf_old_smo, 0, NP-1);
            free dvector (ma 1sf, 0, NP-1);
            free_dvector (dSP_buf, 0, DSP_BUFF_SIZE-1);;
10
            free dvcctor (buffer smo, 0, HI_LAG2+L_SF-1);
            free_dvector (buffer_sum_smo, 0, SMO_BUFF_SIZE-1);
            free_dvector (buffer_max_smo, 0, SMO_BUFF_SIZE-1);
15
                   Memory Deallocation for Open Loop Pitch Detection
            free_svector (ol_lag,
                                   0, N_SF_MAX);
20
           free_svector (lag,
                                           0, N_SF_MAX);
           frec_dvector (Rp_sub,
                                   0, N_SF_MAX-1);
            free_dvector (SincWindows,
                                            0, LEN_SINC_TAB-1);
25
            free_svector (PITmax0, 0, NUM_MAX_SRCH-1);
            free_dvector (Rmax0,
                                   0, NUM_MAX_SRCH-1);
            free_dvector (pitch_f_mem, 0, PIT_F_MEM-1);
30
            free_dvector (PitLagTab5b,
                                            0, LEN_PITCH_TAB_5BIT-1);
            frec_dvector (PitLagTab7b,
                                            0, LEN_PITCH_TAB_7BIT-1);
            free_dvector (PitLagTab8b,
                                            0, LEN_PITCH_TAB_8BIT-1);
35
                   Memory Deallocation for Open Pitch Pre-processing
                                                                       */
            free_dvector (SincWindows_PP, 0, LEN_SINC_TAB_PP-1);
40
```

```
481
           free_dvector (targ_mem, 0, MAX_LAG-1);
           /*____*/
                   Memory Deallocation for Closed Loop Pitch Detection
5
           free_dvector (SincWindows_E, 0, LEN_SINC_TAB_E-1);
           free dvector (NewTg,
                                   0, L FRM+NP-1);
                                   0, N_SF_MAX-1);
10
           free_dvector (lag_f,
                                           0, MAX_LAG+L_SF-1);
           free_dvector (ext,
              Memory Allocation for Residual and target signal buffer
15
                  Memory Deallocation for Short Term Algebraic Analysis
20
            free svector (MaxIdx, 0, MAXPN-1);
            free_dmatrix (PHI,
                                    0, L_SF-1,
                                                    0, L_SF-1);
25
            free smatrix(p_track_2_5_0, 0, 2-1, 0, 32-1);
            free_smatrix(p_track_2_7_1, 0, 2-1, 0, 80-1);
            free s3tensor(p_track_3_2_80, 0, 16-1, 0, 3-1, 0, 4-1);
            free_s3tensor(p_track_3_2_54, 0, 16-1, 0, 3-1, 0, 4-1);
30
            free smatrix(p_track_5_4_0, 0, 5-1, 0, 16-1);
            free_smatrix(p_track_5_3_1, 0, 5-1, 0, 8-1);
            frec_smatrix(p_track_5_3_2, 0, 5-1, 0, 8-1);
35
            free_smatrix(p_track_8_4_0, 0, 8-1, 0, 16-1);
            free dmatrix (unfcod, 0, 1, 0, L_SF-1);
            free dmatrix (fcod,
                                    0, 1, 0, L_SF-1);
```

```
482
                                           0, 1, 0, L_FRM-1);
            free_dmatrix (qua_unfcod,
            free_dmatrix (qua_fcod, 0, 1, 0, L_FRM-1);
 5
            free_dmatrix (unfcod_dec,
                                           0, 1, 0, L_SF-1);
            free_dvcctor (wsp_m, 0, L_WSP-1);
            free dvector (hh_hf, 0, L_HF-1);
10
                   Memory Deallocation for Gain Vector Quantisation
            free_dmatrix (qua_gainQ, 0, 1, 0, N_SF_MAX-1);
15
         free_dvector (past_energyq_2d, 0, GVQ_VEC_SIZE_2D-1);
         free_dvector (past_energyq_3d, 0, GVQ_VEC_SIZE_3D-1);
         free_dvector (past_energyq_4d, 0, GVQ_VEC_SIZE_4D-1);
20
         free_dvector (gp_buf, 0, GP_BUF_SIZE-1);
         free dvector (pred energy d38, 0, GVQ_VEC_SIZE_3D-1);
         free dvector (pred energy_d410, 0, GVQ_VEC_SIZE_4D-1);
25
         free_dvector (Prev_Beta_Pitch,0, BETA_BUF_SIZE-1);
         free_dvector (energy_pred_coeff_1, 0, GVQ_VEC_SIZE_3D-1);
         frec_dvector (energy_pred_coeff_2, 0, GVQ_VEC_SIZE_3D-1);
30
         free_dvector (energy_prcd_coeff_3, 0, GVQ_VEC_SIZE_3D-1);
         free_dvector (energy_pred_coeff4d_1, 0, GVQ_VEC_SIZE_4D-1);
35.
         free_dvector (cnergy_pred_coeff4d_2, 0, GVQ_VEC_SIZE_4D-1);
         free_dvector (energy_pred_coeff4d_3, 0, GVQ_VEC_SIZE_4D-1);
         free_dvector (energy_pred_coeff4d_4, 0, GVQ_VEC_SIZE_4D-1);
         free_dmatrix (gain_cb_2_128, 0,MSMAX_2_128-1, 0, GVQ_VEC_SIZE_2D-1);
         free_dmatrix (gain_cb_2_128_8_5,0,MSMAX_2_128-1, 0, GVQ_VEC_SIZE_2D-1);
40
```

```
Memory Deallocation for bit stream
5
           free_svector(bitno0, 0, 3-1);
           free_svector(bitno1, 0, 3-1);
                 Memory Deallocation for Frame Erasure Concealenement
10
           free dvector (enrg_buff, 0, ERNG_MEM_SIZE-1);
           free_dvector (ext_dec_mcm, 0, MAX_LAG+L_SF-I);
15
                    Memory Deallocation for Speech Synthesis
               */
                                           0, NP-1);
            free dvector (synth_mem,
20
            free_dvector (synth_mem_dec, 0, NP-1);
            free_dvector (dif_mem,
                                           0, L_SF+NP-1);
            free_dvector (target_mem,
                                           0, NP-1);
            free_dvector (qua_synth_mem, 0, NP-1);
                                           0, L_SF+NP-1);
            free_dvector (qua_dif_mcm,
25
            free_dvcctor (qua_target_mem, 0, NP-1);
            free dvector (qua_ext, 0, MAX_LAG+L_SF-1);
                                                 0, L_SF+NP-1);
            free_dvector (sigsyn_dec,
30
                                                 0, MAX_LAG+L_SF-1);
            free_dvector (ext_dec,
35
            return;
```

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```
484
  */
  /* FUNCTION: INI_parameters_setup().
  /* PURPOSE : Performs the encoder parameter initialisation.
  /* INPUT ARGUMENTS:
                                                    */
       (INT32 ) argc: number of input parameters.
       _(char *[]) argv: pointer to the input parameters.
                                                  */
   /* OUTPUT ARGUMENTS:
                                                   */
        (FILE **) fp speech in: input speech file.
       _(FILE **) fp_bitstream: input speech file.
       _(FILE **) fp_mode: input speech file.
15 /*
       _(FILE **) fp_signaling: input speech file.
        _(INT16 **) smv_mode: SMV mode.
   /* INPUT/OUTPUT ARGUMENTS:
                                         */
20 /*
                _ None.
   /* RETURN ARGUMENTS:
                Nonc.
25
   void INI_parameters_sctup (INT32 argc, char *argv [], FILE **fp_speech_in,
          FILE **fp_bitstream, FILE **fp_mode, FILE **fp_signaling,
          INT16 *smv_mode)
30
          INT16 i, non_switch_cnt;
35
                          Initialisation
          *fp speech in = NULL;
          *fp bitstream = NULL;
                               = NULL;
           *fp_mode
40
```

```
= NULL;
            *fp_signaling
            if(argc < 5)
5
                     printf ("Usage: %s -m M [-f mode_file] [-s signaling_file] ",
                                               argv[0]);
                     printf (" input_speech_file output_bitstream_file\n");
                     exit(1);
10
                     }
            else
                     i = 1;
15
                     (*smv_mode) = 0;
                     non_switch_cnt = 0;
                     while (i < argc)
                              if (argv[i][0] != '-')
20
                                       if (non_switch_cnt > 2)
                                               nrerror("Usage: smv_enc -m M [-f mode_file]
                                               [-s signaling_file] input_speech_file
                                                        output_bitstrcam_file\n");
25
                                       if (non switch cnt == 0)
                                                {
                                                *fp_speech_in = file_open_rb (argv[i]);
                                                printf("Input speech file : %s\n", argv[i]);
                                                non_switch_cnt++;
30
                                                }
                                       elsc
                                                 *fp_bitstream = file_open_wb (argv[i]);
                                                 printf("Output bitstream file: %s\n", argv[i]);
35
                                                non_switch_cnt++;
                                                }
                                       i++;
                                       }
                               else
40
```

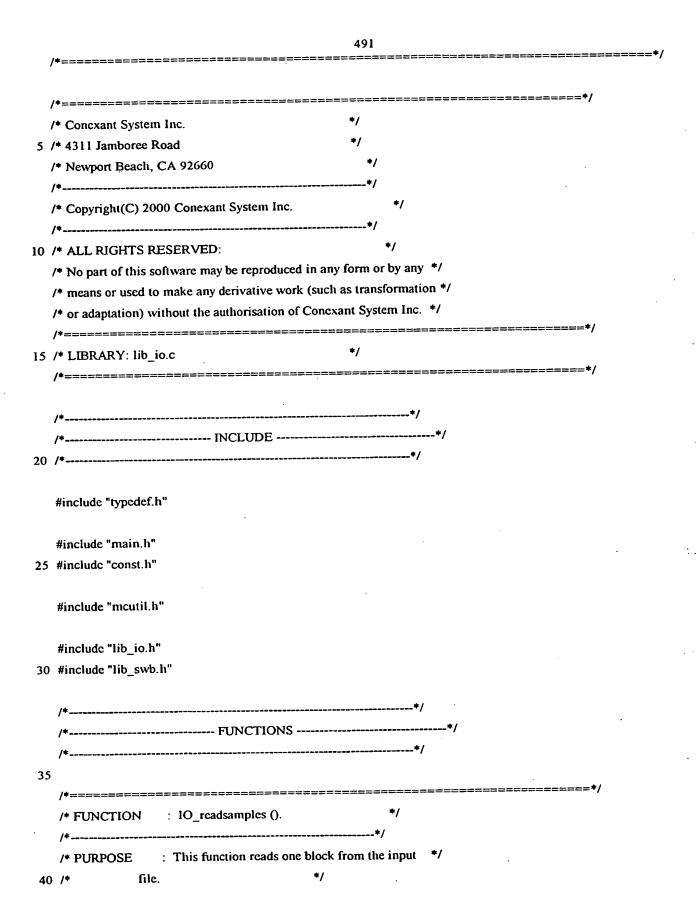
```
486
                                       {
                                        switch(argv[i][1])
                                                {
                                                 case 'm':
                                                                  i++;
 5
                                                                           (*smv_mode) = atoi(argv[i]);
                                                                           if (((*smv_mode) < 0) || ((*smv_mode) >
    2))
                                                                                    {
                                                                                    printf("SMV mode = %hd (0, 1,
10 and
                                                                                                      2 allowed)\n",
    (*smv_mode));
                                                                                    exit(0);
                                                                                    }
15
                                                                          i++;
                                                         break;
                                                case 'f': i++;
                                                                           *fp_modc = fopen(argv[i], "r");
20
                                                                          printf("Mode control file : %s\n", argv[i]);
                                                                          i++;
                                                         break;
                                                case 's': i++;
25
                                                                           *fp_signaling = file_open_rb (argv[i]);
                                                                          printf("Signaling file: %s\n", argv[i]);
                                                                          i++;
                                                         break;
30
                                                default: printf ("Usage: %s -m M [-f modc_file] [-s signaling_file] ",
                                                                                                     argv[0]);
                                                                          printf (" input_speech_file
   output_bitstream_file\n");
                                                                          exit(1);
35
                                                         break;
                                                }
                                       }
                              }
40
```

	487	
/*		
relum;		
/#	·*/	
*	*/ ·	
•=====================================		*
* FUNCTION: INI_init_decoder ().	*/	
* ALGORITHM :	*/	
* INPUT ARGUMENTS :	·	
_ 110110.		
* OUTPUT ARGUMENTS:	*/	
_ 110110.		
	*/	
	*/	
* RETURN ARGUMENTS :	*/	
_ None.	*/	=
/*		
void INI_init_encoder(void)		
()		
/+	+/	
fix rate = RATE8 5K:		
I.K_1.110 141320_553,		
/*	*/	
BIT init lib ().		
PPR_silence_enhan_init ();		
	return; /*	/*

```
488
            PPR_filters_init ();
            VAD_init_lib ();
            CLA_init_lib ();
            PPP_init_lib ();
 5
            PIT_init_lib ();
            GEQ_init_lib ();
            LPC_init_lib ();
            PWF_init_lib ();
            SMO_init_lib ();
10
            LSF_Q_init_lib ();
         SNR_init_lib();
            FCS_init_lib ();
            LTP_init_lib ();
            PRC_init_lib ();
15
            return;
20
   /* FUNCTION : INI_init_decoder ().
                                                         */
   /* PURPOSE : Performs the decoder initialisation.
30 /* ALGORITHM:
   /* INPUT ARGUMENTS:
                  Nonc.
35 /* OUTPUT ARGUMENTS:
   /* INPUT/OUTPUT ARGUMENTS:
   /*
                  Nonc.
40 /*--
```

```
489
  /* RETURN ARGUMENTS:
            None.
                5 void INI_init_decoder(void)
       { ,
        fix_rate_mem = RATE8_5K;
10
        past_bfi = 0;
        ppast_bfi
                   = 0;
        bfi_caution = 0;
      nbfi count = MAX_BFI_COUNT;
        N bfi = 0;
        bfh_oh = 0;
15
        BIT_init_lib ();
        GEQ_init_lib ();
20
        PPR_filters_init ();
        LPC_init_lib ();
        LSF_Q_init_lib ();
        CLA_init_lib ();
        FCS_init_lib();
25
        LTP_init_lib ();
30
        return;
   40
```

Conexant System Inc.		*/	•		
/* 4311 Jamborce Road		*/			
/* Newport Beach, CA 92660		*/			
- , <i>,</i>	* Copyright(C) 2000 Conexant System Inc.		*/		
/* Copyright(C) 2000 Conexant					
/*	*				
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/* No part of this software may b	oe reproduced in an	y form or by any */			
/* means or used to make any de	erivative work (such	as transformation */	•		
/* or adaptation) without the aut	•				
/*			=====*/		
/* PROTOTYPE FILE : lib_ini.i		*/			
/*			====*/ `		
/*	*************************	·*/			
		•			
/*					
/*void INI_allocate_memory	·				
/*FU /*FU void INI_allocate_memory void INI_dcallocate_memory	(void); (void);	+/·			
/*void INI_allocate_memory void INI_dcallocate_memory	(void); (void);], FILE **, FILE **, FILE **,			
/*void INI_allocate_memory void INI_dcallocate_memory	(void); (void);	+/·			
/*void INI_allocate_memory void INI_dcallocate_memory void INI_parameters_setup	(void); (void); (INT32, char *[], FILE **, FILE **, FILE **,			
/*void INI_allocate_memory void INI_dcallocate_memory void INI_parameters_setup void INI_init_encoder	(void); (void); (INT32, char *[(void);], FILE **, FILE **, FILE **,			
/*void INI_allocate_memory void INI_dcallocate_memory void INI_parameters_setup void INI_init_encoder	(void); (void); (INT32, char *[], FILE **, FILE **, FILE **,			
/*void INI_allocate_memory void INI_dcallocate_memory void INI_parameters_setup void INI_init_encoder	(void); (void); (INT32, char *[(void);], FILE **, FILE **, FILE **,			
/*void INI_allocate_memory void INI_dcallocate_memory void INI_parameters_setup void INI_init_encoder void INI_init_decoder	(void); (void); (INT32, char *[(void); (void);], FILE **, FILE **, FILE **,			



```
492
   /* INPUT ARGUMENTS:
            _(FILE *) file_x: input file.
   /*
            _ (INT32) N: number of samples to be
 5 /*
                        extracted.
   /* OUTPUT ARGUMENTS :
            _ (FLOAT64 []) x: extracted frame.
            _(INT16 []) s_x: extrac. frame short format. */
   /* INPUT/OUTPUT ARGUMENTS:
                None.
   /* RETURN ARGUMENTS:
15 /*
                _ None.
   INT32 IO_readsamples (FILE *file_x, INT16 s_x[], FLOAT64 x [], INT32 N)
20
           INT32 rd_samp, i, r_val;
25
           rd_samp = fread(s_x, sizeof(INT16), N, file_x);
30
           if (rd_samp > 0)
                   for(i = 0; i < N; i++)
35 #ifdef BYTE_SWAP_INPUT
                           s_x[i] = bytc_swap_int16(s_x[i]);
   #endif
40
                           x[i] = (FLOAT64) s_x[i];
```

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```
493
                   }
              r_val = rd_samp;
5
        elsc
            r_val = 0;
10
        return r_val;
  /* FUNCTION : IO_writesamples ().
20 /* PURPOSE : This function write one block into the output */
  /* file.
                               */
  /* INPUT ARGUMENTS:
  /* _ (FILE *) file_y: output file.
25 /* _ (INT32) N: number of samples to write. */
        _(FLOAT64 []) y: output frame.
  /*_____
  /* OUTPUT ARGUMENTS:
  /* _ (INT16 []) s_y: output frame short format. */
  /* INPUT/OUTPUT ARGUMENTS:
           Nonc.
  /*_____
  /* RETURN ARGUMENTS:
35 /*
           _ None.
  INT32 IO_writesamples (FILE *file_y, FLOAT64 y [], INT16 s_y[], INT32 N)
40
```

```
INT32 i, wt_samp;
            FLOAT64 tmp;
 5
            for (i = 0; i < N; i++)
                    tmp = y[i];
10
                    if(tmp \ge 0.0)
                            tmp += 0.5;
                    elsc
                            tmp = 0.5;
15
                    if(timp > 32767.0)
                            tmp = 32767.0;
                    if(tmp < -32768.)
20
                            tmp = -32768.;
                    if((INT16)tmp < 0.0)
                            s_y[i] = (INT16)tmp + 2;
                    else
25
                            s_y[i] = (INT16)tmp;
   #ifdef BYTE_SWAP_OUTPUT
                    s_y[i] = byte_swap_int16(s_y[i]);
30 #endif
                    }
            wt_samp = fwrite(s_y, sizeof(INT16), N, file_y);
35
    #ifdef VERBOSE
            if (wt_samp != N)
40
                     nrerror("Error Writing output Samples!");
```

	#endif
	/**/
	/*
5	return wt_samp;
-	
	/**/
	}
	\cdot
10	/ * */
	`/*====================================
	/*========*** /**/
	/*=====================================
15	·
	/*=====================================
	/*====================================
20	/* Conexant System Inc. */
	/* 4311 Jamborce Road */ /* Neumont Reach, CA 92660 */
	/* Newport Beach, CA 92660 */ /**/
	/* Copyright(C) 2000 Conexant System Inc. */
25	/**/
	/* ALL RIGHTS RESERVED: */
	/* No part of this software may be reproduced in any form or by any */
	/* means or used to make any derivative work (such as transformation */
	/* or adaptation) without the authorisation of Conexant System Inc. */
30	/*=====================================
	/* PROTOTYPE FILE: lib_io.h */
	/*====================================
	/**/
35	/**/
,	/**/
	INT32 IO_readsamples (FILE *, INT16 [], FLOAT64 [], INT32);
	INT32 IO_writesamples (FILE *, FLOAT64 [], INT16 [], INT32);
40	

496 /*-----*/
/*-----*/

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			497	
	*======================================			
	*======================================	**********	*/	=======================================
	* Conexant System Inc.		*/	
	* 4311 Jamboree Road		*/	
	* Newport Beach, CA 92660		•	
	*		*/	·
	* Copyright(C) 2000 Conexant Sy		•	
	/\$		*/	
	* ALL RIGHTS RESERVED:		•	
4	/* No part of this software may be	reproduced in any	7 Ionn of by any 17	
	/* means or used to make any deriv	valive work (such	as transformation /	
	/* or adaptation) without the autho	risation of Conex	ant System Inc. +/	=======================================
	/*====================================	==========	*/	
13	/*====================================			=======================================
	/*		*/	
	/*INCL			
	/*			
20	,			
	#include "typedef.h"	•		
	#include typeder.ii			
	#include "main.h"			•
25	#include "const.h"			
23	#include "mcutil.h"			
	#include "gputil.h"			
	#include "ext_var.h"			
	Willedge CXL_var			
20	#include "lib_flt.h"			
30	#include "lib_lpc.h"			
	#mende no_ipe.ii			
	/*		- +/	
	/*DE			
25	/*			
33				
	#define NB_BIS	5		
	#define MAX_LPC_ORDER	50		
	#define RESL	0.005		
AC	Hefine NRIS	6		

```
#define Pl2
                         6.283185307179586
   /*____*/
   /*-----*/
 /* FUNCTION : LPC_init_lib ().
10 /* PURPOSE : This function performs the initialisation of the */
  /*
           global variables of the LPC library.
  /* INPUT ARGUMENTS:
  /*
               _ None.
                                 */
  /* OUTPUT ARGUMENTS:
           _ None.
  /* INPUT/OUTPUT ARGUMENTS:
20 /*
                                 */
               None.
  /* RETURN ARGUMENTS:
               None.
25
  void LPC_init_lib (void)
30
        INT16 i, 1;
        FLOAT64 x, y;
                Generate the LPC Hamming window
35
        I = L_LPC/2;
        for (i = 0; i < 1; i++)
40
             {
```

```
499
                     x = \cos(i*PI/(FLOAT64)I);
                     lpc\_window[i] = 0.54 - 0.46 * x;
                    }
            for (i = 1; i < L_LPC; i++)
 5
                     x = \cos((i-l)*PI/(FLOAT64)(L_LPC-l));
                     lpc\_window[i] = 0.54+0.46 * x;
                    }
10
                  Generate the LPC Hamming window for the last sub-frame
            l = L_LPC-(L_LPCLHD+L_SF4/2-10);
15
            for (i = 0; i < l; i++)
                     x = \cos(i*PI/(FLOAT64)I);
                     lpc\_window1[i] = 0.54 - 0.46 * x;
20
                    }
            for (i = 1; i < L_LPC; i++)
                     x = cos((i-l)*PI/(FLOAT64)(L_LPC-I));
25
                     lpc\_window1[i] = 0.54+0.46 * x;
                    }
30
                        Generate the window for look ahead
            I = L LPC - L LPCLHD/2;
35
            for (i = 0; i < 1; i++)
                     x = \cos(i*PI/(FLOAT64)I);
                     lpc\_window2[i] = 0.54-0.46 * x;
40
```

```
500
          for (i = 1, i < L_LPC, i++)
                   {
                   x = cos((i-1)*0.48*PI/(FLOAT64)(L_LPC-I));
                   lpc\_window2[i] = x;
 5
                    Generate the 60 Hz bandwidth expansion factor
                      with white noise correction factor
10
           bwe_factor\{0\} = 1.0001;
           x = 2.0*PI*60.0/FS;
           for (i = 1; i < NP+1; i++)
15
                   y = -0.5*sqr(x*(FLOAT64)i);
                   bwe_factor[i] = exp(y);
20
                   = 0.0;
           erg
           lpcgain = 0.0;
25
           sub_lpcg = 0.0;
                      */
30
                       LPC Adaptive Interpolation Coeffcients
            IntLSF_C[0] = 0.5;
           IntLSF_C[1] = 0.6;
35
            IntLSF_C[2] = 0.7;
            IntLSF_C[3] = 0.8;
40
            return;
```

```
/* FUNCTION : LPC_analysis ().
10 /* PURPOSE : This function performs windowing.
   /* INPUT ARGUMENTS:
                                                      */
                              LPC frame size.
         _(INT16 ) l_lpc:
   /*
         _(FLOAT64 []) sig:
                                input frame.
         _(FLOAT64 []) lpc_window: analysis window.
15 /*
         _(FLOAT64 []) bwc_factor: bandwidth expansion
         _(INT16 ) Order:
                                prediction order.
         _(INT16 ) flag_flat: flat speech flag.
20 /* OUTPUT ARGUMENTS:
         _ (FLOAT64 []) rxx:
                                 autocrrelation function.
   /*
                                 output reflection coeff.
   /*
         _(FLOAT64 []) refl:
                                  output LPC prediction error. */
         _(FLOAT64 *) pderr:
          _(FLOAT64 []) lpc_alpha: output LP filter coeff.
                                                          */
          _(FLOAT64 []) lsf:
                                line spectrum frequencies.
25 /*
          _ (FLOAT64 *) lpc_gain: output LPC gain.
   /* INPUT/OUTPUT ARGUMENTS:
                     _ Nonc.
   /* RETURN ARGUMENTS:
                                               */
                     _ None.
35 void LPC_analysis (INT16 1_lpc, FLOAT64 sig [], FLOAT64 lpc_window [],
                           FLOAT64 rxx[], FLOAT64 bwc_factor[], FLOAT64 rcfl [],
                           FLOAT64 *pderr, FLOAT64 lpc_alpha [], FLOAT64 lsf [],
                           INT16 LPC_ORDER, FLOAT64 *lpcgain, INT16 flag_flat)
```

```
502
            FLOAT64 w, energy, val;
            INT16 i;
 5
            energy = 0.0;
            for (i = 0; i < l_lpc; i++)
                    energy += (INT16)sig[i]*(INT16)sig[i];
10
            if(energy == 0.0)
                    {
                    ini_dvector (lpc_alpha, 0, LPC_ORDER-1, 0.0);
15
                    ini dvector (rxx, 0, LPC_ORDER, 0.0);
                    ini_dvector (refl, 0, LPC_ORDER-1, 0.0);
20
                    (*pderr) = 0.0;
                    (*lpcgain) = -DBL_MAX;
            else
25
                             Windowing for Linear Prediction Analysis
                    mul_dvector (sig, lpc_window, siglpc, 0, l_lpc-1);
30
                                Autocorrelation Function
                    LPC_autocorrelation (siglpc, l_lpc, rxx, (INT16)(LPC_ORDER+1));
35
                                  Bandwidth Expansion
40
```

```
503
                     mul_dvector (rxx, bwe_factor, rxx, 0, LPC_ORDER);
                                 Leroux-Gueguen Recursion
 5
                    LPC_leroux_gueguen (rxx, refl, pderr, LPC_ORDER);
                           PARCOR to prediction coefficient convertion
10
                    LPC_refl2pred(refl, lpc_alpha, LPC_ORDER);
15
                                 Bandwith expassion
                    (*lpcgain) = -10.*log10((*pdcrr)/(rxx[0] + EPSI));
20
                     if (Nag_Nat == 1)
25
                             if ((*lpcgain) < 10.0)
                                     w = 0.995;
                             else if ((*lpcgain) < 20.0)
                                     w = 0.995;
                             else
30
                                     w = 0.995;
                             }
                     else
                             if ((*lpcgain) < 10.0)
                                     w = 0.995;
35
                             else
                                     if ((*lpcgain) \le 20.0)
                                             w = 0.99;
40
                                      else
```

```
504
                                     w = 0.98;
                              }
                        }
 5
                 val = 1.0;
                 for (i = 0; i < LPC\_ORDER; i++)
10
                        val *= w;
                        lpc_alpha[i] *= val;
15
                  LPC to LSP CONVERSION
20
          LPC_lpctolsf (lpc_alpha, lsf, LPC_ORDER);
25
          return;
  /* FUNCTION : LPC_refl2pred ().
  /*____*/
35 /* PURPOSE : This function calculate the prediction coeff. */
        using the PARCOR coefficients.
  /* INPUT ARGUMENTS:
  /*
          _ (FLOAT64 []) refl : output reflection coeff. */
40 /*
          _ (INT16 ) LPC_ORDER : LPC order.
```

```
505
  /* OUTPUT ARGUMENTS:
           _ (FLOAT64 []) a : output LP filter coeff. */
5 /* INPUT/OUTPUT ARGUMENTS:
                                            */
  /* __None.
  /* RETURN ARGUMENTS:
                   _ None.
          LPC_refl2pred (FLOAT64 refl [], FLOAT64 a [], INT16 LPC_ORDER)
  void
15
          INT32 m, n, j;
           FLOAT64 x;
20
           for (m = 0; m < LPC_ORDER; m ++)
25
                  a[m] = -refl[m];
                  for (j = 0; j < m / 2; j ++)
30
                         {
                          n = m - 1 - j;
                          x = a[j] + refl[m] * a[n];
                          a[n] = a[n] + refl[m] * a[j];
                          a[j] = x;
35
                          }
                   /* Update the prediction coefficients every 2 cycles */
40
```

```
if (m & 1)
                                a[m/2] = a[m/2] + refl[m] * a[m/2];
 5
10
             return;
    /* FUNCTION
                     : LPC_leroux_gueguen ().
20 /* PURPOSE
                     : This function performs Leroux Gueguen recursion. */
    /*
   /* This subroutine implements a modified form of Durbin's
                                                                      */
   /* algorithm for solving a set of auto-correlation equations. This */
    /* routine uses an intermediate variable which is constrained to */
25 /* be less than the energy of the signal, i.e. r [0]. This
   /* variable takes the role of the predictor coefficients which
   /* normally are used in Durbin's form of the algorithm. This
   /* routine returns only the reflection coefficients.
   /* Reference: J. Leroux and C.J. Gueguen, "A Fixed Point
                                                                      */
30 /*
              Computation of the Partial Correlation
    /*
              Coefficients", IEEE Trans. ASSP, June 1977,
              pp. 257-259.
                                                      */
   /* In the case that either numerical instability or an
35 /* inappropriate auto-correlation vector results in a set of
   /* coefficients corresponding to an unstable filter at some stage */
   /* in the recursion, this routine returns the coefficients as
   /* determined at the previous iteration. This is equivalent to
   /* truncating the auto-correlation coefficient at a point such
                                                                    */
40 /* that it is positive definite.
                                                        */
```

```
507
   /* INPUT ARGUMENTS:
              _(FLOAT64 []) r : input autocorrelation.*/
              _ (INT16 ) LPC_ORDER : LPC order.
   /* OUTPUT ARGUMENTS:
              _(FLOAT64 []) refl : output reflection coeff. */
   /*
               _ (FLOAT64 *) re : output residual energy. */
10 /* INPUT/OUTPUT ARGUMENTS:
   /*
                                             */
                    None.
   /* RETURN ARGUMENTS:
                    _ None.
          LPC_leroux_gucgucn (FLOAT64 r [], FLOAT64 refl [], FLOAT64 *re,
   void
                                                                       INT16 LPC_ORDER)
20
           INT32 k, i;
           FLOAT64
                          rf, sum;
           FLOAT64
                          *ep, *en;
25
           ep = dvector (0, LPC_ORDER-1);
           en = dvector (0, LPC_ORDER-1);
30
                           Initialisation
35
           (*re) = r[0];
           for (i = 0; i < LPC\_ORDER; i++)
                   cp[i] = r[i+1];
40
```

```
508
                   cn[i] = r[i];
                   }
                                                         */
                             Algorithm
5
           for (k = 0; k < LPC\_ORDER; k++)
                   {
10
                              Check for zero prediction error
           if (cn[0] == 0.0) && (cp[k] == 0.0)
                                   rf = 0.0;
15
            else
                           rf = -ep[k] / en[0];
                    /*____*/
                    /* Calculate the error (equivalent to re = re * (1-rf**2))
20
                    /* A change in sign of PRERR means that rf has a magnitude */
                    /* greater than unity (corresponding to an unstable
                    /* synthesis filter)
25
            cn[0] += rf * ep[k];
                    if (en[0] < 0.0)
                                   brcak;
30
                          = cn[0];
            (*re)
35
                    refl[k] = rf;
                    if (k == (LPC_ORDER-1))
                                   return;
                    for (i = k+1; i < (LPC_ORDER-1); i++)
40
```

```
509
                                sum = ep[i] + rf * cn[i-k];
                                en [i-k] += rf * cp[i];
                                ep [i]
                                       = sum;
5
                 ep [LPC_ORDER-1] += rf * en [LPC_ORDER-k-1];
                    Fixup for negative prediction error
10
          printf ("\nLPC_leroux_gueguen - negative error energy\n");
          for (i = k; i < LPC_ORDER; i++)
15
                 refl [i] = 0.0;
20
        free_dvector (ep, 0, LPC_ORDER-1);
        free_dvector (cn, 0, LPC_ORDER-1);
25
        · return;
30
   /* FUNCTION : LPC_pred2refl ().
35 /*----*/
   /* PURPOSE : This function calculate the PARCOR coefficients */
            using the prediction coeff.
   /* INPUT ARGUMENTS:
            _ (FLOAT64 []) a : output LP filter coeff. */
```

```
_ (INT16 ) LPC_ORDER : LPC order.
   /* OUTPUT ARGUMENTS:
            _(FLOAT64 []) refl : output reflection coeff. */
   /* INPUT/OUTPUT ARGUMENTS:
                   None.
   /* RETURN ARGUMENTS :
10 /*
                   None.
                                           */
   void LPC_pred2refl (FLOAT64 a [], FLOAT64 refl [], INT16 LPC ORDER)
          {
15
           FLOAT64 f[MAX_LPC_ORDER];
           INT16 m, j, n;
           FLOAT64 km, denom, x;
20
                 *----*/
           if (LPC_ORDER > MAX_LPC_ORDER)
25
                        nrerror ("LPC order is too large!!!");
                         Initialisation
30
          cpy_dvector (a, f, 0, LPC_ORDER-1);
35
          for (m = LPC_ORDER-1; m \ge 0; m--)
                 km = f[m];
                 if km \le -1.0 \parallel km \ge 1.0
40
                         printf("Nonstable reflection coeffs !!!\n");
```

```
return;
5
                     refl[m] = -km;
                     denom = 1.0/(1.0 - km + km);
                     for (j = 0; j < m/2; j++)
10
                              n = m - 1 - j;
                              x = denom * f[j] + km * denom * f[n];
                              [n] = denom * [n] + km * denom * [j];
                              f[j] = x;
15
                     if (m & 1)
                                      f[j] = denom * f[j] + km * denom * f[j];
                     }
20
             return;
25
30 /* FUNCTION
                     : LPC_lpctolsf ().
                     : This function onvert predictor coefficients
   /* PURPOSE
                                                    */
                 to LSF's.
   /*
   /*
         The transfer function of the predictor filter is transformed */
35 /*
         into two reciprocal polynomials having roots on the unit
    /*
    /*
         circle. These roots of these polynomials interlace. It is */
         these roots that determine the line spectral frequencies. The*/
         two reciprocal polynomials are expressed as series expansions*/
          in Chebyshev polynomials with roots in the range -1 to +1. The*/
40 /*
```

```
512
   /*
        inverse cosine of the roots of the Chebyshev polynomial
   /*
        expansion gives the line spectral frequencies. If NPTS line */
        spectral frequencies are not found, this routine signals an */
        error condition.
 5 /*
                                          */
   /* INPUT ARGUMENTS:
       _ (FLOAT64 []) PRCOF: Predictor coefficients.
       _(INT16 ) LPC_ORDER : LPC order.
10 /* _(INT16 ) NPTS: Number of coefficients (at most 50)*/
   /* OUTPUT ARGUMENTS:
       _ (FLOAT64 []) FRLSF: line spectral frequencies (in
   /*
                    ascending order).
   /* INPUT/OUTPUT ARGUMENTS:
   /* RETURN ARGUMENTS:
   void LPC_lpctolsf(FLOAT64 PRCOF[], FLOAT64 FRLSF[], INT16 NPTS)
25
           INT16 NC[2], j, i, NF, IP;
          FLOAT64 F1[(MAX_LPC_ORDER+1)/2+1], F2[(MAX_LPC_ORDER+1)/2+1],
                  T[2][(MAX_LPC_ORDER+1)/2+1];
30
          FLOAT64 XLOW, XMID, XHIGH, XINT, YLOW, YMID, YHIGH:
35 #ifdef VERBOSE
           if (NPTS > MAX_LPC_ORDER)
                  nrerror("PTOLSF - Too many coefficients!!!!");
   #endif
```

```
513
            /* Determine the number of coefficients in each of the polynomials */
                         with coefficients T(.,1) and T(.,2).
5
            if ((NPTS \% 2) != 0)
                     NC[1] = (NPTS+1) / 2;
                     NC[0] = NC[1] + 1;
10
             else
                     NC[1] = NPTS/2 + 1;
                     NC[0] = NC[1];
                     }
15
             /* Let D=z**(-1), the unit delay, then the predictor filter with
             /* N coefficients is
20
                     N
                P(D) = SUM p(n) D.
                     n=1
                                                                               */
             /* The error filter polynomial is A(D)=1-P(D) with N+1 terms.
25
             /* Two auxiliary polynomials * are formed from the error filter
             /* polynomial,
             /* FI(D) = A(D) + D**(N+1) A(D**(-1)) (N+2 terms, symmetric)
                F2(D) = A(D) - D^{**}(N+1) A(D^{**}(-1)) (N+2 terms, anti-symmetric) */
30
             /* Establish the symmetric polynomial F1(D) and the anti-symmetric */
             /* polynomial F2(D)
             /* Only about half of the coefficients are evaluated since the
             /* polynomials are symmetric and will later be reduced in order by */
             /* division by polynomials with roots at +1 and -1
35
             F1[0] = 1.0;
             j = NPTS-1;
             for (i = 1; i < NC[0]; i++)
40
```

```
514
                    F1[i] = -PRCOF[i-1] - PRCOF[j];
                    j = j - 1;
                    }
 5
            F2[0] = 1.0;
            j = NPTS-1;
            for (i = 1; i < NC[1]; i++)
                    F2[i] = -PRCOF[i-1] + PRCOF[j];
10
                    j = j - 1;
                    }
            /* N even, F1(D) includes a factor 1+D,
15
                    F2(D) includes a factor 1-D
            /* N odd, F2(D) includes a factor 1-D**2
            /* Divide out these factors, leaving even order symmetric
            /* polynomials, M is the total number of terms and Nc is the number */
20
            /* of unique terms,
                                                Nc=(M+1)/2
            /* N
                      polynomial
                                        M
            /* even, G1(D) = F1(D)/(1+D) N+1
                                                       N/2+1
                   G2(D) = F2(D)/(1-D) N+1
                                                     N/2+1
            /* odd, Gl(D) = Fl(D)
                                                   (N+1)/2+1
                                                     (N+1)/2
25
                   G2(D) = F2(D)/(1-D^{**}2) N
            if (NPTS \% 2) != 0
                    for (i = 2; i < NC[1]; i++)
                             F2[i] = F2[i] + F2[i-2];
30
            elsc
                    for (i = 1; i < NC[0]; i++)
                             F1[i] = F1[i] - F1[i-1];
                             F2[i] = F2[i] + F2[i-1];
35
                             }
             /* To look for roots on the unit circle, G1(D) and G2(D) are
40
             /* evaluated for D=exp(ja). Since G1(D) and G2(D) are symmetric, */
```

```
515
            /* they can be expressed in terms of a series in cos(na) for D on */
            /* the unit circle. Since M is odd and D=exp(ja)
             /*
                     M-I
                                               (symmetric, f1(n) = f1(M-1-n)) */
                G1(D) = SUM f1(n) D
 5
                                                            */
                     n=0
                                     Mh-1
                    = \exp(j Mh a) [fl(Mh) + 2 SUM fl(n) \cos((Mh-n)a)]
                                                                              */
                                      n=0
                                                            */
                                                            */
                             Mh
10
                    = exp(j Mh a) SUM tl(n) cos(na),
                             n=0
             /* where Mh=(M-1)/2=Nc-1. The Nc=Mh+1 coefficients t1(n) are
             /* defined as
15
             /* t1(n) = f1(Nc-1), n=0,
                     = 2 fl(Nc-1-n), n=1,...,Nc-1.
             /* The next step is to identify cos(na) with the Chebyshev polynomial*/
             /* T(n,x). The Chebyshev polynomials satisfy T(n,\cos(x)) = \cos(nx). */
             /* Then omitting the exponential delay term which does not affect the*/
20
             /* positions of the roots on the unit circle, the series expansion in*/
             /* terms of Chebyshev polynomials is
             /*
                      Nc-1
                                                                    */
                 T1(x) = SUM t1(n) T(n,x)
25
                      n=0
             /* The domain of TI(x) is -1 < x < +1. For a given root of TI(x), */
             /* say x0, the corresponding position of the root of F1(D) on the */
             /* unit circle is exp(j arccos(x0)).
30
             /* Establish the coefficients of the series expansion in Chebyshev */
             /* polynomials
35
             T[0][0] = F1[NC[0]-1];
             j = NC[0]-2;
             for (i = 1; i < NC[0]; i++)
                      {
                      T[0][i] = 2.0*F1[j];
40
```

```
516
                    j = j - 1;
                    }
            T[1][0] = F2[NC[1]-1];
 5
            j = NC[1]-2;
            for (i = 1; i < NC[1]; i++)
                    T[1][i] = 2.0*F2[j];
                    j = j - 1;
10
                   }
            /*----*/
            /* Sample at equally spaced intervals between -1 and 1 to look for */
            /* sign changes. RESL is chosen small enough to avoid problems with */
15
            /* multiple roots in an interval. After detecting a sign change, */
            /* successive bisections and linear interpolation are used to find */
            /* roots corresponding to LSF frequencies. Since the roots of the */
            /* two polynomials interlace, the search alternates between the
                                                                       */
            /* polynomials T(.,1) and T(.,2).IP is either 1 or 2 depending on */
20
            /* which polynomial is being examined.
            NF = 0:
            IP = 0;
25
            XLOW = 1.0;
            YLOW = LPC_chebyshev (XLOW,T[IP],NC[IP]);
            while ((XLOW > -1.) \&\& (NF < NPTS))
30
                    XHIGH = XLOW;
                    YHIGH = YLOW;
                    if ((XHIGH-RESL) > -1.0)
                           XLOW = XHIGH-RESL;
35
                    else
                           XLOW = -1.0;
                    YLOW = LPC_chebyshev(XLOW,T[IP],NC[IP]);
40
                    if (YLOW*YHIGH <= 0.0)
```

```
517
                      NF = NF + 1;
                      /* Bisections of the interval containing a sign change */
5
                      /*____*/
                for (i = 0; i < NBIS; i++)
                             {
                       XMID = 0.5*(XLOW+XHIGH);
10
                       YMID = LPC\_chebyshev(XMID,T[IP],NC[IP]);
                       if (YLOW*YMID \le 0.0)
                                    {
                              YHIGH=YMID;
                              XHIGH=XMID;
15
                                    }
                       else
                                    {
                              YLOW=YMID;
                              XLOW=XMID;
20
                              }
                       /*____*/
                       /* Linear interpolation in the subinterval with a sign */
25
                       /* change (take care if YHIGH=YLOW=0)
                        if (YHIGH != YLOW)
                              XINT = XLOW - YLOW*(XHIGH-XLOW)/(YHIGH-YLOW);
30
                        else
                              XINT=0.5*(XLOW+XHIGH);
                        FRLSF[NF-1] = acos(XINT)/PI2;
 35
                        /*-----*/
                        /* Start the search for the roots of the next polynomial */
                        /* at the estimated location of the root just found
 40
```

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```
518
                             IP = 1 - IP;
                             XLOW = XINT;
                             YLOW = LPC_chebyshev (XLOW,T[IP],NC[IP]);
 5
                      Halt if NPTS frequencies have not been found
10
    #ifdef VERBOSE
            if (NF != NPTS)
                    nrerror("LPC_lpctolsf - Too few frequencies computed");
    #endif
15
            return;
20
25
   /* FUNCTION : LPC_chebyshev ().
   /* PURPOSE
                  : Evaluate a series expansion in Chebyshev
30 /*
                polynomials.
         The series expansion in Chebyshev polynomials is defined as */
               N-1
35 /*
          Y(x) = SUM c(i) T(i,x),
               0=i
         where Y(x) is the resulting value (Y(x) = CHEBPS(...)),
             N is the order of the expansion,
             c(i) is the coefficient for the i'th Chebyshev
40 /*
```

```
519
               polynomial
               (c(i) = COF(i+1)), and
            T(i,x) is the i'th order Chebyshev polynomial
                evaluated at x.
         The Chebyshev polynomials satisfy the recursion
5 /*
         T(i,x) = 2x T(i-1,x) - T(i-2,x),
         with the initial conditions T(0,x)=1 and T(1,x)=x. This */
         routine evaluates the expansion using a backward recursion */
         to obtain a numerically stable solution.
10 /*
   /* INPUT ARGUMENTS:
        _(FLOAT64 ) X: Input value.
        _(FLOAT64 []) COF: Array of coefficient values. COF[i] is */
                    the coefficient corresponding to the */
15 /*
                    i-1'th order Chebyshev polynomial.
        _(INT16 ) N: Order of the polynomial and number of */
                    coefficients.
20 /* OUTPUT ARGUMENTS:
   /* None.
   /* INPUT/OUTPUT ARGUMENTS:
   /* Nonc.
25 /*-----
   /* RETURN ARGUMENTS:
   /* _(FLOAT64 ) CHEBPS: Predictor coefficients.
30 FLOAT64 LPC chebyshev (FLOAT64 X, FLOAT64 COF[], INT16 N)
            INT16 i;
            FLOAT64 B1, B0, B2, CHEBPS;
35
            /* Consider the backward recursion b(i,x)=2xb(i+1,x)-b(i+2,x)+c(i), */
            /* with initial conditions b(N,x)=0 and b(N+1,x)=0.
            /* Then dropping the dependence on x, c(i)=b(i)-2xb(i+1)+b(i+2).
40
```

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```
520
              /*
                                                          */
                      N-1
                                                            */
                 Y(x) = SUM c(i) T(i)
                     i=0
  5
                  N-I
              /* = SUM [b(i)-2xb(i+1)+b(i+2)] T(i)
                                         N-I
             /* = b(0)T(0)+b(1)T(1)-2xb(1)T(0) + SUM b(i)[T(i)-2xT(i-1)+T(i-2)] */
 10
                                         i=2
                                                           */
             /* The term inside the sum is zero because of the recursive
             /* relationship satisfied by the Chebyshev polynomials. Then
             /* substituting the values T(0)=1 and T(1)=x, Y(x) is expressed in */
15
             /* terms of the difference between b(0) and b(2) (errors in b(0) and */
             /* b(2) tend to cancel).
             /*
             /* Y(x) = b(0)-xb(1) = [b(0)-b(2)+c(0)] / 2
20
             B2 = 0.0;
             B1 = 0.0;
             B0 = 0.0;
             for (i = N-1; i >= 0; i--)
25
                     {
                     B2 = B1;
                     B1 = B0;
                      B0 = (2.*X)*B1 - B2 + COF[i];
                     }
30
             CHEBPS = 0.5*(B0 - B2 + COF[0]);
35
             return (CHEBPS);
```

```
/* FUNCTION
                  : LPC_autocorrelation ().
 5 /* PURPOSE
                   : Calculate the correlation for a data sequence. */
         This subroutine calculates the auto-correlation for a
   /*
         given data vector. Rxx(i) gives the auto-correlation at */
         lag i, for i running from 0 to NTERM-1.
10 /*
               NX
          Rxx(i) = SUM X(j) * X(j-i)
               j=i
15 /*
         This algorithm requires
          (N+1)*(NX+N/2) multiplies and
   /*
   /*
          (N+1)*(NX+N/2) adds.
                                            */
20 /* INPUT ARGUMENTS:
        _ (FLOAT64 []) x:
                            Input data vector with NX elements. */
        _(INT16 ) L: Number of data points.
        _(INT16 ) ORDER_1: Order of the polynomial and number of */
                                              */
                     coefficients.
   /* OUTPUT ARGUMENTS:
        _(FLOAT64 []) r: Auto-correlation vector with ORDER_1 */
                                              */
                     elements.
30 /* INPUT/OUTPUT ARGUMENTS:
                                              */
       None.
   /* RETURN ARGUMENTS:
                                              */
   /* Nonc.
   void LPC_autocorrelation (FLOAT64 x[], INT16 L, FLOAT64 r[], INT16 ORDER_1)
```

	INT16 i;	
	/*	*/
	for (i = 0; i < ORDER_1; i++) $dot_dvector(x, x+i, r+i, 0, (INT16)(L-i-1));$	
	/*	·*/
	return;	
	/*	*/
	}	
; /*		*/
	======================================	
	*/	
	PURPOSE : This function convert the prediction coeff. is	n */
/*	tha classical LP filter coefficients. */	
/*	*/	
	*/	
	INPUT ARGUMENTS.	
	_ (FLOAT64 []) p: prediction coeffcients. */ _ (INT16) LPC_ORDER: order of the LPC.	*/
	_(NTTO) LFC_ORDER. GldG. G. till 21 C. */	
•	OUTPUT ARGUMENTS: */	
	T.D. City #/	
	(,*/	
) /*-	INPUT/OUTPUT ARGUMENTS:	*/
/*]	None. */	
/*] /*	_ None.	
/*] /* /*-	_ None.	

```
INT16 i;
 5
   #ifdef VERBOSE
          if (LPC_ORDER > MAX_LPC_ORDER)
                 nrerror("LPC order is too large in ptoa()!!!");
   #endif
10
           a[0] = 1.;
          for (i = 0; i < LPC\_ORDER; i++)
                 a[i+1] = -p[i];
15
           return;
20
25
         _____
   /* FUNCTION : LPC_lsftop ().
   /* PURPOSE : This function convert LSF's to predictor coeff. */
30 /*
   /* The line spectral frequencies are assumed to be frequencies
   /* corresponding to roots on the unit circle. Alternate roots on */
   /* the unit circle belong to two polynomials. These polynomials */
   /* are formed by polynomial multiplication of factors representing */
35 /* conjugate pairs of roots. Additional factors are used to give */
   /* a symmetric polynomial and an anti-symmetric polynomial. The */
     sum (divided by 2) of these polynomials gives the predictor */
      polynomial.
                                        */
        <del>-----</del>+/
```

```
524
  /* INPUT ARGUMENTS:
      _(FLOAT64 []) FRLSF: Array of NPTS line spectral
                                                            */
                    frequencies (in ascending order). */
  /*
                    Each line spectral frequency lies in */
  /*
                    the range 0 to 0.5.
5 /*
      _(INT16, ) NPTS: Number of coefficients (at most 50). */
  /*
  /* OUTPUT ARGUMENTS:
                                                          */
  /* _ (FLOAT64 []) PRCOF: predictor coefficients.
                                                           */
  /* INPUT/OUTPUT ARGUMENTS:
                                             */
   /* None.
   /* RETURN ARGUMENTS:
                                             */
15 /* None.
   void LPC_Isftop( FLOAT64 FRLSF[], FLOAT64 PRCOF[], INT16 NPTS)
           /*____*/
20
           INT16 NC, i, M, k;
           FLOAT64 F1[(MAX_LPC_ORDER+1)/2 +1], F2[(MAX_LPC_ORDER/2)+1];
            FLOAT64 A, x;
25
            F1[0] = 1.0;
            F2{0} = 1.0;
30
            /* Each line spectral frequency w contributes a second order
                                                                      */
            /* polynomial of the form Y(D)=1-2*cos(w)*D+D**2. These polynomials */
            /* are formed for each frequency and then multiplied together.
 35
            /* Alternate line spectral frequencies are used to form two
            /* polynomials with interlacing roots on the unit circle. These two */
            /* polynomials are again multiplied by 1+D and 1-D if NPTS is even */
            /* or by I and 1-D**2 if NPTS is odd. This gives the symmetric and */
            /* anti-symmetric polynomials that in turn are added to give the */
 40
```

```
525
          /* predictor coefficients.
          /*-----
  #ifdcf VERBOSE
          if (NPTS > MAX_LPC_ORDER)
                 nrerror ("LSFTOP - Too many coefficients!!!");
  #endif
              Form a symmetric F1(D) by multiplying together second order */
10
               polynomials corresponding to odd numbered LSF's
               NC = 0;
          for (i = 0; i < NPTS; i += 2)
15
                  x = cos(PI2*FRLSF[i]);
                  A = -2.0 * x;
                  LPC_convsm(F1, &NC, A);
20
             Form a symmetric F2(D) by multiplying together second order */
                 polynomials corresponding to even numbered LSF's
                  */
25
          NC = 0;
          for (i = 1; i < NPTS; i += 2)
                  x = cos(P12*FRLSF[i]);
30
                  A = -2.0 * x;
                  LPC_convsm(F2, &NC, A);
                 }
35
          /* Both F1(D) and F2(D) are symmetric, with leading coefficient
          /* equal to unity. Exclusive of the leading coefficient, the
          /* number of coefficients needed to specify F1(D) and F2(D) is:
             NPTS
                      FI(D)
                             F2(D)
          /* even NPTS/2
                             NPTS/2
                                                        */
40
```

```
526
            /* odd (NPTS+1)/2 (NPTS-1)/2
            if ( (NPTS \% 2) != 0)
5
                     /* NPTS odd -
                     /* F2(D) is multiplied by the factor (1-D**2)
10
                     M = (NPTS-1)/2;
                      for (i = M; i \ge 2; i--)
                      F2[i] = F2[i] - F2[i-2];
15
                      /* Form the predictor filter coefficients
                      /* Note that F1(D) is symmetric and F2(D) is now
                      /* anti-symmetric. Since only the first half of the
                      /* coefficients are available, symmetries are used to get */
                      /* the other half.
20
                      k = NPTS-1;
                      for (i=0; i < M; i++)
25
                               PRCOF[i] = -0.5*(F1[i+1] + F2[i+1]);
                               PRCOF[k] = -0.5*(F1[i+1] - F2[i+1]);
                               k = k - 1;
                       PRCOF[k] = -0.5*F1[k];
 30
                      }
             else
                       /* NPTS even
 35
                       /* F1(D) is multiplied by the factor (1+D)
                       /* F2(D) is multiplied by the factor (1-D)
              M = NPTS/2;
 40
```

```
527
           for (i=M; i >= 1; i--)
                    F1[i]=F1[i]+F1[i-1];
                    F2[i]=F2[i]-F2[i-1];
5
                    /* Form the predictor filter coefficients
                                                                           */
                    /* Note that F1(D) is symmetric and F2(D) is now
10
                    /* anti-symmetric.
                    k = NPTS - 1;
                    for (i = 0; i < M; i++)
15
                            PRCOF[i]=-0.5*(F1[i+1]+F2[i+1]);
                            PRCOF[k]=-0.5*(F1[i+1]-F2[i+1]);
                            k = k - 1;
                            }
                    }
20
             return;
25
30
    /* FUNCTION : LPC_adptive_interp ().
    /* PURPOSE : This function this function generates the LPC */
                 coefficients by interpolating in a NON linear */
35 /*
                                                  */
                 way the lsf.
    /* INPUT ARGUMENTS:
    /* _(FLOAT64 []) lsf_1: first set of lsf.
40 /* _ (FLOAT64 []) lsf_2: second set of lsf.
```

```
528
     _(FLOAT64 []) lsf_old: lsf from last frame.
      _(INT16 *) int_idx: codebook index.
  /* OUTPUT ARGUMENTS:
5 /* _(FLOAT64 **) pdcf: predictor coefficients.
  /* INPUT/OUTPUT ARGUMENTS:
  /* None.
10 /* RETURN ARGUMENTS:
   /* _ None.
   void LPC_adptive_interp (FLOAT64 lsf [], FLOAT64 lsf1 [], FLOAT64 lsf_old [],
                                                            FLOAT64 **pdcf, INT16 *int_idx)
15
             FLOAT64 ref_lsf[NP], int_lsf[NP], dis, mindis, delta, val1, val2;
             INT16 i, k;
 20
                              Middle Isf search
 25
                                  Weights
              int_ls[0] = (1.0-lsf1[0]) * (1.0 - lsf1[1] + lsf1[0]);
  30
               for (i = 1; i < NP-1; i++)
                       val1 = Isfl[i+1] - Isfl[i];
                       val2 = Isfl[i]
                                        - lsf1[i-1];
   35
                       delta = MIN(val1, val2);
                       int_lsf[i] = (1.0 - lsf1[i]) * (1.0 - delta);
                     . }
   40
```

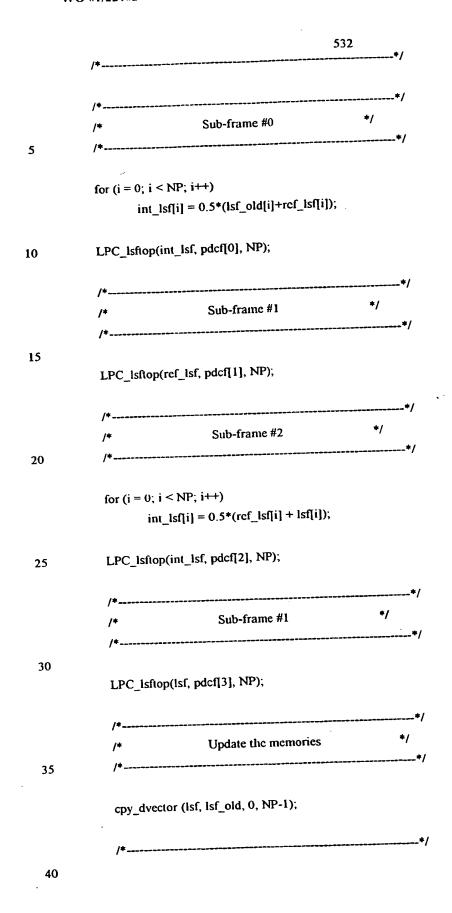
```
529
            delta = Isfl[NP-1] + Isfl[NP-2];
            int Isf[NP-1] = (1.0 - Isf1[NP-1]) * (1.0 - delta);
                                                             */
5
                                    Search
            mindis = DBL_MAX;
            for (k = 0; k < N_SF4; k++)
10
                     {
                     for (i = 0; i < NP; i++)
                              ref_lsf[i] = IntLSF_C[k]*lsf[i]+(1-IntLSF_C[k])*lsf_old[i];
                     dis = 0.0;
15
                      for (i = 0; i < NP; i++)
                              dis += fabs(ref_lsf[i]-lsfl[i]) * int_lsf[i];
                      if (dis < mindis)
20
                              mindis = dis;
                              *int_idx = k;
                     }
25
             k = *int_idx;
             for (i = 0; i < NP; i++)
                     ref_lsf[i] = IntLSF_C[k]*lsf[i] + (1-IntLSF_C[k])*lsf_old[i];
30
                        LSF to prediction coefficients convertion
35
                                  Sub-frame #0
              for (i = 0; i < NP; i++)
40
```

WO 01/22402

530 $int_ls[i] = 0.5*(lsf_old[i]+ref_ls[[i]);$ LPC_lsftop(int_lsf, pdcf[0], NP); 5 /* Sub-frame #1 LPC_lsftop(ref_lsf,pdcf[1], NP); 10 Sub-frame #2 for (i = 0; i < NP; i++)15 $int_lsf[i] = 0.5*(ref_lsf[i] + lsf[i]);$ LPC lsftop(int_lsf, pdcf[2], NP); 20 Sub-frame #1 LPC lsftop(lsf, pdcf[3], NP); 25 Update the memories cpy_dvector (lsf, lsf_old, 0, NP-1); 30 return; 35

531

/* FUNCTION : LPC_adptive_interp_dcc (). /*_____*/ /* PURPOSE : This function this function generates the LPC */ coefficients by interpolating in a NON linear */ way the lsf. /* INPUT ARGUMENTS: /* _(FLOAT64 []) lsf_1: first set of lsf. 10 /* _ (FLOAT64 []) lsf_old: lsf from last frame. _(INT16 *) int_idx: codebook index. /* OUTPUT ARGUMENTS: /* _ (FLOAT64 **) pdcf: predictor coefficients. 15 /*-----*/ /* INPUT/OUTPUT ARGUMENTS: /* None. /* RETURN ARGUMENTS: 20 /* None. void LPC adptive interp dec (FLOAT64 lsf [], FLOAT64 lsf_old [], FLOAT64 **pdcf, INT16 int_idx) 25 { FLOAT64 rcf_lsf[NP], int_lsf[NP]; INT16 i; 30 Middle LSF 35 for (i = 0; i < NP; i++)ref_lsf[i] = IntLSF_C[int_idx]*lsf[i] + (1-IntLSF_C[int_idx])*lsf_old[i]; LSF to prediction coefficients convertion 40



```
rcturn;
5
     _______
  /* FUNCTION : LPC_interpolate_lpc_4to2 ().
  /* PURPOSE : This function this function generates the LPC */
             coefficients by interpolating the lsf
             (frequency domain).
15 /* INPUT ARGUMENTS:
  /* _ (FLOAT64 []) lsf_1: first set of lsf.
      _ (FLOAT64 []) lsf_2: second set of lsf.
      _(FLOAT64 []) lsf_old: lsf from last frame.
       _(INT16 ) flag: switch between interpolation coeff. */
                   for quantized (1) or unquantized
20 /*
                    (0) LSF.
  /* OUTPUT ARGUMENTS:
   /* _(FLOAT64 **) pdcf: predictor coefficients.
   /* INPUT/OUTPUT ARGUMENTS:
  /* _ None.
                                          */
   /* RETURN ARGUMENTS:
30 /* None.
   void LPC interpolate lpc_4to2 (FLOAT64 lsf_1 [], FLOAT64 lsf_2 [],
                                             FLOAT64 Isf_old [], FLOAT64 **pdcf, INT16 flag)
35
          INT16 i;
          FLOAT64 C;
40
       FLOAT64 tmpmcm[NP];
```

```
*/
                                Sub-frame #1
5
            if (flag == 1)
                     {
                     C = 60.0 / 160.0;
                     for (i = 0; i < NP; i++)
                             tmpmem[i] = C*lsf_1[i] + (1-C)*lsf_old[i];
10
                     }
             else
                      C = 60.0 / 80.0;
                      for (i = 0; i < NP; i++)
15
                              tmpmem[i] = (1-C)*lsf_old[i] + C*lsf_2[i];
                      }
              LPC_lsftop (tmpmem, pdcf[0], NP);
 20
                                   Sub-frame #2
              if (flag == 1)
 25
                        C = (60.0 + 80.0) / 160.0;
                        for (i = 0; i < NP; i++)
                                tmpmem[i] = C*lsf_1[i] + (1-C)*lsf_old[i];
  30
              else
                        C = 60.0 / 80.0;
                         for (i = 0; i < NP; i++)
                                tmpmem[i] = C*lsf_1[i] + (1-C)*lsf_2[i];
  35
                        LPC_isstop (tmpmem, pdcf[1], NP);
                         }
                LPC_Isftop (tmpmcm, pdcf[1], NP);
```

	/* * /
	/* Update the 1sf for interpolation in the next coding frame */
5	/**/
	cpy_dvector (lsf_1, lsf_old, 0, NP-1);
	/**/
10	return;
	/**/
	} .
	/**/
	/*====================================
	/**/
	/* PURPOSE : This function this function generates the LPC */ /* coefficients by interpolating the lsf */
	/* (frequency domain). */ /**/
25	/* INPUT ARGUMENTS: */ /* _ (FLOAT64 []) isf_1: first set of isf. */
23	/* _(FLOAT64 []) lsf_2: second set of lsf. */
	/* _ (FLOAT64 []) Isf_old: Isf from last frame. */ /* _ (INT16) flag: switch between interpolation coeff. */
30	/* for quantized (1) or unquantized */ /* (0) LSF. */
	/**/ /* OUTPUT ARGUMENTS: */
	/* _(FLOAT64 **) pdcf: predictor coefficients. */
35	/* INPUT/OUTPUT ARGUMENTS : */
	/* _ None.
	/* RETURN ARGUMENTS : */ /* _ None. */
	/ -

```
void LPC_interpolate_lpc_4to3 (FLOAT64 lsf_1 [], FLOAT64 lsf_2 [],
                                                     FLOAT64 lsf_old [], FLOAT64 **pdcf, INT16 flag)
5
            INT16 i;
            FLOAT64 C;
        FLOAT64 tmpmem[NP];
10
                                Sub-frame #1
15
            if (flag == 1)
                     C = 46.5 / 160.0;
                     for (i = 0; i < NP; i++)
                             tmpmem[i] = C*lsf_l[i] + (1-C)*lsf_old[i];
20
            else
                     C = 46.5 / 80.0;
                     for (i = 0; i < NP; i++)
                             tmpmcm[i] = (1-C)*lsf_old[i] + C*lsf_2[i]; .
25
                     }
             LPC_lsftop (tmpmem, pdcf[0], NP);
30
                                 Sub-frame #2
             if (flag == 1)
 35
                      C = (46.5 + 53.0) / 160.0;
                      for (i = 0; i < NP; i++)
                              tmpmcm[i] = C*lsf_l[i] + (1-C)*lsf_old[i];
                      }
 40
             elsc
```

```
537
                     C = 20.0 / 80.0;
                     for (i = 0; i < NP; i++)
                             tmpmem[i] = C*lsf_1[i] + (1-C)*lsf_2[i];
5
                     LPC_Isftop (tmpmem, pdcf[1], NP);
            LPC_Isftop (tmpmem, pdcf[1], NP);
10
                                Sub-frame #3
15
            if (flag == 1)
                     C = (46.5 + 2.0*53.0) / 160.0;
                     for (i = 0; i < NP; i++)
                             tmpmem[i] = C*lsf_1[i] + (1-C)*lsf_old[i];
20
                     }
             else
                     C = (20.0 + 53.0) / 80.0;
                     for (i = 0; i < NP; i++)
                             tmpmem[i] = C*lsf_1[i] + (1-C)*lsf_2[i];
25
                     }
             LPC_lsftop (tmpmem, pdcf[2], NP);
30
                Update the 1sf for interpolation in the next coding frame */
             cpy_dvector (lsf_1, lsf_old, 0, NP-1);
35
             return;
40
```

```
}
  /* FUNCTION : LPC_convsm ().
  /* PURPOSE : This function convolve coefficients for
     symmetric polynomials.
10 /*-----
  /* INPUT ARGUMENTS:
  /* _(INT16 *) N: number of coefficients.
  /* _(FLOAT64 ) A: cosinusoidal coefficient.
15 /* OUTPUT ARGUMENTS:
  /* None.
  /* INPUT/OUTPUT ARGUMENTS :
  /* _(FLOAT64 []) X: input/outuput signal.
   /* RETURN ARGUMENTS:
                                    */
   /* _ None.
   25 void LPC_convsm (FLOAT64 X[], INT16 *N, FLOAT64 A)
          INT16 k, n;
 30
          n = (*N);
          if (n \ge 2)
 35
                X[n+1] = X[n-1];
                for (k = n+1; k \ge 3; k--)
                      X[k] = X[k] + A * X[k-1] + X[k-2];
 40
```

```
539
                    X[2] = X[2] + A*X[1] + 1.0;
                    X[1] = X[1] + A;
            else if (n == 1)
 5
                    X[2] = 2.0 + A*X[1];
                    X[1] = X[1] + A;
            else if (n == 0)
                   X[1] = A;
10
            n = n + 1;
            (*N) = n;
 15
            return;
20
    /* FUNCTION : LPC ImpulseResponse ().
    /* PURPOSE : This function calculates the LPC synthesis
               filter filter response including perceptual */
 30 /*
                weighting.
    /* INPUT ARGUMENTS:
           (FLOAT64 []) wpdcf_zero: precetual filter numerator */
    /*
                          coefficients.
           _(FLOAT64 []) wpdcf_pole: precetual filter denominator */
 35 /*
    /*
                          coefficients.
                                   quantized prediction coeff. */
           _(FLOAT64 []) pdcfq:
           _(INT16 ) l_sf: sub-frame size.
40 /* OUTPUT ARGUMENTS:
```

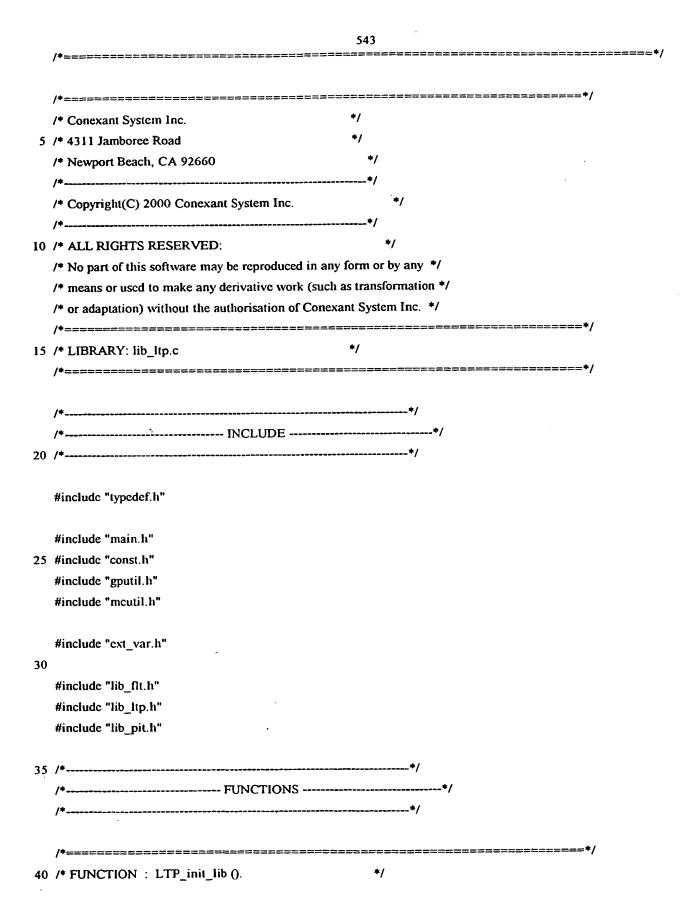
/*	_ (FLOAT64 []) hh: impulse response.	
	NPUT/OUTPUT ARGUMENTS:	*/
/* /*	_ None. */	*/
	ETURN ARGUMENTS :	*/
/ *	_ None. */	
/*		:======================================
void	I LPC_ImpulseResponse (FLOAT64 hh[], FLOAT6	64 wpdcf zero [],
voiu	FLOAT64 wpdcf_polc[], F	
	INT 16 l_sf)	
	{ ·	
	/*	* /
	FLOAT64 tmpmem[NP];	
	/*	***************************************
	/*	
	ini_dvector (hh, 0, 1_sf-1, 0.0);	
	LPC_ptoa (wpdcf_zero, hh, NP);	
	ini_dvector (tmpmem, 0, NP-1, 0.0);	
	FLT_allsyn (hh, l_sf, pdcfq, NP, hh, tmpme	:m);
	ini_dvcctor (tmpmem, 0, NP-1, 0.0);	
	FLT_allsyn (hh, i_sf, wpdcf_pole, NP, hh, to	mpmem);
	/*	*/
	,	·
	return;	
	/*	*/
	}	
/*		*/
/*-		

3N2UUUID: >WU__0133403¥1 | 1 |>

```
541
   /* Conexant System Inc.
 5 /* 4311 Jamboree Road
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
   15 /* PROTOTYPE FILE: lib_lpc.h
       -----*/
         LPC init lib
   void
                             (void);
                             (INT16, FLOAT64 [], FLOAT64 [], FLOAT64 [],
         LPC_analysis
   void
                                          FLOAT64 [], FLOAT64 [], FLOAT64 *, FLOAT64 [],
25
                                                 FLOAT64 Π, INT16, FLOAT64 *, INT16);
         LPC_autocorrelation (FLOAT64 [], INT16, FLOAT64 [], INT16);
   void
                              (FLOAT64 [], FLOAT64 [], FLOAT64 *, INT16);
         LPC_leroux_gueguen
   void
30
         LPC_refl2pred
                              (FLOAT64 [], FLOAT64 [], INT16);
   void
         LPC_lpctolsf
                              (FLOAT64 [], FLOAT64 [], INT16);
   void
                                    (FLOAT64, FLOAT64 [], INT16);
   FLOAT64
                LPC_chebyshev
35
                                           (FLOAT64 [], FLOAT64 [], INT16);
   void
         LPC_ptoa
                                           (FLOAT64 [], FLOAT64 [], INT16);
         LPC_lsftop
   void
                              (FLOAT64 [], FLOAT64 [], INT16);
         LPC pred2refl
   void
40
```

void	542 LPC_adptive_interp (FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 **, INT16 *);
void	LPC_adptive_interp_dec (FLOAT64 [], FLOAT64 [], FLOAT64 **, INT16);
5 void	LPC_interpolate_lpc_4to3 (FLOAT64 [], FLOAT64 [], FLOAT64 [],
void	FLOAT64 **, INT16); LPC_interpolate_lpc_4to2 (FLOAT64 [], FLOAT64 [], FLOAT64 [],
0	FLOAT64 **, INT16);
void	LPC_convsm (FLOAT64 [], INT16 *, FLOAT64);
15 void	LPC_ImpulseResponse (FLOAT64 [], FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16);
/*== 20 /*	======================================

3NSDOCID: <WQ___0122402A1_!_>



	/*	,_,	544 */		
	/* PURPOSE : This function initialise the gl	•			
	/* library LTP.	*/	*/		
5	/* ALGORITHM :	•	*/		
	/* INPUT ARGUMENTS :		*/		-
	/* _ None.	*/	*/		
10	/* OUTPUT ARGUMENTS :		*/		
	/* _ None.	*/	*/		
	/* INPUT/OUTPUT ARGUMENTS :		*/		
	/* _ None.	*/	*/	·	
15	/*/* RETURN ARGUMENTS :		*/		
	/* _ None.	*/			*/
20	void LTP_init_lib (void) { /*		*/		
25	ini_dvector(lag_f, 0, N_SF_MAX-I		*/		
20	min_pit = MIN_LAG; max_pit = HI_LAG;		,		
30	LTP_Init_SincWindows (); LTP_generate_PitLagTab();				
35	/* ; return;		*/		
	/*}		*/		
40)				

```
545
  /* FUNCTION : Hwind ().
5 /*----*/
  /* PURPOSE : This function returns a value of a Hamming window */
  /* centered at WinLim.
  /* ALGORITHM :
  /* INPUT ARGUMENTS:
       _ (INT16 ) WinLim: the center of the Hamming window. */
       _ (FLOAT64) t: the location of the value.
  /*_____
15 /* OUTPUT ARGUMENTS:
                                  */
  /*
           _ None.
  /* INPUT/OUTPUT ARGUMENTS:
      _ None.
  /* RETURN ARGUMENTS:
        _ (FLOAT64 ) val: hamming window value at t.
  25
  FLOAT64 Hwind (FLOAT64 t, INT16 WinLim)
30
        FLOAT64 val, x;
        val = 0.0;
35
        if (fabs(t) <= WinLim)
              x = cos(t*PI/WinLim);
              val = 0.54 + 0.46 * x;
40
```

,	/*		*/
1	rcturn val;		
	/*		*/
}			
,			
		+/	,
	·		
s====			
	TION: sincFwin2().	*/	
			**
	OSE: This function returns i		ng */
	windowed SINC function to		
	digital signal.	*/	
		*/	
	PRITHM : 	•	
	r arguments :	*/	
	(INT16) WinLim: the cent	·	, */
	(FLOAT64) t: the location		
_	_(,		
	PUT ARGUMENTS :	* /	
	_ None.	*/	
+		*/	
NPU	T/OUTPUT ARGUMENTS:	*	1
	_None.	*/	
ŧ			
* RETU	JRN ARGUMENTS:	*/	
*	_(FLOAT64) F: hammin		·/ .
*====		:======================================	
T 0 4 T	CA simple in 2011 O ATCA A D	TT16 WinI im)	
LOAT	64 sincFwin2(FLOAT64 t, I)	110 Whithii)	
	{ / *		*/
	j ·		
	FLOAT64 x, num, den, since	•	

```
x = t;
 5
            if (fabs(t) < 0.000001)
                    F = 1.0;
            else
                     if (WinLim \leq 6)
10
                            {
                             num = sin(x*PI*0.9);
                             den = PI*x*0.9;
                             sinc = num / den;
                             F = Hwind(t,WinLim) * sinc;
15
                     else
                             num = sin(x*PI*0.95);
                             den = PI*x*0.95;
20
                             sinc = num / den;
                             F = Hwind(t,WinLim) * sinc;
                    }
25
            return F;
30
         . }
                                                          */
   /* FUNCTION: sincFwin ().
    /* PURPOSE : This function returns returns a value of a Hamming */
             windowed SINC function to interpolate a discrete
40 /*
             digital signal.
```

```
548
  /* ALGORITHM:
  /* INPUT ARGUMENTS:
         _(INT16 ) WinLim: the center of the Hamming window. */
         _(FLOAT64) t: the location of the value.
   /* OUTPUT ARGUMENTS:
                _ None.
   /*
10 /*-----
   /* INPUT/OUTPUT ARGUMENTS:
   /*
                Nonc.
   /* RETURN ARGUMENTS:
          _ (FLOAT64 ) F: hamming window valuea at t.
15 /*
   FLOAT64 sincFwin(FLOAT64 t, INT16 WinLim)
20
           {
           FLOAT64 x, num, den, sinc;
           FLOAT64 F;
25
            x = t;
            if (fabs(t) < 0.000001)
                   F = 1.0;
 30
            else
                   num = sin(x*PI);
                   den = PI*x;
                   sinc = num / den;
 35
                   F = Hwind(t, WinLim) * sinc;
 40
```

	/*	***************************************	
	}	,	
	,		
/*		+/	
•			
/*====	*======================================		
/* FUNC	CTION: D2A_InterpWeight ().	*/	
/+		*/	
/* PURF	POSE: This function produces a weighting	ng function w[] for */	
/ *	the purpose of interpolating signal.	*/	
/*		+/	
/* ALG	ORITHM :	*/	
/*		+/	
/* INPU	T ARGUMENTS:	*/	
/*	_(INT16) WinLim: half Weighting for	unction length. */	
/*	_(FLOAT64) f: Interpolation fraction	onal value. */	
/*		*/	
/* OUT	PUT ARGUMENTS :	*/	
	_(FLOAT64 []) w : Weighting function	•	
/*		*/	
/* INPU	JT/OUTPUT ARGUMENTS:	*/	
	_ None. *	•	
		*/	
/* RETU	URN ARGUMENTS :	*/	
/*	_ None. *.	•	

```
550
          for (k = 0; k < 2*WinLim; k++)
                 w[k] = sincFwin((f+WinLim-1-k), WinLim);
                          Normalization
          C = 0;
          for (k = 0; k < 2*WinLim; k++)
10
                 C += w[k];
          C = 1.0/MAX(C, 0.1);
           for (k = 0; k < 2*WinLim; k++)
                 w[k] *= C;
15
           return;
20
   /* FUNCTION: D2A_InterpWeight2 ().
30 /* PURPOSE : This function produces a weighting function w[] for */
           the purpose of interpolating signal.
   /* ALGORITHM :
   /*______
35 /* INPUT ARGUMENTS:
          _(INT16 ) WinLim: half Weighting function length. */
   /*
          _(FLOAT64 ) f: Interpolation fractional value. */
   /* OUTPUT ARGUMENTS :
          (FLOAT64 []) w: Weighting function.
```

```
551
  /* INPUT/OUTPUT ARGUMENTS:
     _ None.
                                     */
5 /* RETURN ARGUMENTS:
     _ None.
  void D2A_InterpWeight2(FLOAT64 w[],FLOAT64 f,INT16 WinLim)
10
         INT16 k;
                  C;
         FLOAT64
15
                                             */
                       Sinc Function
20
         for (k = 0; k < 2*WinLim; k++)
               w[k] = sincFwin2((f+WinLim-1-k), WinLim);
                      Normalization
25
         C = 0;
         for (k = 0; k < 2*WinLim; k++)
               C += w[k];
30
         C = 1.0/MAX(C, 0.1);
         for (k = 0; k < 2*WinLim; k++)
               w[k] = C;
35
         rcturn;
40
         }
```

/±		
-	NCTION: D2A_interp ().	*/
/+		*/
/* PUF	RPOSE: This function produces a interpolation	on of digital */
/ *	signal to return the signal value between to	wo samples */
/ *	*/	
/*		*/
/* ALC	GORITHM:	*/
/*		*/
/* INP	PUT ARGUMENTS :	*/
/*	_ (FLOAT64 []) w: Weighting function.	*/
/*	_(FLOAT64 []) s : signal.	*/
/ *	_(INT16) WinLim : half Weighting fund	ction length. */
/*	_(INT16) Len : Length of signal.	*/
/ *	_ (FLOAT64) t: Time point of the sign	nal value */
/*	between two samples.	*/
/*		*/
/* OU	TPUT ARGUMENTS:	*/
/*	_ None. */	
/*		*/
/* INI	PUT/OUTPUT ARGUMENTS:	*/
; / *	_ None. */	
/*		*/
/* RE	TURN ARGUMENTS :	*/
	_ (FLOAT64) X : Interpolated signal s	
/ * ===		=======================================
)		
FLOA	AT64 D2A_interp (FLOAT64 w[],FLOAT64 s	[],INT16 Lcn,FLOAT64 t,INT16 WinLim)
	€	
	/*	*/
5	INT16 k, L1, L2, LowL, HighL;	
	FLOAT64 X;	
	•	
	/*	*/
0	LowL = (INT16)t-WinLim+1;	

```
553
       Ll
            = MAX(0,LowL);
       HighL = LowL+2*WinLim;
            = MIN(Lcn,HighL);
       X
            = 0.0;
5
       for (k = L1; k < L2; k++)
            X += s[k]*w[k-LowL];
10
       return X;
15
20 /* FUNCTION: LTP_lag_to_idx7b ().
  /*____*/
  /* PURPOSE: This function produces the extract the 7 bits index */
       corresponding to the pitch value
                             */
25 /*-----*/
  /* ALGORITHM:
  /*______
  /* INPUT ARGUMENTS:
       (FLOAT64) pit: pitch value.
  /* OUTPUT ARGUMENTS:
       None.
                               */
  /* INPUT/OUTPUT ARGUMENTS:
35 /*
       _ None.
                               */
  /* RETURN ARGUMENTS:
       _(INT16 ) idx : corresponding index.
  40
```

```
554
   INT16 LTP_lag_to_idx7b (FLOAT64 pit)
            FLOAT64
                            Min, D;
5
            INT16 i, idx;
10
          if (pit <53) {
           Min = fabs(PitLagTab7b[0] - pit);
           idx = 0;
           for (i = 1; i \le 60; i++) {
             D = fabs(PitLagTab7b[i] - pit);
15
             if (D \le Min) {
               Min = D;
               idx = i;
           }
20
         else {
           idx = (pit-PitLagTab7b[60]) + 0.5;
           idx= MIN(60 + idx, MAX_PIT_IDX_7b);
          }
25
             return (idx);
30
    /* FUNCTION : LTP_lag_to_idx8b ().
    /* PURPOSE : This function produces the extract the 8 bits index */ .
              corresponding to the pitch value
                                               */
 40 /* ALGORITHM:
```

```
555
   /* INPUT ARGUMENTS:
                                                    */
          (FLOAT64) pit: pitch value.
   /+_____
 5 /* OUTPUT ARGUMENTS:
                                             */
          _ None.
   /* INPUT/OUTPUT ARGUMENTS:
                                              */
          None.
10 /*---
   /* RETURN ARGUMENTS:
          _(INT16 ) idx : corresponding index.
15 INT16 LTP_lag_to_idx8b( FLOAT64 pit)
        {
        FLOAT64 Min, D;
        INT16 i, idx;
20
        if (pit <= 33.2) {
         idx = (INT16)((pit-MIN_LAG)/0.2 + 0.5);
         }
25
        clsc {
         if (pit < 44) {
            Min = fabs( PitLagTab8b[81] - pit);
            idx = 81;
            for ( i = 82; i \le 121;i++) {
               D = fabs(PitLagTab8b[i] - pit);
30
               if (D < Min) {
                 Min = D;
                 idx = i;
                }
35
              }
          }
          else {
             if (pit < 62.0) {
                Min = fabs(PitLagTab8b[121] - pit);
                idx = 121;
 40
```

```
556
                for ( i = 122; i \le 160; i++) {
                    D = fabs(PitLagTab8b[i] - pit);
                    if ( D < Min) {
                       Min = D;
                       idx = i;
5
              }
              clse {
                 if (pit < 92) {
10
                     Min = fabs(PitLagTab8b[160] - pit);
                     idx = 160;
                     for (i = 161; i \le 199; i++) {
                         D = fabs(PitLagTab8b[i] - pit);
                         if (D \le Min) {
15
                           Min = D;
                           idx = i;
20
                  }
                      idx = (INT16)((pit - 92) + 199 + 0.5);
25
          idx = MAX(idx, 0);
          idx = MIN(idx, MAX_PIT_IDX_8b);
          rcturn (idx);
 30
 35 /* FUNCTION: LTP_generatc_PitLagTab ().
     /* PURPOSE : This function generates the pitch lag table.
                                                         */
 40 /* ALGORITHM:
```

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```
557
   /* INPUT ARGUMENTS:
                                              */
                   _ None.
5 /* OUTPUT ARGUMENTS:
                   _ None.
   /* INPUT/OUTPUT ARGUMENTS:
                   None.
10 /*----
   /* RETURN ARGUMENTS:
                   _ None.
15 void LTP_generate_PitLagTab (void)
           INT16 i, k;
           FLOAT64
20
                          D;
                              8 bit table
                                                      */
25
        PitLagTab8b[0] = MIN_LAG;
        for (i = 1; i \le MAX_PIT_IDX_8b; i++) {
         if ( PitLagTab8b[i-1] < 33) {
            PitLagTab8b[i] = PitLagTab8b[i-1] + 0.2;
30
         }
         else {
            if (PitLagTab8b[i-1] < 91) {
             D = 0.2+0.8*(PitLagTab8b[i-1]-33.0)/(91.0-33.0);
             PitLagTab8b[i] = PitLagTab8b[i-1] + D;
35
             }
             k = (INT16)(PitLagTab8b[i-1] + 1.1);
             PitLagTab8b[i] = k;
             }
40
            }
```

```
}
                                 7 bit table
5
         PitLagTab7b[0] = MIN_LAG;
        for (i = 1; i \le MAX_PIT_IDX_7b; i++) {
            D = MIN(0.325*PitLagTab7b[i-1]/MIN_LAG, 1);
            if (D > 0.99) {
10
              k = PitLagTab7b[i-1] + D + 0.5;
              PitLagTab7b[i] = k;
              }
            elsc
              PitLagTab7b[i] = PitLagTab7b[i-1]+D;
15
            }
                                  5 bit table
20
            PitLagTab5b[0] = 0.0;
            for (i = 1; i \le MAX_PIT_IDX_5b; i++) {
             if (PitLagTab5b[i-1] \le 0.8)
               \mathbf{p} = 1.0/5.0;
 25
             else {
                if (PitLagTab5b[i-1] \leq 1.8)
                   D = 1.0/4.0;
                else
                   D = 1.0/3.0;
 30
              PitLagTab5b[i] = PitLagTab5b[i-1] + D;
 35
               return;
  40
```

559 } */ /* FUNCTION: LTP_Init_SincWindows (). /*____*/ 10 /* PURPOSE: This function initializes the table of Sinc interpolation windows. */ /* ALGORITHM : 15 /*-----/* INPUT ARGUMENTS: None. /* OUTPUT ARGUMENTS: 20 /* _ None. */ /* INPUT/OUTPUT ARGUMENTS: */ /* _ None. /*-----25 /* RETURN ARGUMENTS: **/*** _ None. .*/ void LTP_Init_SincWindows (void) 30 FLOAT64 f; INT16 i; 35 for (i=0;i<NUM_SINC_WIN;i++) { f=DELT F*i; D2A_InterpWcight2(SincWindows+i*LEN_SINC,f,SINC_LIMIT);

```
560
      }
      for (i=0;i<NUM_SINC_WIN;i++) {
          f=DELT_F*i;
          D2A_InterpWeight(SincWindows_E+i*LEN_SINC_E,f,SINC_LIMIT_E);
5
       for (i=0;i<NUM_SINC_WIN;i++) {
        f=DELT_F*i;
        D2A\_InterpWeight(SincWindows\_PP+i*LEN\_SINC\_PP,f,SINC\_LIMIT\_PP);
10
       }
15
           return;
20
 25 /* FUNCTION: LTP_excit_vari_pitch ().
    /* PURPOSE : This function calculates LTP excitation with
             interpolated pitch track.
  30 /* ALGORITHM:
     /*-----
     /* INPUT ARGUMENTS:
          _(FLOAT64 []) SincWindows : table of interpolation weights. */
          _(FLOAT64 []) PitFun: Pitch track.
                                  Excitation memory.
          _(FLOAT64 []) LTm:
  35 /*
          _(INT16 ) L_LTm: Length of the excitation memory.*/
          _(INT16 ) Len : Length of the LTP excitation. */
                                                        */
      /* OUTPUT ARGUMENTS:
   40 /* _(FLOAT64 []) E: LTP excitation.
```

```
561
   /* INPUT/OUTPUT ARGUMENTS:
                                            */
   /*
                None.
5 /* RETURN ARGUMENTS:
             _ None.
   void LTP_excit_vari_pitch (FLOAT64 *SincWindows,INT16 Len_Sinc,INT16 Sinc_Limit,
                FLOAT64 PitFun[],INT16 L_LTm,FLOAT64 LTm[],INT16 Len,
10
                FLOAT64 E[])
          {
           FLOAT64 t, f, *Weight;
15
           INT16 k, T, m;
20
           for (k = 0; k < Len; k++)
                  {
                   t = L_LTm + k - PitFun[k];
                   T = (INT16)t;
                   f = t-(FLOAT64)T;
                   m = (INT16)(f*(FLOAT64)DELT_F2+0.500001);
25
                   Weight = SincWindows + m*Len_Sinc;
                   E[k] = D2A_interp (Weight, LTm, (INT16)(L_LTm+Len), t,
                          Sinc_Limit);
                   LTm[L_LTm+k] = E[k];
30
                   }
35
            rcturn;
           }
```

```
562
  /* FUNCTION: LTP_excit_const_pitch ().
5 /*----*/
  /* PURPOSE : This function calculates LTP excitation with
          constant pitch track.
  /* ALGORITHM:
10 /*-----
   /* INPUT ARGUMENTS:
       _(FLOAT64 ) Pit_f: Pitch lag (fractional).
   /*
       _(FLOAT64 []) LTm: Excitation memory.
       _(INT16 )L_LTm: Length of the excitation memory.*/
   /*
       _(INT16 ) Len : Length of the LTP excitation. */
15 /*
        _(INT16 ) mem_flag : memory update flag.
   /* OUTPUT ARGUMENTS :
                            LTP excitation.
      _(FLOAT64 []) E:
   /* INPUT/OUTPUT ARGUMENTS:
                                         */
                None.
    /* RETURN ARGUMENTS:
 25 /*
    void LTP_excit_const_pitch (FLOAT64 Pit_f,INT16 L_LTm, FLOAT64 LTm[],INT16 Len,
                          FLOAT64 E[],INT16 mem_flag)
 30
                         t, f, *Weight;
            FLOAT64
            INT16 k, T, m;
  35
             t = L LTm - Pit_f;
             T = (INT16)t;
             f = t - (FLOAT64)T;
  40
```

```
563
          m = (INT16)(f*(FLOAT64)DELT_F2 + 0.500001);
          Weight = SincWindows E + m*LEN_SINC_E;
          for (k = 0; k < Len; k++)
5
                t = L_LTm + k - Pit_f;
                E[k] = D2A_interp (Weight, LTm, (INT16)(L_LTm+Len), t,
                SINC_LIMIT_E);
                if (mcm_flag == 1)
10
                       LTm[L_LTm+k] = E[k];
                }
15
          return;
20
   25 /* FUNCTION: LTP_FineIndex_search ().
   /* PURPOSE : This function does the fine search of the correlation.*/
   /* ALGORITHM:
30 /*-----
   /* INPUT ARGUMENTS:
       _ (FLOAT64 []) PitTab:
                            table of pitch values.
       _(INT16 ) PI:
                         sarch minimum value.
                                                 */
       _(INT16
                         sarch maximum value.
               ) P2:
       _(INT16 ) LowL : lower pitch value.
35 /*
       _ (INT16 ) L_R:
                          length of correlation.
   /* OUTPUT ARGUMENTS:
       _ (INT16 ) index : memory update flag.
```

```
564
  /* INPUT/OUTPUT ARGUMENTS:
     _(FLOAT64 []) R:
                           correlation.
  /* RETURN ARGUMENTS:
               None.
  void LTP_FineIndex_scarch (FLOAT64 PitTab[], INT16 P1,INT16 P2,INT16 LowL,
              INT16 L R,FLOAT64 R[],INT16 *index)
10
                       t, GG, f, MaxR=0, *Weight;
          FLOAT64
          INT16 i, T, m;
15
                   Search for the best precise pitch and index
           for (i = P1; i \le P2; i++)
20
                 t = PitTab[i] - LowL;
                 T = (INT16)t;
                 f = t - (FLOAT64)T;
                  m = (INT16)(f*(FLOAT64)DELT_F2 + 0.500001);
25
                  Weight = SincWindows + m*LEN_SINC;
                  GG = D2A_interp(Weight, R, L_R, t, SINC_LIMIT);
                  if (i == P1 \parallel GG > MaxR)
                        {
                         MaxR = GG;
30
                         (*index) = i;
                 }
           T = (INT16)(PitTab[*index] + 0.5 - LowL);
 35
           R[T] = MaxR;
 40
            return;
```

	/*}	
	,	
/*		*/
/ * ===	=======================================	
	NCTION: LTP_PP_pitch_ext().	*/
/* PUI	RPOSE: This function extracts the adaptive	
/*	excitation for pitch pre-processing (Mo	de 1) at the */
/*	Chedaer.	*/
•		
	GORITHM:	*/
•		
	PUT ARGUMENTS :	*/
-	_ (FLOAT64 []) PitFun: Pitch track.	*/ .5
	_ (FLOAT64 []) Tgs: target signal.	·
-	_ (FLOAT64 []) ext: cxcitation signal.	
	_ (FLOAT64 []) hh: impulse response	
	_ (INT16) i_sf: sub-frame number.	*/
	_ (INT16) l_sf: sub-frame size.	*/
	TPUT ARGUMENTS:	*/
	_ (INT16 *) lag: lag value.	*/
	_(FLOAT64 *) lag_f: fractional lag va	
	_ (FLOAT64 *) R_ltp: pitch correlatio	
	_ (FLOAT64 []) unfcod: unfiltered excit	
	_ (FLOAT64 []) fcod: filtered excitation	
•		
	PUT/OUTPUT ARGUMENTS:	*/
/*	_ 1 10110.	*/
•		*/
	TURN ARGUMENTS :	- /
/*	_ None.	*/
/ * ===		======================================
	ren po tal armotento o ma	NATEA TOO D. FLOATEA OF D.
void I	LTP_PP_pitch_ext(FLOAT64 PitFun[],FLC	JA 104 1gs [], FLUA 104 ext [],

566 FLOAT64 *fcod, INT16 *lag, FLOAT64 *lag_f, FLOAT64 *R_ltp, INT16 l_sf)

{ /*		*/
FLOAT6	4 numerator, denominator, eng_T	gs, tmp[L_SF];
FLOAT6	4 *buf;	
/* 		+/
buf = dve	ector (0, l_sf-1);	
-	LTP excitation : unfcod[]	*/
	LIP exchange in uncould	·
	ctor(ext + MAX_LAG, tmp, 0, L_SF-1); it_vari_pitch(SincWindows_E, LEN_SI MA	
cpy_dvec	ctor(tmp, ext+MAX_LAG, 0, L_SF-1);	
/*/*	Filter the pitch vector (LTP excitate	ion) */
/*/ /*	Filter the pitch vector (LTP excitat	ion) */ */
/* /* /* filterAZ /*/	Filter the pitch vector (LTP excitated) (hh, unfcod, fcod, buf, (INT16)(l_sf-1), Integer pitch	ion) */*/ l_sf);*/
/* filterAZ /* /* lag[i_sf]	Filter the pitch vector (LTP excitated) (hh, unfcod, fcod, buf, (INT16)(i_sf-1), Integer pitch	ion) */*/ l_sf);*/ */
/* filter AZ /* /* lag[i_sf] lag_f[i_ /*/*	Filter the pitch vector (LTP excitated) (hh, unfcod, fcod, buf, (INT16)(l_sf-1), Integer pitch = (INT16)(MIN(PitFun[l_sf/2]+0.5, 1)	ion) */*/ l_sf); */ */ */ max_pit));

```
567
          dot_dvector (Tgs, fcod, &numerator, 0, l_sf-1);
          dot dvector (fcod, fcod, &denominator, 0, I_sf-1);
          dot_dvector (Tgs, Tgs, &eng_Tgs, 0, l_sf-1);
          (*R_ltp) = MAX(numerator, 0.0) / sqrt(MAX(denominator*eng_Tgs, 0.1));
 5
          free_dvector (buf, 0, 1_sf-1);
10
          return;
15
                                                        */
20 /* FUNCTION: LTP_PP_pitch_ext_dccod ().
        /* PURPOSE : This function extracts the adaptive codebook
           excitation for pitch pre-processing (Mode 1) at the */
                                           */
           decoder.
   /* ALGORITHM:
   /+_____
   /* INPUT ARGUMENTS :
        _ (FLOAT64 []) PitFun: Pitch track.
                             excitation signal.
30 /*
        _ (FLOAT64 []) ext:
        _(INT16 ) i_sf:
   /*
                           sub-frame number.
        _(INT16 ) 1_sf:
                           sub-frame size.
   /* OUTPUT ARGUMENTS:
                                                */
       _(INT16 *) lag:
                           lag value.
        _(FLOAT64 *) lag_f:
                              fractional lag value.
        _(FLOAT64 []) unfcod: unfiltered excitation.
   /* INPUT/OUTPUT ARGUMENTS:
40 /*
                                           */
                None.
```

URN	ARGUMEI	NTS:		*/			
O.G.	_ None.		*/				
== = =			=======			=====	EBES====
			4 D2E	T OATCA o	an Deriki	c e	
TP_PI	pitch_ext	_decod (FLOAT6	4 Pitrun[],r \T64 *unfco				f. INT161
{		10071	irov umoo	-, · · ·	-6,		
-					*/		
FL	OAT64	tmp[L_SF];					
/*		LTP excitation					
/*-					· <i>,</i>		
сру	y dvector (e	ext+MAX_LAG,	tmp, 0, L_SI	F-1);			
LI	P_excit_va	ri_pitch(SincWin	dows_E, LE		, SINC_LIM	IIT_E, P	itFun,
LI	P_excit_va	ri_pitch(SincWin	dows_E, LE		, SINC_LIM	IIT_E, Pi	itFun, MAX_
	P_excit_va	ri_pitch(SincWin	dows_E, LE		, SINC_LIM	IIT_E, Pi	
eod);				n_sinc_e	, SINC_LIM	IIT_E, Pi	
cod);		ri_pitch(SincWin mp, ext+MAX_L		n_sinc_e	, SINC_LIM	IIT_E, Pi	
od); .cp	y_dvector(t	mp, ext+MAX_L	AG, 0, L_SF	N_SINC_E 1);		UT_E, P	
od); cp /*.	y_dvector(t	mp, ext+MAX_L	AG, 0, L_SF	N_SINC_E 1);		IIT_E, Pi	
cod); cp	y_dvector(t	mp, ext+MAX_L.	AG, 0, L_SF	N_SINC_E */	*/	IIT_E, Pi	
cod); cp	y_dvector(t	mp, ext+MAX_L	AG, 0, L_SF	N_SINC_E */	*/	IIT_E, Pi	
cod); cp. /*. /*.	y_dvector(t	mp, ext+MAX_L.	AG, 0, L_SF	N_SINC_E */	*/	IIT_E, Pi	
cod); cp /*. /* /*	y_dvector(t g[i_sf] = (II	mp, ext+MAX_L	AG, 0, L_SF	N_SINC_E */	*/	IIT_E, Pi	
fcod); cp: /*: /*: la; la;	y_dvector(t g[i_sf] = (II g_f[i_sf] =)	mp, ext+MAX_L. pitch NT 16)(PitFun[l_st/2];	AG, 0, L_SF 	N_SINC_E	*/	IIT_E, Pi	
(cod); cp: /*. /* la; la;	y_dvector(t g[i_sf] = (II g_f[i_sf] =)	mp, ext+MAX_L. pitch NT 16)(PitFun[l_st	AG, 0, L_SF 	N_SINC_E	*/	IIT_E, Pi	
fcod); cp: /*: /*: la; la;	y_dvector(t g[i_sf] = (II g_f[i_sf] =)	mp, ext+MAX_L. pitch NT 16)(PitFun[l_st/2];	AG, 0, L_SF 	N_SINC_E	*/	IIT_E, Pi	
fcod); cp. /* /* la; la	y_dvector(t g[i_sf] = (II g_f[i_sf] =)	mp, ext+MAX_L. pitch NT 16)(PitFun[l_st/2];	AG, 0, L_SF 	N_SINC_E	*/	IIT_E, Pi	
fcod); cp /* /* la la /*	y_dvector(t g[i_sf] = (ll g_f[i_sf] =)	mp, ext+MAX_L/ pitch NT 16)(PitFun[1_st/2];	AG, 0, L_SF	N_SINC_E	*/	IIT_E, Pi	
fcod); cp /* /* la la /* re	y_dvector(t g[i_sf] = (ll g_f[i_sf] =)	mp, ext+MAX_L. pitch NT 16)(PitFun[l_st/2];	AG, 0, L_SF	N_SINC_E	*/	IIT_E, Pi	
cp. /*. /* la; la; /*	y_dvector(t g[i_sf] = (ll g_f[i_sf] =)	mp, ext+MAX_L/ pitch NT 16)(PitFun[1_st/2];	AG, 0, L_SF	N_SINC_E	*/	IIT_E, Pi	
fcod); cp /* /* la la /* re	y_dvector(t g[i_sf] = (ll g_f[i_sf] = l	mp, ext+MAX_L/ pitch NT 16)(PitFun[1_st/2];	AG, 0, L_SF	N_SINC_E *-1);	*/	IIT_E, Pi	

```
569
   /* FUNCTION: LTP_close_7b_pitch ().
   /*-----*/
   /* PURPOSE: This function performs the traditional search of the */
   /*
            the adaptive codebook in Mode 0.
 5 /*-----*/
   /* ALGORITHM:
   /*------
   /* INPUT ARGUMENTS:
   /* _(FLOAT64 []) Tgs:
                             target signal.
10 /* _ (FLOAT64 []) ext:
                             excitation signal.
       _ (FLOAT64 []) hh:
                             impulse response.
   /* _(INT16 ) i_sf:
                           sub-frame number.
      _(INT16 ) l_sf:
                           sub-frame size.
      _(INT16 ) frm_class_pp : pitch pre-proc. classification. */
   /* OUTPUT ARGUMENTS:
        _(INT16 *) lag:
                           lag value.
   /*
        _(FLOAT64 *) lag_f:
                              fractional lag value.
        _(FLOAT64 *) pgain:
                              pitch gain.
20 /*
        _(FLOAT64 *) R_ltp:
                              LTP criterion.
   /*
        _ (FLOAT64 []) unfcod:
                              unfiltered excitation.
   /*
        _(FLOAT64 []) fcod:
                             filtered excitation.
        _(INT16 *) pit_idx: pitch index.
25 /* INPUT/OUTPUT ARGUMENTS:
                                          */
                None.
   /* RETURN ARGUMENTS:
                None.
                                          */
   void LTP_close_7b_pitch(FLOAT64 Tgs [], FLOAT64 ext [], FLOAT64 hh [],
                               INT16 i_sf, FLOAT64 unfcod [],
                        FLOAT64 fcod [], INT16 *lag,FLOAT64 *lag_f,
35
                        FLOAT64 *pgain, FLOAT64 *R_ltp, INT16 1_sf,
                        INT16 frm_class_pp, INT16 pit_idx [])
40
                        pit_f, numerator, denominator, MaxR, eng_Tgs;
          FLOAT64
```

```
570
                            ftmp[L_SF];
            FLOAT64
                            i, k, IP1, IP2, L_Rp, PITCHi;
            INT16
            FLOAT64
                            *buf;
                            Rp[20] = \{0.0\};
            FLOAT64
 5
                            Allocate temporary memory
            buf = dvector (0, l_sf-1);
10
                                                            */
                              Searching range
15
            if (lag[i\_sf] < MIN\_LAG2)
                    lag[i_sf] = 2;
            IP1 = MAX(MIN\_LAG2, lag[i\_sf] - 3);
            IP2 = MIN(HI\_LAG2, lag[i\_sf] + 3);
            if ((frm_class_pp > 0) && (i_sf == N_SF2-1) && (lag[i_sf] > l_sf*0.75))
20
                    IP1 = MAX(MIN\_LAG2, lag[i\_sf]-1);
                    IP2 = MIN(Hl\_LAG2, lag[i\_sf]+1);
                    }
25
                         Integer correlation for integer lag
30
             L_Rp = IP2 - IP1 + 1;
    #ifdef VERBOSE
             if (L_Rp \ge 20)
                     nrcrror("L_Rp>=20 !!");
 35
    #endif
 40
```

NSDGGID: «WO___0122402A1_I_>

```
571
            for (i = IP1; i \le IP2; i++)
                    if (i == IP1)
                                     filterAZ (hh, ext+MAX_LAG-i, ftmp, buf,
 5
            (INT16)(i_sf-1), i_sf);
                    clse
                            {
                             for (k = l_sf-1; k > 0; k-)
                                     ftmp[k] = ftmp[k-1] + ext[MAX_LAG-i]*hh[k];
10
                             fimp[0] = ext[MAX_LAG-i]*hh[0];
                            }
                    dot_dvcctor (Tgs, ftmp, &Rp[i-IP1], 0, 1_sf-1);
                     dot dvector(ftmp, ftmp, &denominator, 0, 1_sf-1);
15
                    Rp[i-IP1] /= sqrt(MAX(denominator, 0.1));
                    }
20
                           Searching for integer pitch
            MaxR = Rp[0];
            PITCHi = IP1;
25
            for (i = IP1+1; i \le IP2; i++)
                    {
                     if (Rp[i-IP1] > MaxR)
30
                             MaxR = Rp[i-IP1];
                             PITCHi = i;
                             }
         if ((MaxR < 0) && (i_sf==N_SF2-1) && (frm_class_pp > 0))
                    PITCHi = lag[i_sf];
35
                              Fine index scarching
40
```

```
572
            pit_idx[i_sf] = PITCHi - MIN_LAG2;
            pit_f = PITCHi;
            lag[i_sf] = PITCHi;
            lag_f[i_sf] = PITCHi;
5
                            LTP excitation : unfcod[]
             cpy_dvector (ext+MAX_LAG, ftmp, 0, 1_sf-1);
10
             LTP_excit_const_pitch (pit_f, MAX_LAG, ext, l_sf, unfcod, 1);
             cpy_dvector (ftmp, ext+MAX_LAG, 0, 1_sf-1);
                         Filter the pitch vector (LTP excitation)
15
             ini_dvector (buf, 0, 1_sf-1, 0.0);
             filterAZ (hh, unfcod, fcod, buf, (INT16)(l_sf-1), l_sf);
20
                           Compute the pitch gain (LTP gain)
                                                                         0, 1_sf-1);
                                               &numerator,
                                       fcod,
              dot_dvector (Tgs,
 25
              dot_dvector (fcod, fcod, &denominator, 0, 1_sf-1);
                                                                         0, 1_sf-1);
                                               &eng_Tgs,
              dot_dvector (Tgs,
                                       Tgs,
              (*pgain) = numerator / MAX(denominator, 0.1);
 30
                         Limit the pitch gain -> 0 <= pgain <= 1.2
               if ((*pgain) < 0.0)
 35
                       (*pgain) = 0.0;
               if ((*pgain) > 1.2)
                       (*pgain) = 1.2;
               (*R_ltp) = MAX(numerator, 0.0) / sqrt(MAX(denominator*eng_Tgs, 0.1));
  40
```

```
Deallocate temporary memory
 5
           free_dvector (buf, 0, 1_sf-1);
10
           return;
   /* FUNCTION: LTP_close_8_5k_pitch ().
                                                         */
20 /*----*/
   /* PURPOSE : This function performs the traditional search of the */
            the adaptive codebook for SMV 8.5 kbps.
   /* ALGORITHM:
   /* INPUT ARGUMENTS:
   /* _ (FLOAT64 []) Tgs:
                             target signal.
      _ (FLOAT64 []) ext:
                              excitation signal.
      _ (FLOAT64 []) hh:
                              impulse response.
      _(INT16 ) i_sf:
                            sub-frame number.
       _(INT16 ) l_sf:
                            sub-frame size.
       _ (INT16 ) frm_class_pp : pitch pre-proc. classification. */
                                                       */
   /* OUTPUT ARGUMENTS:
35 /*
        _(INT16 *) lag:
                             lag value.
        _(FLOAT64 *) lag_f:
                                fractional lag value.
        _ (FLOAT64 *) pgain:
                                pitch gain.
        _(FLOAT64 *) R_ltp:
                                LTP criterion.
        _ (FLOAT64 []) unfcod:
                                unfiltered excitation.
        _ (FLOAT64 []) fcod:
                               filtered excitation.
40 /*
```

```
574
   /* _ (INT16 *) pit_idx : pitch index.
                                                            */
   /* INPUT/OUTPUT ARGUMENTS:
                                               */
                 _ None.
   /* RETURN ARGUMENTS:
                                               */
                  None.
10 void LTP_closc_8_5k_pitch (FLOAT64 Tgs [], FLOAT64 ext [], FLOAT64 hh [],
                                  INT16 i_sf, FLOAT64 unfcod [],
                          FLOAT64 fcod [], INT16 *lag,FLOAT64 *lag_f,
                           FLOAT64 *pgain, FLOAT64 *R_ltp, INT16 l_sf,
                           INT16 frm_class_pp, INT16 pit_idx [])
15
                           pit_f, numerator, denominator, MaxR, eng_Tgs;
            FLOAT64
            FLOAT64
                           ftmp[L_SF], DifPitTab[33];
                           i, k, PP1 = 0, PP2 = 0, IP1, IP2, LowL, HighL, L_Rp, PITCHi;
            INT16
20
            FLOAT64
                           *buf;
                           Rp[2*SINC_LIMIT+40] = \{0.0\};
            FLOAT64
                          Allocate temporary memory
25
            buf = dvector (0, l_sf-1);
30
                             Searching range
           IP1 = MAX(MIN\_LAG, lag[i\_sf] - 3);
            IP2 = MIN(HI\_LAG2, lag[i\_sf] + 3);
35
            if ((i_sf == 1) || (i_sf == N_sF4-1))
                    for (i = 0; i < 16; i++)
                           DifPitTab[16+i] = MIN (HI_LAG2,
40
```

```
575
                                                                                lag_f[i_sf-1] + PitLagTab5b[i]);
                    for (i = 1; i \le 16; i++)
                            DiPitTab[16-i] = MAX(MIN_LAG,
                                                                                lag_f[i_sf-1] - PitLagTab5b[i]);
5
                    PP1 = 0;
                     PP2 = 31;
                     if ((frm_class_pp > 0) && (i_sf == N_SF4-1))
                             IP1 = MAX(MIN\_LAG, lag[i\_sf]-1);
10
                             IP2 = MIN(HI\_LAG2, lag[i\_sf]+1);
                             if (DifPitTab[0] >= IP2)
                                     PP2 = 0;
                             if (DifPitTab[31] \leq IP1)
                                     PP1 = 31;
15
                             for (i = 0; i < 32; i++)
                                      if (DifPitTab[i] >= IP1)
                                               PP1 = i;
20
                                               break;
                                              }
                                      }
                              for (i = 31; i \ge 0; i--)
25
                                      if (DifPitTab[i] <= IP2)
                                               PP2 = i;
                                               break;
30
                                               }
                                      }
                      IP1 = DifPitTab[PP1] + 0.5;
                      IP2 = DifPitTab[PP2]+0.5;
35
                     }
                          Integer correlation for integer lag
40
```

```
LowL = IP1 - SINC_LIMIT - 1;
            HighL = IP2 + SINC_LIMIT + 1;
            L_Rp = HighL - LowL + 1;
 5
   #ifdef VERBOSE
            if (L_{Rp} \ge 2*SINC_{LIMIT} + 40)
                    nrcrror("L_Rp>=2*SINC_LIMIT+24 !! \n");
10
   #endif
            for (i = LowL; i \le HighL; i++)
15
                    {
                    if (i == LowL)
                                    filterAZ (hh, ext+MAX_LAG-i, ftmp, buf,
            (INT16)(l_sf-1), l_sf);
20
                    else
                             for (k = 1_sf-1; k > 0; k-)
                                     ftmp[k] = ftmp[k-1] + ext[MAX_LAG-i]*hh[k];
                             ftmp[0] = ext[MAX_LAG-i]*hh[0];
25
                             }
                     dot_dvector (Tgs, ftmp, &Rp[i-LowL], 0, 1_sf-1);
                     dot_dvector(ftmp, ftmp, &denominator, 0, 1_sf-1);
                     Rp[i-LowL] /= sqrt(MAX(denominator, 0.1));
30
                     }
                            Searching for integer pitch
 35
             MaxR = Rp[IP1 - LowL];
             PITCHi = IP1;
             for (i = IP1+1; i \le IP2; i++)
 40
```

```
577
                     if (Rp[i-LowL] > MaxR)
                             MaxR = Rp[i-LowL];
                             PITCHi = i;
 5
                    }
            if ((MaxR < 0) && (frm_class_pp > 0) && (i_sf == N_SF4-1))
10
                    PITCHi = lag[i_sf];
                              Fine index searching
15
            if ((i_sf == 0) || (i_sf == N_SF4/2))
                     PP1 = LTP_lag_to_idx8b(MAX(PITCHi - 0.9, MIN_LAG));
                     PP2 = LTP_lag_to_idx8b(MIN(PITCHi + 0.9, HI_LAG2));
20
                     LTP_FineIndex_search (PitLagTab8b, PP1, PP2, LowL, L_Rp, Rp,
                                                                       pit_idx+i_sf);
                     pit_f = PitLagTab8b[pit_idx[i_sf]];
                     lag[i_sf] = (INT16)(pit_f + 0.5);
25
                     lag_f[i_sf] = pit_f;
                     }
             else
                     if (DifPitTab[PP1] \geq (PITCHi+0.9))
30
                             PP2 = PP1;
                     if (DifPitTab[PP2] \leftarrow (PITCHi-0.9))
                             PP1 = PP2;
                     for (i = PP1; i \le PP2; i++)
35
                              if (DifPitTab[i] \geq (PITCHi-0.9))
                                      PPl = i;
                                      break;
40
                                      }
```

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```
578
                             }
                    for (i = PP2; i >= PP1; i--)
                             if (DifPitTab[i] \leq= (PITCHi + 0.9))
5
                                      PP2 = i;
                                      break;
                                      }
                              }
10
                      PP1 = MIN(PP1, PP2);
                      LTP_FineIndex_search (DifPitTab, PP1, PP2, LowL, L_Rp, Rp,
                                                                         pit_idx+i_sf);
15
                      pit_f = DifPitTab[pit_idx[i_sf]];
                      lag[i_sf] = (INT16)(pit_f + 0.5);
                       lag_{i_sf} = pit_f;
                      }.
 20
                                                                    */
                              LTP excitation : unfcod[]
               cpy_dvector (cxt+MAX_LAG, ftmp, 0, 1_sf-1);
  25
               LTP_excit_const_pitch (pit_f, MAX_LAG, ext, l_sf, unfcod, 1);
               cpy_dvector (ftmp, ext+MAX_LAG, 0, 1_sf-1);
                           Filter the pitch vector (LTP excitation)
  30
                ini_dvector (buf, 0, 1_sf-1, 0.0);
                filterAZ (hh, unfcod, fcod, buf, (INT16)(l_sf-1), l_sf);
   35
                              Compute the pitch gain (LTP gain)
                                                                             0, 1_s(-1);
                                                   &numerator,
                                          fcod,
                 dot_dvector (Tgs,
    40
```

```
579
         dot_dvector (fcod, fcod, &denominator, 0, 1_sf-1);
                                                       0, l_sf-1);
                                   &eng_Tgs,
         dot_dvector (Tgs,
                             Tgs,
         (*pgain) = numerator / MAX(denominator, 0.1);
5
                  Limit the pitch gain -> 0 <= pgain <= 1.2
         if ((*pgain) < 0.0)
10
                (*pgain) = 0.0;
         if ((*pgain) > 1.2)
                (*pgain) = 1.2;
         (*R_ltp) = MAX(numerator, 0.0) / sqrt(MAX(denominator*eng_Tgs, 0.1));
15
                     Dellocate temporary memory
20
         free_dvector (buf, 0, l_sf-1);
25
          rcturn;
30
  */
   /* FUNCTION : LTP_pitch_dccodtemp ().
35 /*----*/
  /* PURPOSE : This function perfroms a temporary decoding of the */
        LTP information codebook contribution.
  /* ALGORITHM:
```

```
580
  /* INPUT ARGUMENTS:
                             excitation signal.
      (FLOAT64 []) ext:
       _(INT16 ) i_sf: sub-frame number.
       _(INT16 ) 1_sf:
                           sub-frame size.
                             fractional lag value.
       _(FLOAT64 ) pit_f:
5 /*
  /* OUTPUT ARGUMENTS:
  /* _(FLOAT64 []) unfcod: unfiltered LTP excitation.
10 /* INPUT/OUTPUT ARGUMENTS:
                                            */
                _ None.
   /* RETURN ARGUMENTS:
                None.
   void LTP_pitch_decodtemp (FLOAT64 ext [], INT16 i_sf, FLOAT64 unfcod [],
                                        FLOAT64 pit_f, INT16 l_sf)
           {
20
           FLOAT64 LTm[MAX_LAG+L_SF];
 25
            cpy_dvector (ext, LTm, 0, MAX_LAG-1);
            LTP_excit_const_pitch (pit_f, MAX_LAG, LTm, l_sf, unfcod, 1);
  30
             return;
  35.
  40 /* FUNCTION: LTP_7b_pitch_decod().
                                                          */
```

```
581
   /* PURPOSE : This function decodes the traditional adaptive
          codebook contribution.
   /*______
 5 /* ALGORITHM:
   /*-----
   /* INPUT ARGUMENTS:
      _(INT16 ) bfi : bad frame indicator.
       _(FLOAT64 []) ext: excitation signal.
       _(INT16 ) i_sf: sub-frame number.
10 /*
       _(INT16 ) l_sf: sub-frame size.
       _(lNT16 *) pit_idx : pitch index.
   /* OUTPUT ARGUMENTS:
                                              */
15 /* _(INT16 *) lag : lag value.
       _(FLOAT64 *) lag_f: fractional lag value.
       _ (FLOAT64 []) unfcod: unfiltered excitation.
   /* INPUT/OUTPUT ARGUMENTS:
20 /*
                                         */
   /* RETURN ARGUMENTS:
               None.
                                         */
25
   void LTP_7b_pitch_decod (INT16 bfi, FLOAT64 ext [], INT16 i_sf,
                                      FLOAT64 unfcod [], INT16 lag [], FLOAT64 lag_f [],
                       INT16 l_sf, INT16 pit_idx [])
30
          FLOAT64
                      LTm[MAX_LAG+L_SF], pit_f;
35
                                                  */
                           Decode lag
          if (bfi == 0)
40
                 pit_f = pit_idx[i_sf] + MIN_LAG2;
```

```
582
                   lag[i_sf] = (INT16)(pit_f+0.5);
                   lag_f[i_sf]=pit_f;
           elsc
5
                   if (i_sf == 0)
                           lag[i_sf] = lag[1];
                   clse
                           lag[i_sf] = MIN(lag[i_sf-1], HI_LAG2);
10
                   lag_{i\_sf} = (FLOAT64) lag[i\_sf];
                   pit_f = (FLOAT64) lag[i_sf];
15
                           LTP excitation : unfcod[]
            cpy dvector (ext, LTm, 0, MAX_LAG-1);
            LTP excit const_pitch (pit_f, MAX_LAG, LTm, l_sf, unfcod, 1);
20
            rcturn;
25
30
    /* FUNCTION: LTP_8_5k_pitch_decod ().
    /* PURPOSE: This function decodes the traditional adaptive
             codebook contribution in Mode 0.
                                                        */
35 /*
    /* ALGORITHM:
    /*_____
    /* INPUT ARGUMENTS:
40 /* _ (INT16 ) bfi : bad frame indicator.
```

```
_(FLOAT64 []) ext: excitation signal.
        _(INT16 ) i_sf: sub-frame number.
        (INT16 ) l_sf:
                             sub-frame size.
        (INT16 *) pit_idx : pitch index.
   /* OUTPUT ARGUMENTS:
       _(INT16 *) lag:
                             lag value.
        _(FLOAT64 *) lag_f: fractional lag value.
        _(FLOAT64 []) unfcod: unfiltered excitation.
10 /*----
   /* INPUT/OUTPUT ARGUMENTS:
                 _ Nonc.
   /* RETURN ARGUMENTS:
15 /*
                 None.
   void LTP_8_5k_pitch_dccod (INT16 bfi, FLOAT64 ext [], INT16 i_sf,
                                          FLOAT64 unfcod [], INT16 lag [], FLOAT64 lag_f [],
                          INT16 l sf, INT16 pit_idx [])
20
          FLOAT64 LTm[MAX_LAG+L_SF], pit_f, DisPitTab[33];
           INT16 i;
25
                              Decode lag
30
           if (bfi == 0)
                   if ((i_sf == 1) || (i_sf == N_SF4-1))
                           for (i = 0; i < 16; i++)
35
                                  DifPitTab [16+i] = MIN (HI LAG2,
                                                                                 lag_f[i_sf-
   1]+PitLagTab5b[i]);
                           for (i = 1; i \le 16; i++)
40
```

584 DisPitTab [16-i] = MAX(MIN_LAG,

lag_f[i_sf-1]-

```
PitLagTab5b[i]);
5
                              pit_f = DifPitTab[pit_idx[i_sf]];
                             }
                     else
                             pit_f = PitLagTab8b[pit_idx[i_sf]];
10
                     lag[i_sf] = (INT16)(pit_f+0.5);
                     lag_f[i_sf] = pit_f;
                     }
             clsc
15
                     if (i_sf == 0)
                              lag[i_sf] = lag[3];
                      clse
                              lag[i_sf] = MIN(lag[i_sf-1], HI_LAG2);
20
                      lag_{i\_sf} = (FLOAT64) lag[i\_sf];
                      pit_f = (FLOAT64) lag[i_sf];
25
                              LTP excitation : unfcod[]
              cpy_dvector (ext, LTm, 0, MAX_LAG-1);
 30
              LTP_excit_const_pitch (pit_f, MAX_LAG, LTm, l_sf, unfcod, 1);
 35
              return;
```

```
585
  /* FUNCTION :LTP_adap_cbk_correction ().
5 /*----*/
  /* PURPOSE : This function extrapolated the adaptive codebook
          excitation when a frame erasure occurs.
   /* ALGORITHM:
10 /*-----
   /* INPUT ARGUMENTS:
   /* _ (INT16 ) fix_rate_m: previous frame bit-rate.
   /* _ (INT16 ) bfi:
                      frame # n Bad Frame Indicator.
   /* _(INT16 ) past_bfi: frame # n-1 Bad Frame Indicator.
15 /* _ (INT16 ) ppast_bfi: frame # n-2 Bad Frame Indicator.
   /* _ (FLOAT64 **) qua_gainQ: adaptive and fixed codebook gains.
   /* _(INT16 ) SVS_deci_m: framc # n-1 mode indicator.
   /* (FLOAT64 []) lag f: decoded pitch lags
   /* (FLOAT64 ) temp_lagf: extrapolated fractional pitch lags */
20 /*-----
   /* OUTPUT ARGUMENTS:
   /* _ (INT16 *) update :
                            update flag.
   /* _(INT16 []) lag:
                          extrapolated lag values.
   /* _ (FLOAT64 **) gainQ: adaptive and fixed codebook gains.
                             extrapolated fract. lag values. */
25 /* _ (FLOAT64 []) lag_f:
   /* _(FLOAT64 *) ForPitch_decTEMP: interpolated pitch track.
                                                          */
   /* _ (FLOAT64 []) ext_dec :
                              updated LTP memory.
   /* _ (FLOAT64 **) unfcod_dec : extrapolated LTP excitation.
30 /* INPUT/OUTPUT ARGUMENTS:
   /* _ (FLOAT64 []) pitch_f_mem : past pitch values.
   /* (FLOAT64 []) tempext_dcc : temporary LTP memory.
   /*-----
   /* RETURN ARGUMENTS:
35 /*
               None.
   void LTP_adap_cbk_correction (INT16 fix_rate_m, INT16 bfi, INT16 past_bfi,
                                             INT16 ppast bfi, FLOAT64 **qua_gainQ,
                                             FLOAT64 gainQ [], INT16 SVS_deci_m,
40
```

```
586
                                                   FLOAT64 temp_lagf, INT16 *update,
                                                   FLOAT64 pitch_f_mem [], INT16 lag [],
                                                   FLOAT64 lag_f [], FLOAT64 ForPitch_decTEMP [],
                                                   FLOAT64 tempext_dec [], FLOAT64 ext_dec [],
                                                   FLOAT64 **unfcod_dcc, PARAMETER channel)
 5
           INT16 n_sf, i_s, i_sf, l_sf;
10
           INT16 curr_lsf;
           FLOAT64 *px, temp_lag;
           FLOAT64 FET, PFET, AA;
15
            if ((bfi == 0) && (past_bfi == 1) && (ppast_bfi == 0) &&
          ((channel.fix_rate == RATE4_0K) || (channel.fix_rate == RATE8_5K))
          && ((fix_rate_m == RATE4_0K) \parallel (fix_rate_m == RATE8_5K)) &&
                   (qua_gainQ[0][0] > 0.6))
20
                    (*update) = 1;
                    if (fix_rate_m == RATE8_5K)
                            n_sf = N_SF4;
25
                    else
                            if (SVS_deci_m == 1)
                                    n_sf = N_SF3;
30
                             clse
                                    n_sf = N_SF2;
                            }
                     if (channel.fix_rate ==RATE8_5K)
                            curr_lsf = L_SF4;
35
                     clse
                            curr_lsf = L_SF0;
                     if ((SVS_dcci_m == 1) && (channel.idx_SVS_deci == 0))
 40
```

```
587
                           {
                                          PP then LTP
5
                            temp_lag = (temp_lagf- pitch_f_mcm [0])*159.0 /
                                            ((double) L_FRM+curr_lsf-1.0)+pitch_f_mem [0];
                            PIT_PitchInterpolat (temp_lag, pitch_f_mem,
                                                                                     ForPitch_decTEMP, 0);
10
                            }
                                   Sub-frame loop
15
                    i_s = 0;
                    for (i_sf = 0; i_sf < n_sf; i_sf++)
20
                                         Set subframe size
25
                            if (fix_rate_m != RATE4_0K)
                                    l_sf = L_SF4;
                             clse
                                     if (SVS_deci_m == 1)
30
                                             if (i_sf == N_SF3-1)
                                                     I_sf = L_SF3;
                                             else
                                                     l_sf = L_SF0;
35
                                             }
                                     clse
                                             l_sf = L_SF;
                                     }
40
```

```
588
                                       Adaptive Codebook Contribution
                             if (SVS_dcci_m == 1)
5
                                     {
                                                 PP then LTP (LTP or PP)
10
                                      LTP\_PP\_pitch\_ext\_decod\ (For Pitch\_decTEMP+i\_s,
                                                               tempext_dec, i_sf, unfcod_dec[0], lag,
                                                                       lag_f, l_sf);
15
                                      }
                              clse
                                       if ((SVS_deci_m == 0) && (channel.idx_SVS_deci == 1))
                                               {
20
                                                           LTP then PP
                                               temp\_lag = ((pitchf-lag\_f[0])*(i\_sf+1.0)*l\_sf)/
25
                                                                        ((double) L_FRM*2.0) + lag_f[0];
                                               }
                                       else
30
                                                            LTP then PP
                                                temp_lag = ((temp_lagf - lag_f[0])*(i_sf+1.0)*l_sf) /
                                                                         ((double) L_FRM+curr_lsf) + lag_f[0];
 35
                                                }
                                        lag[i_sf] = (short) temp_lag;
                                        LTP_pitch_decodtemp(tempext_dec, i_sf, unfcod_dec[0],
                                                                                  temp_lag, l_sf);
 40
```

```
589
                                   }
                                             Gains
5
                            gainQ[0] = qua\_gainQ[0][i\_sf];
                            gainQ[1] = qua\_gainQ[1][i\_sf];
10
                                       RE-Build the excitation
                            wad_dvector (unfcod_dec[0], gainQ[0], qua_unfcod[1]+i_s,
                                                   gainQ[1], tcmpext_dec+MAX_LAG, 0, 1_sf-1);
15 .
                            dot_dvcctor (tempext_dec+MAX_LAG, tempext_dec+MAX_LAG, &FET,
                                                    0, l_sf-1);
                            FET += EPSI;
20
                            if (lag[i_sf] > 0)
                                    {
                                    if (SVS_deci_m == 1)
                                            px = tempext_dec+MAX_LAG-(short)ForPitch_decTEMP[i_s];
                                    else
25
                                            px = tempext_dec+MAX_LAG-lag[i_sf];
                                    dot_dvector (px, px, &PFET, 0, 1_sf-1);
                                    AA = sqrt(PFET/FET);
30
                                    }
                            clse
                                    px = tempext_dec+MAX_LAG-l_sf;
35
                                    dot_dvector(px, px, &PFET, 0, 1_sf-1);
                            AA = sqrt(PFET/FET);
                                    }
                            px = tempext_dec+MAX_LAG;
                            sca_dvector(px, AA, px, 0, 1_sf-1);
40
```

	<pre>cpy_dvector (tempext_dec+l_sf, tempext_dec, 0, MAX_LAG-1);</pre>
5	i_s += l_sf;
	/*===========*/ }
10	/**/
	/* Replace Adaptive CB */ /**/
	cpy_dvector (tempext_dec, ext_dec, 0, MAX_LAG-1);
15	} /**/
	return;
.20	/**/ }
25	**/
,	*=====================================

```
591
  */
  /* Conexant System Inc.
5 /* 4311 Jamborec Road
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  */
15 /* PROTOTYPE FILE: lib_ltp.h
  /+____*/
  /*----*/
                              (void);
  void
       LTP_init_lib
        LTP_Init_SincWindows (void);
  void
25 void
        LTP_generate_PitLagTab (void);
                         (FLOAT64, INT16);
  FLOAT64
             Hwind
  FLOAT64 sincFwin2
                   (FLOAT64, INT16);
  FLOAT64 sincFwin
                   (FLOAT64, INT16);
30
                         (FLOAT64 [], FLOAT64, INT16);
        D2A_InterpWeight
  void
                         (FLOAT64 [], FLOAT64, INT16);
        D2A InterpWeight2
  void
                                    (FLOAT64 [],FLOAT64 [],INT16, FLOAT64, INT16);
  FLOAT64
             D2A interp
35 INT16 LTP_lag_to_idx5b
                              (FLOAT64);
  INT16 LTP_lag_to_idx7b
                               (FLOAT64);
  INT16 LTP_lag_to_idx8b
                               (FLOAT64);
        LTP_FineIndex_search (FLOAT64 [], INT16, INT16, INT16, INT16,
  void
                                          FLOAT64 [], INT16 *);
```

592

LTP_excit_vari_pitch (FLOAT64 *, INT16, INT16, FLOAT64 [], INT16, void FLOAT64[], INT16, FLOAT64 []); 5 (FLOAT64, INT16, FLOAT64 [], INT16, FLOAT64 [], LTP_excit_const_pitch void INT16); (FLOAT64 [], FLOAT64 *, FLOAT64 *, FLOAT64 *, 10 void LTP_PP_pitch_ext INT16, FLOAT64 *, FLOAT64 *, INT16 *, FLOAT64 *, FLOAT64 *, INT16); (FLOAT64 [], FLOAT64 *, INT16, FLOAT64 *, LTP_PP_pitch_ext_decod void INT16 *, FLOAT64 *, INT16); 15 LTP_closc_8_5k_pitch (FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16, void FLOAT64 [], INT16 *, FLOAT64 *, FLOAT64 [], FLOAT64 *, FLOAT64 *, INT16, INT16, INT16 20 **(I)**; (FLOAT64 [], FLOAT64 [], FLOAT64 [], INT16, LTP_close_7b_pitch void FLOAT64 [], FLOAT64 [], INT16 *, FLOAT64 *, 25 FLOAT64 *, FLOAT64 *, INT16, INT16, INT16 []); LTP_pitch_decodtemp (FLOAT64 [], INT16, FLOAT64 [], FLOAT64, INT16); void 30 (INT16, FLOAT64 [], INT16, FLOAT64 [], INT16 [], LTP_7b_pitch_decod void FLOAT64 [], INT16, INT16 []); LTP_8_5k_pitch_decod (INT16, FLOAT64 [], INT16, FLOAT64 [], INT16 [], void FLOAT64 [], INT16, INT16 []); 35 void LTP_adap_cbk_correction (INT16, INT16, INT16, INT16, FLOAT64 **, FLOAT64 [], INT16, FLOAT64, INT16 *, FLOAT64 [], INT16 [], FLOAT64 [], FLOAT64 [], FLOAT64 [],

593				
FLOAT64	[]	FLOAT64	**,	PARAMETER);

	/*====================================
	/**/
5	/*====================================

	594
	*/
Conexant System Inc.	*/
4311 Jamboree Road Newport Beach, CA 92660	*/
Newport Death, CA 92000	*/
Copyright(C) 2000 Conexant System Inc.	*/
Copyright(C) 2000 Conexam System 2005	*/
ALL RIGHTS RESERVED:	*/
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means or used to make any derivative work (such as transformation */
or adaptation) without the authorisation of C	onexant System Inc. */
======================================	
LIBRARY: lib_pit.c	*/
INCLUDE	*1
include "typedef.h"	
include "main.h"	
include "const.h"	
include "gputil.h"	
include gputti.ii	
finclude "ext_var.h"	
menude ext_viii.ii	
finclude "lib_pit.h"	
finclude "lib_ltp.h"	
morado molastro	
#ifdef DIAGNOSTICS	
#ifdef DIAGNOSTICS	·
#ifdef DIAGNOSTICS #include "lib_dia.h"	

			59:		•
		FUNCTIONS			
•		======================================		*/	
			+	·/	
		his function performs init			
/*	variable	s of the PIT library.	*/		
/*			+	·/	
0 /* IN	PUT ARGUME	NTS:		*/	
/*	_	None.	*/		
/*			+	' /	
/* O	UTPUT ARGUM	ENTS:		*/	
	_		*/		
			*		
	PUT/OUTPUT A			*/	
	· _		*/		
	×				
	ETURN ARGUM		**	*/	
0 /*	_	None.	*/		
/===					-
void	DIT init lib	(void)			
void	PIT_init_lib {	(voia)			•
25	•			*/	
,	,	• .			
	lagl = 20;				
	/*			*/	
60					
	pitchf = MT	N_LAG;			
	ini_dvector	IIN_LAG);			
15	/*			*/	
	/*	PIT_ol_2pitchs		*/	
	/*			+/	
	VUVm = 1				
40	VUVmm =	0;			

```
pitch_m = 20;
           pitch_mm = 40;
           Iopt0 = 0;
           ini_svector (PITmax0, 0, NUM_MAX_SRCH-1, 20);
5
           LagCount = 0;
            VadCount = 0;
            Av_{lag} = MIN_{LAG};
10
            Rp_m = 0.0;
            ini_svector(ol_lag, 0, N_SF_MAX, 30);
            ini_svector(lag, 0, N_SF_MAX, 30);
15
            return;
20
25
                                                             */
    /* FUNCTION : PIT_LT_Corr_Rinax ().
                    : This function performs a first pitch
    /* PURPOSE
                determination by finding NUM_MAX_SRCH maxima in */
 30 /*
                the autocorrelation.
                                              */
    /* INPUT ARGUMENTS:
                        Low_order : lower autocorrelation order. */
            _(INT16)
 35 '/*
                        High_order: Higher autocorrelation order. */
            _(INT16)
    /*
                                length of correlation window. */
            _ (INT16)
                        L:
                                 length of input.
            _(INT16)
                        L_r:
            _(FLOAT64 []) res:
                                   input frame.
```

```
597
  /* OUTPUT ARGUMENTS:
         _ (INT16 []) PITmax: autocorrelation maxima
  /*
                                            */
                        position.
  /*
         _ (FLOAT64 []) Rmax: autocorrelation maxima values */
  /*
                                                          */
  /* INPUT/OUTPUT ARGUMENTS:
                                            */
  /*
                    None.
  /* RETURN ARGUMENTS:
10 /*
                    None.
          PIT_LT_Corr_Rmax (INT16 Low_order, INT16 High_order, INT16 L,
  void
                                         INT16 L_r, FLOAT64 res[], INT16 PITmax [],
                                                FLOAT64 Rmax [])
15
           INT16 i, k, i0, T1, T2, K1, K2, L_R, low, high;
           FLOAT64 x, y, eng;
20
           FLOAT64 R[(HI_LAG2-MIN_LAG+4+2*SINC_LIMIT)];
                       Residual Aurocorrelation Calculation
25
                  = Low_order-SINC_LIMIT-1;
           low
                  = High_order+SINC_LIMIT+1;
           high
           L_R
                  = high-low+1;
30
   #ifdef VERBOSE
           if (L R>=HI LAG2-MIN_LAG+4+2*SINC_LIMIT)
                   printf(" L_R>=HI_LAG2-MIN_LAG+4+2*SINC_LIMIT !!!\n");
                   exit(0);
35
   #endif
            i0 = L r-L;
40
            dot dvector (res+i0, res+i0, &cng, 0, L-1);
```

```
598
            dot_dvector (res+i0-low+1, res+i0-low+1, &y, 0, L-1);
            for (i = low; i \le high; i++)
                    {
                     dot_dvector (res+i0, res+i0-i, &R[i-low], 0, L-1);
                     y += res[i0-i]*res[i0-i];
5
                    y = res[i0-i+L]*res[i0-i+L];
                     R[i-low] = sqrt(MAX(y*eng, 0.01));
                    }
10
                         Determine 4 best lags
       T2 = Low_order;
       for (k = 0; k < 4; k++)
15
                                                            */
                               Initial search
20
            T1 = T2;
            T2 = MIN(T1*2, High_order+1);
             PITmax[k] = T1;
             Rmax[k] = R[T1-low];
             for (i = T1+1; i < T2; i++)
25
                     if(R[i-low] > Rmax[k])
                              Rmax[k]=R[i-low];
30
                              PITmax[k]=i;
                         Further search with low-pass filtering
35
             K1 = MAX(PITmax[k]-1, Low_order);
              K2 = MIN(PITmax[k]+1, High_order);
             x = K1-low;
```

```
599
           R[K1-low] = D2A\_interp(SincWindows, R, L_R, x, SINC\_LIMIT);
           PITmax[k] = K1;
           Rmax[k] = R[K1-low];
           for(i = K1+1; i \le K2; i++)
 5
                   x = i-low;
                   R[i-low] = D2A\_interp(SincWindows, R, L_R, x, SINC\_LIMIT);
                    if (R[i-low] > Rmax[k])
                            PITmax[k] = i;
10
                           Rmax[k]=R[i-low];
                   }
15
        }
                                                           */
                          In case of 3 regions
20
            if (Low_order*8 >= High_order)
                    {
                    PITmax[3] = PITmax[2];
                    Rmax[3] = Rmax[2];
25
30
            return;
35
    /* FUNCTION : PIT_FirstPitchLag ().
```

```
/* PURPOSE : This function performs a first pitch
          determination.
 5 /* INPUT ARGUMENTS:
          _ (INT16 []) PITmax: autocorrelation maxima
                       position.
          _ (FLOAT64 []) Rmax: autocorrelation maxima values */
10 /* OUTPUT ARGUMENTS:
          _(INT16 *) lag_opt: optimum lag position.
          _(FLOAT64 *) R_opt: optimum lag frame
                       autocorrelation value.
         _(INT16 *) Iopt: optimum PITmax index.
   /* INPUT/OUTPUT ARGUMENTS:
                    _ None.
   /* RETURN ARGUMENTS:
20 /*
                    _ None.
          PIT_FirstPitchLag (INT16 PITmax [], FLOAT64 Rmax [], INT16 *lag_opt,
   void
                                                                      FLOAT64 *R_opt, INT16 *Iopt)
25
          {
           FLOAT64 MaxR;
           INT16 i;
30
                        First search and correction
35
           (*lopt) = 0;
           MaxR = Rmax[0];
           for (i = 1; i < NUM\_MAX\_SRCH; i++)
                  if (Rmax[i] > MaxR)
40
```

```
601
                        (*Iopt) = i;
                        MaxR = Rmax[i];
                 }
 5
          (*lag_opt)
                       = PITmax[(*Iopt)];
          (*R_opt)
                       = MaxR;
10
          return;
15
          }
/* FUNCTION : PIT_PitchLagCorrect ().
   /* PURPOSE : This function corrects the first pitch
             determination.
25 /*
                                      */
   /* INPUT ARGUMENTS:
         _(INT16 ) pitch_m: pitch of previous frame.
   /*
         (INT16 ) VUVm:
                             voice / unvoiced decision of */
30 /*
                      previous frame.
   /*
         _(INT16 ) pitch_mm: pitch value of 2 frames
                                                   */
                                       */
                      earlier.
         _(INT16 ) VUVmm:
   /*
                              voice / unvoiced decision of */
                      2 frames earlier.
35 /*
         _ (INT16 []) PITmax: autocorrelation maxima
   /*
                      position.
         _ (FLOAT64 []) Rmax: autocorrelation maxima values */
   /* OUTPUT ARGUMENTS:
40 /*
         _ (INT16 *) lop:
                           optimum PITmax index.
```

```
602
   /* INPUT/OUTPUT ARGUMENTS:
                    None.
 5 /* RETURN ARGUMENTS:
          (INT16 ) PITi: corrected pitch value.
   INT16 PIT_PitchLagCorrect(INT16 pitch_m,INT16 VUVm,INT16 pitch_mm,INT16 VUVmm,
10
               INT16 hi lag,INT16 *Iop,INT16 *PITmax,FLOAT64 *Rmax)
           {
           INT16 PITi, i, k, P1, P2, PIT test, lopt, flag;
15
           INT16 Decis_m, Decis_mm;
           FLOAT64
                          D, D0;
20
                  = *Iop;
           Iopt
                  = PITmax[lopt];
           PITi
25
                           Second Correction
           D0 = 1.0;
           for (i = Iopt-1; i \ge 0; i-)
30
                  {
                   P1 = (INT16)MAX(PITmax[i]*0.7, MIN_LAG);
                   P2 = (INT16)MIN(PITmax[i]*1.3, hi_lag);
35
                   D = D0;
                   if (VUVm>3 || (VUVm>2 && VUVmm>2))
                          if (Rmax[i] > 0.95) D = 0.95*D0;
40
                          }
```

```
603
                   else
                           {
                           if (R_{max}[i] > 0.85) D=0.9*D0;
                           }
5
                   Decis_m = (VUVm > 2 \&\& pitch_m >= P1 \&\& pitch_m <= P2);
                   Decis_mm = (VUVmm > 2 && pitch_mm >= P1 && pitch_mm <= P2);
10
                   if (Decis m || Decis_mm)
                           D = 0.85*D0;
                   if (Dccis_m && Decis_mm)
                           D = 0.65*D0;
15
                   Decis m = (VUVm > 3 \&\& pitch_m >= P1 \&\& pitch_m <= P2);
                   Decis_mm = (VUVmm > 3 && pitch_mm >= P1 && pitch_mm <= P2);
20
                   if (Decis_m || Decis_mm)
                           D = 0.75*D0;
                   if (Decis_m && Decis_mm)
                           D = 0.6*D0;
25
                   flag = 0;
                   for (k = 2; k \le 4; k++)
30
                                                  PITmax[Iopt]/k;
                           PIT_test
                           Ρl
                                                  (INT16)MAX(PIT_test*0.8, MIN_LAG);
                                                  (INT16)MIN(PIT_test*1.2, hi_lag);
                           if (PITmax[i] \ge P1 && PITmax[i] \le P2)
35
                                   flag=1;
                           }
40
```

BNSDOCID: <WO___0122402A1_I_>

```
if ((flag == 1) && (Rmax[i] > D*Rmax[Iopt])
                         && (Rmax[i] > 0.25))
                         {
                         PITi = PITmax[i];
                         *Iop = i;
5
                         break;
10
15
           return PITi;
   /* FUNCTION : PIT_ol_2pitchs ().
25 /* PURPOSE : This function estimati the 2 pitch per frame. */
   /*
   /* INPUT ARGUMENTS:
          _(FLOAT64 []) wspeech: weighted input speech.
         _ (INT16 ) L_ws:
                            input frame size.
30 /*
                              autocorrelation maxima values */
          _(FLOAT64 []) Rp:
   /*
                             vad decision.
          _(INT16 ) vad:
   /* OUTPUT ARGUMENTS:
                                                     */
          _(INT16 []) lag:
                             estimated lag values.
35 /*
   /* INPUT/OUTPUT ARGUMENTS:
                     _ None.
                                             */
40 /* RETURN ARGUMENTS:
```

605 _ None. 5 void PIT_ol_2pitchs (FLOAT64 wspeech[], INT16 L_ws, INT16 lag[], FLOAT64 Rp[], INT16 frame_class, INT16 hi_lag) { 10 INT16 i, L corr, L, Iopt, Iopt1, Iopt2, CorrectFlag, PITmax1[NUM_MAX_SRCH], PITmax2[NUM_MAX_SRCH]; FLOAT64 Rmax1[NUM_MAX_SRCH], Rmax2[NUM_MAX_SRCH]; /*_----*/ 15 Open loop pitch estimation in lookahead /+_----*/ L corr = L FRM/2;lag[0] = lag[2];20 Rp[0] = Rp[2];Calculate the autocorrelation values 25 $L = MIN(L ws-L_LPCLHD+L_corr, L_ws);$ PIT LT Corr_Rmax(MIN_LAG,hi_lag,L_corr,L,wspeech,PITmax2,Rmax2); 30 Searching for integer pitch PIT_FirstPitchLag(PITmax2, Rmax2, &(lag[2]), &(Rp[2]), &Iopt2); 35 Iopt = Iopt2;lag[2] = PIT_PitchLagCorrect(pitch_m, VUVm, pitch_mm, VUVmm, hi_lag, &Iopt2, PITmax2, Rmax2); Rp[2] = Rmax2[lopt2];40

```
606
                     Open loop pitch in second half frame
                                                              */
        /*
        if ((VUVmm < 3) && (VUVm < 3))
5
                   if (Rp[2] > 0.5)
                            VUVmm=3;
                           pitch_mm=lag[2];
                            if (Rp[0]>0.5 && lag[0]<lag[2])
10
                                   pitch_mm=lag[0];
                           }
                   else
                           {
                            if (Rp[0] > 0.5)
15
                                    VUVmm = 3;
                                    pitch_mm = lag[0];
                           }
                           }
20
                   }
                   Calculate the autocorrelation values
25
        L = L_ws-L_LPCLHD;
        PIT_LT_Corr_Rmax(MIN_LAG,hi_lag,L_corr,L,wspeech,PITmax1,Rmax1);
30
                     Searching for integer pitch
         PIT FirstPitchLag (PITmax1, Rmax1, &(lag[1]), &(Rp[1]), &lopt1);
35
         Iopt = Iopt1;
         lag[1] = PIT_PitchLagCorrect(pitch_m, VUVm, pitch_mm, VUVmm, hi_lag,
                    &Ioptl, PITmax1, Rmax1);
         Rp[1] = Rmax1[Iopt1];
```

```
607
                       Second correction for lag [1]
5
         CorrectFlag=0;
         if (abs(lag[2]-pitch_m) < 0.25*MIN(lag[2],pitch_m) &&
            abs(lag[2]-lag[1]) > 0.25*lag[1] &&
            (Rp[2]>0.5 \parallel Rp_m>0.5))
10
                     if (Rp[2] > 0.5)
                              pitch_mm = lag[2];
                     clse
                              pitch_mm = pitch_m;
                     CorrectFlag = 1;
15
                     }
          if (abs(lag[2]-lag[0]) < 0.25*MIN(lag[2],lag[0]) &&
            abs(lag[2]-lag[1]) > 0.25*lag[1])
20
             {
                      if ((Rp[2] > 0.5) && (Rp[0] > 0.5))
                               lag[1] = (lag[0] + lag[2])/2;
                               CorrectFlag = 0;
25
                       }
                      else
                              {
                               if (Rp[0] > 0.5)
                                       pitch_mm = lag[0];
30
                                       CorrectFlag = 1;
                                       }
                               if (Rp[2] > 0.5)
                                       pitch_mm = lag[2];
35
                                       CorrectFlag = 1;
                                       }
                               }
                      }
 40
```

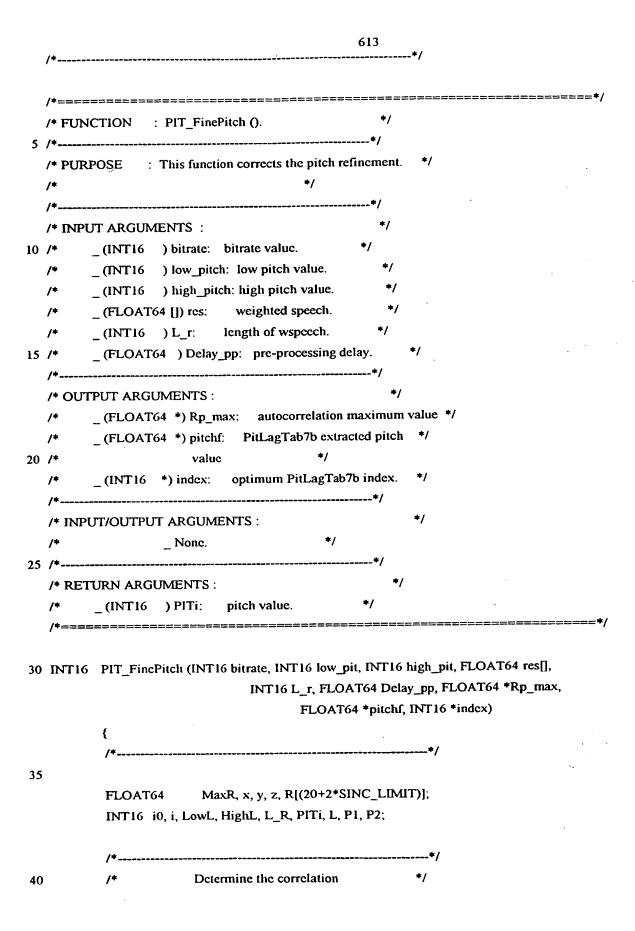
```
608
             if (((lag[1]-lag[2] \ge 0.3*MIN(lag[1],lag[2])) \&\&
             (Rp[2]>1.25*Rp[1] && Rp[1]<0.75 && Rp[2]>0.5)) \parallel
                 ((lag[2]-lag[1]>0.3*lag[1]) && (Rp[2]>1.25*Rp[1] && Rp[2]>0.5)))
 5
                             lag[1] = lag[2];
                             CorrectFlag = 0;
            if (CorrectFlag==1)
10
                    {
                     for (i=0;i<NUM_MAX_SRCH;i++)
                             if ((PITmax1[i] > 0.75*pitch_mm) &&
                                             (PITmax1[i] < 1.25*pitch_mm) &&
15
                                                     (Rmax1[i] > 0.5*Rmax1[lopt1]) &&
                                                              (PITmaxl[i] >
   PITmax1[lopt1]/NUM_MAX_SRCH))
                                                              {
                                                              lag[1] = PITmax1[i];
20
                                                              Rp[1] = Rmax1[i];
                                                              break;
25
                                                                 */
                          Second correction for lag [0]
30
            CorrectFlag=0;
            if ((lag[0]-lag[1] \ge 0.25*MIN(lag[0],lag[1])) &&
            (Rp[1] > 1.1*Rp[0]) && (Rp[0] < 0.75) && (Rp[1] > 0.5))
                    {
35
                     pitch_mm = lag[1];
                     CorrectFlag = 1;
          if (abs(lag[2]-lag[1]) < 0.25*MIN(lag[2],lag[1]) &&
            abs(lag[0]-lag[1]) > 0.25*lag[0] && Rp[1] > 0.5) {
40
```

```
609
              pitch_mm=lag[1];
              CorrectFlag=1;
            }
         if (abs(lag[1]-pitch_m) < 0.25*MIN(lag[1],pitch_m) &&
5
           abs(lag[1]-lag[0]) > 0.25*lag[0]) {
              if (Rp[1]>0.5 && Rp_m>0.5) {
                  lag[0]=(pitch_m+lag[1])/2;
                     CorrectFlag=0;
10
               }
              clse {
                     if (Rp_m>0.5) {
                pitch_mm=pitch_m;
                CorrectFlag=1;
15
              }
                     if (Rp[1]>0.5) {
                pitch_mm=lag[1];
                CorrectFlag=1;
20
         if (CorrectFlag==1) {
               for (i=0;i<NUM_MAX_SRCH;i++) {
                      if (PITmax0[i] > 0.75*pitch_mm &&
25
                 PITmax0[i] < 1.25*pitch_mm &&
                        Rmax0[i] > 0.75*Rmax0[lopt0] &&
                 PITmax0[i] > PITmax0[Iopt0]/NUM\_MAX\_SRCH)
                        {
                            lag[0]=PITmax0[i];
30
                            Rp[0]=Rmax0[i];
                            break;
35
                                                      */
                           Sct large lag
40
```

```
610
        if ((frame_class \geq 3) && (lag[1] \leq 50) &&
                    (Av_lag > MAX(2.25*lag[1], 50)))
           {
                    if (PITmax1[1]>lag[1] && Rmax1[1]>Rmax1[2])
 5
                            lag[1] = MAX(PITmax1[1], pitch_m);
                            Rp[1] = Rmaxl[1];
                    else
10
                            if (Rmax1[3]>Rmax1[2])
                                     lag[1] = MAX(PITmax1[3], pitch_m);
                                     Rp[1] = Rmax1[3];
15
                             else
                                     lag[1] = MAX(PITmax1[2], pitch_m);
                                     Rp[1] = Rmax1[2];
20
                            }
                    }
            if ((frame_class \geq 3) && (lag[0] \leq 50) &&
                            (Av_{lag} > MAX(2.25*lag[0], 50)))
25
                            {
                             if ((PITmax0[1] > lag[0]) && (Rmax0[1] > Rmax0[2]))
                                     lag[0] = MAX(PITmax0[1], pitch_m);
                                     Rp[0] = Rmax0[1];
30
                                     }
                             else
                                     if (Rmax0[3] > Rmax0[2])
35
                                             lag[0] = MAX(PITmax0[3], pitch_m);
                                             Rp[0] = Rmax0[3];
                                             }
                                     else
                                             {
40
```

```
611
                                              lag[0] = MAX(PITmax0[2], pitch_m);
                                              Rp[0] = Rmax0[2];
                                     }
 5
                            }
                         Estimate Average pitch
10
         if ((frame_class \geq 3) && (Rp[1] \geq 0.5) && (lag[1] \geq 0.7*pitch_m)
            && (lag[1] < 1.3*pitch_m)
                            LagCount++;
         else
                            LagCount = 0;
15
         if (LagCount >= 2)
           Av\_lag = (short)((double)Av\_lag * 0.75 + (double)lag[1] * 0.25);
20
         if (frame_class <= 0)
           VadCount++;
         else
           VadCount=0;
         if (VadCount > 3)
25
           Av_{lag} = 40/*MIN_{LAG}*/;
30
                            Update memory
         pitch_mm = pitch_m;
         pitch_m = lag[1];
         VUVmm = VUVm;
35
         VUVm = 1;
         if (Rp[1] > 0.25)
            VUVm = 2;
         if (Rp[1] > 0.5)
            VUVm = 3;
40
```

```
612
        if (Rp[1] > 0.75)
           VUVm = 4;
        Rp_m = Rp[1];
        Iopt0 = Iopt2;
 5
        for (i = 0; i < NUM\_MAX\_SRCH; i++)
                    PITmax0[i] = PITmax2[i];
                    Rmax0[i] = Rmax2[i];
10
                           high limit
15
        if (hi_lag == HI_LAG)
                    if (lag[0] > HI\_LAG)
                            {
                            if ((PITmax0[2] \le HI\_LAG) && (PITmax0[2] > L\_SF))
20
                                    lag[0] = PITmax0[2];
                            clsc
                                    lag[0] = HI_LAG;
                            }
25
                    if (lag[1] > HI_LAG)
                            if ((PITmax1[2] \le HI\_LAG) && (PITmax1[2] > L_SF))
                                    lag[1] = PITmax1[2];
                             else
30
                                    lag[1] = HI_LAG;
                            }
                    }
35
            return;
40
```



```
614
             LowL = low_pit - SINC_LIMIT - 2;
            HighL = high_pit + SINC_LIMIT + 2;
 5
            L_R = HighL - LowL + 1;
                    = L_FRM/2;
            L
10
            i0
                    = L r-L/2;
            dot_dvector (res+i0, res+i0, &x, 0, L-1);
            for (i = LowL; i \le HighL; i++)
15
                    dot_dvector (res+i0-i, res+i0-i, &y, 0, L-1);
                    dot_dvcctor (res+i0, res+i0-i, &z, 0, L-1);
                    R[i-LowL] = z / sqrt(MAX(x*y, 0.0001));
20
                                                              */
            /*
                          Searching for integer pitch
25
            PITi = low_pit;
            MaxR = R[low_pit-LowL];
            for (i = low_pit+1; i \le high_pit; i++)
            if (R[i-LowL] > MaxR)
                    {
                   PITi = i;
30
                    MaxR = R[i-LowL];
35
         if (bitrate == RATE8_5K)
                           Search for the best precise pitch and index
40
```

```
615
                    P1 = LTP_lag_to_idx8b ((double)(PITi-1.0));
                    P2 = LTP_lag_to_idx8b ((double)(PITi+1.0));
                    LTP_FincIndex_search (PitLagTab8b, P1, P2, LowL, L_R, R, index);
 5
                                 Estimate Rp_max
                    PITi = (INT16)(PitLagTab8b[*index]+0.5-LowL);
                    (*Rp_max) = R[PITi];
10
                    PITi = (INT16)(PitLagTab8b[*index]+0.5);
                                Modify pitch index
15
                    if (Dclay_pp > 10)
                            (*index) = MIN((*index)+1, MAX_PIT_IDX_8b);
                    if (Delay_pp < -10)
20
                            (*index) = MAX((*index)-1,0);
                    if (Delay_pp > 15)
                            (*index) = MIN((*index)+1, MAX_PIT_IDX_8b);
                    if (Delay_pp<-15)
25
                            (*index) = MAX((*index)-1, 0);
                    (*pitchf) = PitLagTab8b[(*index)];
                    PITi = (INT16)MIN(*pitchf+0.5, high_pit);
30
                    }
            elsc
                           Search for the best precise pitch and index
35
                    PI = LTP_lag_to_idx7b((FLOAT64)(PITi-1.0));
                    P2 = LTP_lag_to_idx7b((FLOAT64)(PlTi+1.0));
                    LTP FineIndex scarch(PitLagTab7b, P1, P2, LowL, L_R, R, index);
40
```

```
Estimate Rp_max
5
                            = (INT16)(PitLagTab7b[*index] + 0.5 - LowL);
                   PITi
                   (*Rp_max) = R[PITi];
                            = (INT16)(PitLagTab7b[*index] + 0.5);
                   PITi
10
                                Modify pitch index
                    if (Delay_pp > 10)
15
                           (*index) = MTN((*index)+1, MAX_PIT_IDX_7b);
                    if (Delay_pp < -10)
                           (*index) = MAX((*index)-1, 0);
20
                    (*pitchf) = PitLagTab7b[(*index)];
                    PITi = (INT16)MIN(*pitchf+0.5, high_pit);
                   }
25
            return PITi;
30
35
    /* FUNCTION : PIT_PitchInterpolat ().
    /* PURPOSE : This function calculate the interpolated pitch */
                                              */
40 /*
            track.
```

```
617
   /* INPUT ARGUMENTS:
         _(FLOAT64) pitch: current pitch value.
          _(FLOAT64 ) pitch_1: previous frame (n-1) pitch. */
          _(FLOAT64 ) pitch_2: previous frame (n-2) pitch. */
          _(INT16 ) enc_flag: signals encoder (=1) or decoder*/
                        (=0) operation.
   /* OUTPUT ARGUMENTS:
          _ (FLOAT64 []) ForPitch: interpolated pitch track.
   /* INPUT/OUTPUT ARGUMENTS:
15 /* RETURN ARGUMENTS:
   void PIT PitchInterpolat (FLOAT64 pitch, FLOAT64 pitch_mem [],
                                          FLOAT64 ForPitch[], INT16 enc_flag)
20
            INT16 i;
            FLOAT64 pitch_mean;
25
            pitch_mean = (pitch_mem [1] + pitch)*0.5;
30
            if ( (fabs(pitch_mem \{0\}-pitch)< 0.2*0.5*(pitch_mem \{0\}+pitch)) \parallel
                           (fabs(pitch_mem [0]-pitch_mean) < 0.15*pitch_mem [0]))
                   {
35
                    for (i = 0; i < L_FRM; i++)
                           ForPitch [i] = (i*pitch+(L_FRM-i)*pitch_mem [0]) / L_FRM;
                    if(cnc_flag == 1)
                     for (i = 0; i < L_FRM/2; i++)
                      ForPitch [i + L_FRM] = pitch;
40
```

```
618
                  }
           clse
                   if(enc_flag == 1)
 5
                          for (i = 0; i < L FRM+L FRM/2; i++)
                                 ForPitch[i] = pitch;
                   else
                          for (i = 0; i < L FRM; i++)
                                 ForPitch[i] = pitch;
10
                  }
           return;
15
20
   /* FUNCTION : PIT_pitch_track_recons ().
      -----*/
   /* PURPOSE : This function reconstruct the pitch track in
25 /*
              case of frame crasure.
   /* INPUT ARGUMENTS:
   /* _ (INT16) fix_rate_m: previous frame fixed rate.
   /* _ (INT16) bfi:
                      frame # n Bad Frame Indicator.
30 /* _ (INT16) past_bfi:
                           frame # n-1 Bad Frame Indicator.
   /* _ (INT16) ppast_bfi: frame # n-2 Bad Frame Indicator.
   /* _ (FLOAT64 []) lag_f:
                             decoded pitch lags
   /* _ (INT16) SVS_deci_m:
                              frame # n-1 mode indicator.
   /* _ (FLOAT64 **) qua_gainQ: adaptive and fixed codebook gains. */
35 /* _ (parameter) channel: decoded parameters indexes structure. */
   /* OUTPUT ARGUMENTS:
   /* _ (FLOAT64 *) pitchf :
                                  extrapolated pitch value. */
       _(FLOAT64 *) ForPitch_decTEMP: interpolated pitch track. */
```

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```
619
                                                              */
   /* INPUT/OUTPUT ARGUMENTS:
       _ (FLOAT64 []) pitch_f_mcm : past pitch values.
   /* RETURN ARGUMENTS :
       _ None.
   void PIT_pitch_track_recons (INT16
                                            fix_rate_m, INT16
                                                                    bfi, INT16
                                                                                    past_bfi,
                                            INT16 ppast_bfi, FLOAT64 lag_f[], INT16 SVS_deci_m,
                                            FLOAT64 **qua_gainQ, PARAMETER channel,
10
                                            FLOAT64 *pitchf, FLOAT64 pitch_f_mem [],
                                            FLOAT64 ForPitch_decTEMP [])
15
        FLOAT64 temp_lagf;
            if ((bfi == 0) && ((ppast_bfi == 1) \parallel (past_bfi == 0)))
20
                    pitch_index = channel.idx_pitch[0];
                    pitch_f_mem [1] = pitch_f_mem [0];
25
                    pitch_f_mem [0] = (*pitchf);
                    if (channel.fix_rate==RATE8_5K)
                           (*pitchf) = PitLagTab8b[pitch_index];
                    else
                           (*pitchf) = PitLagTab7b[pitch_index];
30
                    if ((fix_rate_m == RATE2_0K) || (fix_rate_m == RATE0_8K))
                    pitch_f_mem [1] = (*pitchf);
35
                    pitch_f_mem [0] = (*pitchf);
                           }
                    else if (SVS_deci_m == 0)
                            if (fix_rate_m != RATE8_5K)
40
```

```
620
                                     pitch_f_mem [1] = lag_f[N_SF2-1];
                                     pitch_f_mem [0] = lag_f[N_SF2-1];
                             else
5
                                     pitch_f_mem [1] = lag_f[N_SF4-1];
                                     pitch_f_mem [0] = lag_f[N_SF4-1];
                             }
10
                    }
            else
                     if ((bfi == 0) && (ppast_bfi == 0) && (past_bfi == 1))
                             {
15
                             if ((fix_rate_m == RATE2_0K) ||
                                             (fix_ratc_m == RATE0_8K))
                                              pitch_f_mem [1] = (*pitchf);
                                              pitch_f_mem [0] = (*pitchf);
20
                             else if (SVS_deci_m == 0)
                                     {
                                      if (fix_rate_m != RATE8_5K)
                                              pitch_f_mem [1] = lag_f[N_SF2-1];
25
                                              pitch_f_mem [0] = lag_f[N_SF2-1];
                                      else
                                              pitch_f_mem[1] = lag_f[N_SF4-1];
30
                                              pitch_f_mem [0] = lag_f[N_SF4-1];
                                     }
35
                              pitch_index = channel.idx_pitch[0];
                              if (channel.fix_rate == RATE8_5K)
                                     (*pitchf) = PitLagTab8b[pitch_index];
                              else
                                     (*pitchf) = PitLagTab7b[pitch_index];
                              temp_lagf = ((*pitchf) - pitch_f_mem [0])*159.0 / 319.0
40
```

```
621
                                                                       + pitch_f_mem [0];
                             if (SVS_dcci_m == 1)
                                     PIT_PitchInterpolat (temp_lagf, pitch_f_mcm,
                                                     ForPitch_decTEMP, 0);
                             pitch_f_mem [1] = pitch_f_mem [0];
                             if ((SVS_deci_m == 1) || (qua_gainQ[0][0] >= 0.6))
                                             pitch_f_mem [0] = temp_lagf;
10
                            }
                     else
                             pitch_f_mem [1] = pitch_f_mem [0];
15
                             pitch_f_mcm [0] = (*pitchf);
                    }
20
            return;
25
```

,	*	622	622 ===================================		
/	*======================================				
/	* Conexant System Inc.	*/			
5 /	* 4311 Jamborce Road	*/			
	* Newport Beach, CA 92660	*/	•		
/	÷	*/			
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/	* means or used to make any de	rivative work (such as transformation */			
	or adaptation) without the authorisation of Conexant System Inc. */				
/	*		=======================================		
15 /	* LIBRARY: lib_pit.h	*/ .			
1	*======================================		=======================================		
		•			
/	*	+/			
20 /	*FU	NCTIONS*/			
,	*	+/			
·					
,	void PIT_init_lib	(void);			
	/old 1 11_lill(_ilo	(10.0),			
	void PIT_LT_Corr_Rmax	(INT16, INT16, INT16, INT16, FLOAT6	4 INT 6 *.		
25	void TTT_ET_Con_ranax	FLOAT64 *);	,, ,		
		TEORIO),			
	1 1 DYT 171 (D) 1 1	(NITEL OF THE OFTER A TRUTH A THOUGH	24 * INT16 *\·		
•	void PIT_FirstPitchLag	(INT16 *, FLOAT64 *, INT16 *, FLOAT	04 ·, INTIO ·),		
			ATTC1 / *		
30 I	INT16 PIT_PitchLagCorrect	(INT16, INT16, I	N110 +,		
		INT16 *, FLOAT64 *);			
,	void PIT_ol_2pitchs	(FLOAT64 [], INT16, INT16 []	FLOAT64 [], INT16,		
		INT 16);			
35		·			
	INT16 PIT_FinePitch	(INT16, INT16, INT16, FLOAT	64 [], INT16, FLOAT64,		
		FLOAT64 *, FLOAT64	*, INT16 *);		

40 void PIT_PitchInterpolat (FLOAT64, FLOAT64 [], FLOAT64 [], INT16);

	void PIT_pitch_track_recons (INT16, INT16, INT16, INT16, FLOAT64 [], INT16,		
	FLOAT64 **, PARAMETER, FLOAT64 *,		
5	FLOAT64 [], FLOAT64 []);		
	/*=====================================		
	/**/		
	/*		
10	·		

```
624
  /* Conexant System Inc.
5 /* 4311 Jamboree Road
  /* Newport Beach, CA 92660
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  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  */
15 /* LIBRARY: lib_ppp.c
  /*____*/
     -----*/
  #include "typedef.h"
  #include "main.h"
25 #include "const.h"
  #include "gputil.h"
  #include "ext_var.h"
  #include "lib lpc.h"
30 #include "lib_flt.h"
   #include "lib_ltp.h"
   #include "lib_ppp.h"
   #ifdef DIAGNOSTICS
35
   #include "lib_dia.h"
   #endif
```

			525 */	
/*	FUNCTION	1S	*/	
			/	-=======/
	NCTION : PPP_init_lib ().			
/* PU	RPOSE : This function initialise	the global va	riables of */	
	the library PPP.	*/		•
	PUT ARGUMENTS :		- -'/ ` */	
	_ Nonc.	*/		·
/*				
	JTPUT ARGUMENTS:	* */	*/	
	_ None.	•	*/	
/* IN	PUT/OUTPUT ARGUMENTS:		*/	
	_ None.	*/	**	
	ETURN ARGUMENTS :		*/	
	Nonc.	*/		
/*===		=======		_=======*/
noid.	DDD init lib (void)	•		
voia . 25	PPP_init_lib (void) {			
	/*		+/	
	/* pitch_preproc		*/	
	/*		*/	
30	$Delay_pp = 0.0;$			
	frame_class_pp_m = 3;			
	$Last_Rp = 0.0;$			
	/+		*/	
35	/ · · · · · · · · · · · · · · · · · · ·		•	
	return;			
			*1	
	/*			
40	}			

	626
UNCTION: PPP_mapping_residu().	*/
	*/
PURPOSE : This function this subrouti	
(from T0 to T1) to modified resid	dual */
(MapRcsidu[0, 1,, Len-1]).	*/
	*/
INPUT ARGUMENTS:	*/
(FLOAT64 []) Sinc: table of inte	erpolation weights.*/
_ (INT16) sinc_limit: half interpola	ation table size. */
[[INT16] lcn_sinc: interpolation	table size. */
_ (FLOAT64 []) residu: number of	pulses. */
(INT 16) T0: Original starting	ng time point. */
(INT16) T1: Original endin	
t	*/
OUTPUT ARGUMENTS:	*/
_ (FLOAT64 []) MapResidu: mapped	
	*/
INPUT/OUTPUT ARGUMENTS:	*/
Nonc.	*/
*	*/
* RETURN ARGUMENTS :	*/
	*/
* _ None.	
void PPP_mapping_residu (FLOAT64 sinc []	
	[], FLOAT64 T0, FLOAT64 T1, INT16 Len,
	4 MapResidu[])
{	*/
/*	*/
FLOAT64 *Weight;	
FLOAT64 t, f, D;	
INT16 i, m, T;	
/*	*/
• •	
D = (T1-T0)/(FLOAT64)Len;	

```
if (fabs(D-1.0) > 0.0001)
                    for (i = 0; i < Len; i++)
5
                            t = T0 + i*D;
                            T = (INT16)t;
                            f = t-(FLOAT64)T;
                            m = (INT16)(f*(FLOAT64)DELT_F2+0.500001);
                            Weight = sinc + m*len_sinc;
10
                            MapResidu[i] = D2A_interp (Weight, residu, L_OLPIT, t,
                                                                                     sinc_limit);
                           }
                    }
15
           elsc
                    T = (INT16)T0;
                    f = T0 - (FLOAT64)T;
                    m = (INT16)(f*(FLOAT64)DELT_F2+0.500001);
                    Weight = sinc + m*len_sinc;
20
                    for (i = 0; i < Len; i++)
                            t = T0 + i;
                            MapResidu[i] = D2A_interp (Weight, residu, L_OLPIT, t,
25
                                                                                      sinc_limit);
                            }
                    }
30
            return;
35
40 /* FUNCTION : PPP_update_LT_mem ().
                                                               */
```

```
628
  /*_____*/
  /* PURPOSE : This function updates the excitation memory. */
  /*-----*/
  /* INPUT ARGUMENTS:
      _ (FLOAT64 []) LTm: table of interpolation weights. */
      _(INT16 ) L_LTm: excitation memory size.
  /*
      _(INT16 ) L_E: new excitation memory size.
  /* OUTPUT ARGUMENTS:
10 /*
      _ (FLOAT64 []) E: new excitation.
      /* INPUT/OUTPUT ARGUMENTS:
      _ None.
                             */
15 /* RETURN ARGUMENTS:
      None.
  /*------*
  void PPP_update LT_mem (INT16 L_LTm, FLOAT64 LTm[], INT16 L_E, FLOAT64 E[])
20
       /*----*/
       INT16 j;
       /*____*/
25
       for (j = 0; j < L \ LTm-L \ E; j++)
            LTm[j] = LTm[j+L_E];
30
       cpy_dvector (E, LTm+L_LTm-L_E, 0, L_E-1);
       /*_____*/
35
       return;
       }
40
```

/*		629 **/
•		
/* FU	NCTION: PPP_search_shift_pp().	*/
/*		*/
/* PU	RPOSE : This function find the first sub-c	optimal shift */
/*	by maximising the intercorrelation bet	ween the */
/ *	target signal and the input signal.	*/
/+		*/
/* INI	PUT ARGUMENTS :	*/
/*	_ (FLOAT64 []) residu: original signal.	*/
/*	(INT16) T0: original starting time p	oint. */
/*	(INT16) i0: starting index for the int	tercorr. */
/*	_ (INT16) i1: ending index for the inte	
/ *	- ·	*/
/*	(FLOAT64 []) Tg: target signal.	*/
•	_ (, 20110 , (), 1880. 0.8	*/
•	JTPUT ARGUMENTS :	*/
	_ (FLOAT64 *) Shift: optimum shift.	*/
	—	·
/ *	_(FLOAT64 *) MaxRp: maximum inter	
•		*/
	PUT/OUTPUT ARGUMENTS :	•
	_ None. */	
•		
/* RE	TURN ARGUMENTS :	*/
	_ None. */	
/*===		
void I	PPP_scarch_shift_pp (FLOAT64 residu[], FLC)AT64 T0, INT16 i0, INT16 i1,
	INT16 Len, FLOAT64 Tg[], FLOAT64	4 *Shift, FLOAT64 *MaxRp)
	{	
	/+	* /
	INT16 L, i, i00, i11, i0L, i1H, t0, lopt, lo	dx, L_R;
	FLOAT64 R, R1, R2, crit, Rp[20+2*SINC	-
	DifTab[MAX DIF TAB], ShiftR	
		· · · · ·
	/*	*/
1	•	-
0	/* 	*/

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```
630
            (*Shift) = 0;
            lopt
                      = 0;
            dot_dvcctor (Tg, Tg, &R1, 0, Len-1);
            i00 = i0 - SINC_LIMIT - 1;
            i11 = i1 + SINC_LIMIT + 1;
5
            TT0 = T0 + i00;
            TT1 = T0 + ill + Len + 1;
            L = ill + Len + 1 - i00;
10
            PPP mapping residu(SincWindows_PP, SINC_LIMIT_PP, LEN_SINC_PP, residu,
                                               TT0, TT1, L, ShiftRes);
15
             for (i = i0; i \le i1; i++)
                      t0 = i - i00;
                      dot_dvector (Tg, ShiftRes+t0, &R, 0, Len-1);
                      dot dvector (ShiftRes+t0, ShiftRes+t0, &R2, 0, Lcn-1);
                      Rp[i-i00] = R / sqrt(MAX(0.001, R2*R1));
20
                      crit = Rp[i-i00];
                      if (crit > (*MaxRp))
                              (*MaxRp) = crit;
                              (*Shift) = i;
25
                              Iopt
                                       = i;
                     }
                                                                 */
                              Additional correlation
30
             iOL = Iopt - SINC LIMIT - 1;
             i1H = Iopt + SINC_LIMIT + 1;
35
             for (i = i0L; i < i0; i++)
                      {
                      10 = i - i00;
```

dot_dvector (Tg, ShiftRes+t0, &R, 0, Len-1);

dot_dvector (ShiftRes+t0, ShiftRes+t0, &R2, 0, Len-1);

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```
Rp[i-i00] = R / sqrt(MAX(0.001, R2*R1));
            for (i = i1+1; i \le i1H; i++)
 5
                    {
                     t0 = i - i00;
                     dot_dvcctor (Tg, ShiftRes+t0, &R, 0, Len-1);
                     dot dvector (ShiftRes+t0, ShiftRes+t0, &R2, 0, Len-1);
                     Rp[i-i00] = R / sqrt(MAX(0.001, R2*R1));
10
                         Fractional search of the delay
15
            for (i = 0; i < MAX_DIF_TAB; i++)
                    DifTab[i] = Iopt - 0.75 + 0.1*i;
    #ifdef PROG_BUG_FIX
            L_R = i1H - i00;
20
    #else
            L_R = i1H - i0L + 1;
    #endif
             LTP_FineIndex_scarch (DifTab, 0, MAX_DIF_TAB-I, i00, L_R, Rp, &Idx);
25
             (*Shift) = DifTab[Idx];
             i = (INT16)(DifTab[Idx] + 0.5 - i00);
             (*MaxRp) = MAX(Rp[i], 0);
30
             return;
35
```

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```
632
   /* FUNCTION
                  : PPP_search_opt_shift_pp ().
   /* PURPOSE
                                                             */
                   : This function find the optimal shift by
                maximising the intercorrelation between the
                target signal and the input signal. And if the */
               procedure "fails" the waveform interpolation is */
               performed.
   /* INPUT ARGUMENTS:
        _ (INT16 ) frame_class: class of the current frame.
        _ (FLOAT64 ) PitLag:
                                current pitch lag.
        _ (FLOAT64 []) residu:
                                original signal.
        _ (INT 16
                  ) T0:
                             starting time point.
        (INT 16
                  ) Center:
                              segment center.
15 /*
        _ (INT 16
                  ) l_sf:
                             sub-frame size.
        _(INT16 ) L_Tg:
                               target size.
        _ (FLOAT64 []) Tg:
                                target signal.
        _ (INT 16
                   ) SRCH0:
                                starting index for max intercorr. */
                                              */
                        search.
20 /*
        (INT16 ) SRCH1:
                                ending index for max intercorr. */
                                              */
                        search.
   /* OUTPUT ARGUMENTS:
        _ (FLOAT64 []) MapResOpt: wrapped signal.
25 /*
        _ (FLOAT64 *) Delay:
                                  optimum shift.
        _ (FLOAT64 *) Rp_Opt:
                                   maximum intercorrelation value. */
   /* INPUT/OUTPUT ARGUMENTS:
          _ None.
                                               */
   /* RETURN ARGUMENTS:
          Nonc.
                                               */
35 void PPP_search_opt_shift_pp (INT16 frame_class, FLOAT64 PitLag, FLOAT64 residu[],
                                   FLOAT64 T0, INT16 Center, INT16 1_sf, INT16 L_Tg,
                                   FLOAT64 Tg[], FLOAT64 MapResOpt[], INT16 SRCH0,
                                   INT16 SRCH1, FLOAT64 *Delay, FLOAT64 *Rp_Opt)
40
```

```
FLOAT64 Shift, CRITmax, TT0, T1;
           FLOAT64 Tg_m[MAX_L_TG];
           INT16 i;
5
                                                           */
                            Search of the delay
10
           if (frame_class >= 3)
                   {
                    CRITmax = 0.3;
                    for (i = 0; i < Center; i++)
                           Tg_m[i] = (0.25 + 0.75*i/Center)*Tg[i];
15
                   }
            else
                    CRITmax = 0.4;
                    for (i = 0; i < Center; i++)
                           Tg_m[i] = (0.5 + 0.5*i / Center)*Tg[i];
20
                    }
            cpy_dvector(Tg, Tg_m, Center, L_Tg-1);
25
           PPP_scarch_shift_pp (residu, T0, SRCH0, SRCH1, L_Tg, Tg_m, &Shift,
                                                    &CRITmax);
                            = Shift;
            (*Delay)
                            = CRITmax;
            (*Rp_Opt)
30
                            Wrapped residual
35
            T1 = T0 + Center + Shift;
            PPP_mapping_residu (SincWindows_E, SINC_LIMIT_E, LEN_SINC_E, residu,
                                                    T0, T1, Center, MapResOpt);
```

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```
634
            if (L_Tg > Center)
                   {
                    TT0 = T1:
                    TI = TT0 + L_Tg - Center;
 5
                    PPP_mapping_residu (SincWindows_E, SINC_LIMIT_E, LEN_SINC_E,
                                                                  residu, TT0, T1, (INT16)(L_Tg - Center),
                                                                          (MapResOpt + Center));
                   }
10
            return;
15
   /* FUNCTION : PPP_periodicity_dctcct ().
   /* PURPOSE
                  : This function estimate the level of periodicity */
   /*
               of the input signal and calculate the normalized */
               crosscorrelation factors.
25 /* INPUT ARGUMENTS:
   /* _ (FLOAT64 []) Tg0:
                               target signal.
       _(INT16 ) L_ModiSig: size of the modified signal.
   /* OUTPUT ARGUMENTS:
       _ (FLOAT64 *) Gp:
                               optimum shift.
       _ (FLOAT64 *) Rp:
                               normalized crosscorrelation factor.*/
   /* INPUT/OUTPUT ARGUMENTS:
   /* _ (FLOAT64 []) ModiSig0: modified signal.
   /* RETURN ARGUMENTS:
          None.
40 void PPP_periodicity_detect (FLOAT64 ModiSig0 [], FLOAT64 Tg0 [], INT16 L ModiSig,
```

```
FLOAT64 *Gp, FLOAT64 *Rp, INT16 smv_mode, FLOAT64 nsr)
           {
 5
            FLOAT64 R0, R1, R2, R3;
            FLOAT64 Tg[MAX_L_TG], ModiSig[MAX_L_TG];
            INT16 i;
10
            dot_dvector (ModiSig0, ModiSig0, &R0, 0, L_ModiSig-1);
                                                            0, L_ModiSig-1);
                                           Tg,
            cpy_dvector (Tg0,
            cpy_dvector (ModiSig0, ModiSig, 0, L_ModiSig-1);
15
            dot_dvector (ModiSig,
                                   Tg,
                                            &R1, 0, L_ModiSig-1);
                                                    &R2, 0, L_ModiSig-1);
                                           Tg,
            dot_dvector (Tg,
            dot_dvector (ModiSig,
                                   ModiSig, &R3, 0, L_ModiSig-1);
20
            (*Gp) = R1 / MAX(R2, 0.001);
            (*Rp) = R1 / sqn(MAX(0.1, R2*R3));
25
            if ((*Rp) > 0.5)
                    if (smv_mode >= 1)
                           R1 = MIN(nsr*4 + 0.25, 0.4) * MIN((*Gp), 1.0);
                    else
                           R1 = MIN(nsr*4 + 0.125, 0.25) * MIN((*Gp), 1.0);
30
                    for (i = 0; i < L_ModiSig; i++)
                           ModiSig0[i] += R1 * Tg0[i];
                    dot_dvector (ModiSig0, ModiSig0, &R3, 0, L_ModiSig-1);
35
                    R2 = sqrt (R0 / MAX(R3, 0.0001));
                    for (i = 0; i < L_ModiSig; i++)
                           ModiSig0[i] *= R2;
40
```

	}				
	/*		·*/		
	,		•		
5	return;				
	/*		+/		
	}	,	,		,
10 /*			*/		
/ * =:	+=====================================	=======		:========	
/* F	FUNCTION : PPP_sharpness ().		*/		
	PURPOSE : This function estimate the input signal.	sharpeness */	of the */	4.4	
		•	*/		
	NPUT ARGUMENTS :		*/		
	_(INT16) Len: size of the input s				
	_ (FLOAT64 []) res: target signal.		-		
	OUTPUT ARGUMENTS:		*/		
	None.	*/	•		
•	NPUT/OUTPUT ARGUMENTS:		*/		
	None.	*/	,		
	_		*/		
	RETURN ARGUMENTS:		*/		
	_(FLOAT64) P_SHP: sharpness fac		*/		
30 7					
FLO	OAT64 PPP_sharpness (INT16 len, FLOA'	T64 res[])			
	(
35	/*		·*/	·	
رد	FLOAT64 Max, E_x, M, P_SHP;				
	INT16 i;				
40	/*		*/	•	
40					

```
637
          E_x = 0.0;
          Max = 0.0;
          for (i = 0; i < lcn; i++)
5
                       = fabs(res[i]);
                 M
                 E_x += M;
                 if (M > Max)
                       Max = M;
10
          P SHP = E_x / (len * MAX(1.0, Max));
15
          return (P_SHP);
                                                   */
  /* FUNCTION : PPP_locat_max_puls ().
   /*____*/
25 /* PURPOSE : This function locate the peak of energy into a */
  /*
             pitch period.
   /*_____
  /* INPUT ARGUMENTS:
     _ (FLOAT64 []) Res: input signal.
      _(INT16 ) Start: starting index.
30 /*
      _(INT16 ) End:
  /*
                       ending index.
      _ (INT16 ) n_peak: number of peaks.
   /* OUTPUT ARGUMENTS:
35 /* _(INT16 *) PulsLoc: pulse index.
      _(FLOAT64 *) PeakRatio: ration between the peak energy and */
                                         */
                    signal energy.
   /* INPUT/OUTPUT ARGUMENTS:
40 /* Nonc.
                                         */
```

```
638
   /* RETURN ARGUMENTS:
                                              */
   /* None.
  void PPP_locat_max_puls (FLOAT64 Res[], INT16 Start, INT16 End, INT16 n_peak,
              INT16 *PulsLoc, FLOAT64 *PeakRatio)
10
           FLOAT64 Eng, MaxEng[MAX_PULS_NUM], AvgEng;
            INT16 i, k, Loc, PulsLocat[MAX_PULS_NUM];
15
            for (k = 0; k < n_pcak; k++)
                   PulsLocat[k] = Start;
            Eng = 0;
            for (k = -HALFP; k \leq HALFP; k++)
                   Eng += Res[Start+k]*Res[Start+k];
20
            AvgEng = Eng;
            for (k = 0; k < n_peak; k++)
                   MaxEng[k] = Eng;
25
            for (i = Start+1; i \le End; i++)
                    Eng = 0.0;
                    for (k = -HALFP; k \leq HALFP; k++)
30
                           Eng += Res[i+k]*Res[i+k];
                    if (Eng < 4*AvgEng)
                            AvgEng = 0.875*AvgEng + 0.125*Eng;
                    if (Eng > MaxEng[0])
35
                            MaxEng[0] = Eng;
                            PulsLocat[0] = i;
                            for (k = 1; k < n_peak; k++)
                                    if (MaxEng[k-1] > MaxEng[k])
 40
```

```
639
                                          {
                                           Eng
                                                                 = MaxEng[k];
                                           MaxEng[k]
                                                                 = MaxEng[k-1];
                                           MaxEng[k-1] = Eng;
                                                                  = PulsLocat[k];
 5
                                           Loc
                                           PulsLocat[k] = PulsLocat[k-1];
                                           PulsLocat[k-1] = Loc;
                                           }
                          }
10
                   }
           Loc = PulsLocat[n_peak-1];
15
           Eng = MaxEng[n_peak-1];
           for (k = n_peak-2; k \ge 0; k--)
                   if ((MaxEng[k] > 0.75*MaxEng[n_peak-1]) &&
                                   (AvgEng/MAX(MaxEng[k],0.1) < 1/9.0))
20
                   {
                   Loc = PulsLocat[k];
                   Eng = MaxEng[k];
                   break;
25
           (*PulsLoc) = Loc;
           (*PeakRatio) = AvgEng/MAX(Eng, 0.1);
30
            return;
35
   /* FUNCTION : PPP_pitch_preproc ().
```

```
640
                  : This function perform the pitch modify the input */
  /* PURPOSE
               signal in order to improve the LTP performance */
               and without introducing perceptually significant */
                                              */.
               distortions.
   /* INPUT ARGUMENTS:
                                                            */
                                  selected SMV mode.
       _(INT16 ) smv_mode:
                               selected fixed bit-rate.
       (INT16 ) fix_rate:
                               flat input speech flag.
       (INT16 ) flat_flag:
                                estim, noise to signal ratio. */
10 /*
       (FLOAT64 ) nsr:
        (FLOAT64 []) wspeech:
                                   input signal.
   /*
       _ (FLOAT64 []) ForPitch:
                                   forward pitch estimations.
        _(INT16 ) frame_class_pp: class of the current frame for */
                         picth preprocessing.
   /* OUTPUT ARGUMENTS:
                                   modified signal.
       _ (FLOAT64 []) ModiSig:
                                   pitch pre-processing delay. */
        _(FLOAT64 *) Delay_pp:
        _(INT16 *) frame_class: class of the current frame. */
                               —voice unvoice decision.
        _(INT16 *) VUV:--
20 /*
        _(INT16 []) gp:
                               Pitch gains.
   /* INPUT/OUTPUT ARGUMENTS:
                                               */
       Nonc.
25 /*-----
   /* RETURN ARGUMENTS:
                                               */
    /* None.
30 void PPP_pitch_preproc (INT16 smv_mode, INT16 fix_rate, INT16 flat_flag,
                                           FLOAT64 nsr, FLOAT64 wspeech[], FLOAT64 ModiSig[],
                                                   FLOAT64 ForPitch[], FLOAT64 *Delay_pp,
                                                           INT16 frame_class_pp, INT16 *frame_class,
                                                                   INT16 *VUV, FLOAT64 gp[])
35
             INT16 i0, i, i_s, L_Tg, L, l_sf, Center;
             FLOAT64 T0, T1, Delay, Delay_tmp, P_SHP, P_SHP_Tg, Rp_Opt;
             FLOAT64\ LTm[MAX\_LAG+MAX\_L\_TG],\ Tg[MAX\_L\_TG],\ PitFunc[L\_FRM+L\_FRM/2];
 40
```

```
FLOAT64 ModiRes[MAX_L_TG];
           INT16 SRCH0, SRCH1, StartPoint, EndPoint, LocPuls, PulsLen;
           INT16 cond_abs, cond0, cond1, cond2, cond3, cond;
           FLOAT64 ltp_gain=0, pit_corr=0, avg_Rp, min_Rp;
           FLOAT64 Rp[N_SF4] = \{0.0\};
5
                          .....*/
           Delay_tmp = (*Delay_pp);
10
           if ((frame_class_pp <= 0) || (frm_count == 1))
                   {
                                Reset of the delay
15
                   if (frame_class_pp == -1)
                           Delay_tmp = 0;
                   T0 = L_OLPIT - L_LPCLHD - L_FRM + Delay_tmp;
20
                   T1 = T0 + L_FRM;
                   PPP_mapping_residu (SincWindows_E, SINC_LIMIT_E, LEN_SINC_E,
                                                                wspeech, T0, T1, L_FRM, ModiSig);
25
                                Memorize the wspeech
                   PPP_updatc_LT_mem (MAX_LAG, targ_mem, L_FRM, ModiSig);
30
                    ltp_gain= 0.0;
                    pit_corr = 0.0;
                                  = 0.0;
                    avg_Rp
                    min_Rp
                                  = 1;
                    ini_dvector(gp, 0, N_SF4-1, 0.0);
35
                    ini_dvector(Rp, 0, N_SF4-1, 0.0);
           else
                    i0 = 0;
 40
```

```
i_s = 0;
```

```
ini_dvector(gp, 0, N_SF4 - 1, 0.0);
ini_dvector(Rp, 0, N_SF4 - 1, 0.0);
```

5 $avg_Rp = 0.0;$

 $min_Rp = 1.0;$

while (i0 < L_FRM) {

/* Determine some parameters

/*----*/

642

15 /* Pulse Length *
/*-----*/

PulsLen = (INT16)MAX(ForPitch[i0]*0.2, 5);

PulsLen = MIN(PulsLen, 30);

/*____*/

/* Peak location */

/*____*/

25 /*----*/
/* Starting point on the original wspeech */

/* Starting point on the original wspeech */

T0 = L_OLPIT - L_LPCLHD - L_FRM + i0 + Delay_tmp;

StartPoint = (INT16)T0;

EndPoint = (INT16)MIN(StartPoint + ForPitch[i0],

L_OLPIT - 8 - SINC_LIMIT);

35 PPP_locat_max_puls (wspeech, StartPoint, EndPoint, 1,

&LocPuls, &P_SHP);

if ((i0 == 0) && (LocPuls < PulsLen + T0)) LocPuls = (INT16)(PulsLen + T0);

40

-20-

```
643
                                          Searching Length
                           1_sf = (INT16)MIN(LocPuls - T0 + PulsLen, L_FRM - i0);
5
                           L_Tg = (INT16)(LocPuls - T0 + 2*PulsLen);
                           if (LocPuls + ForPitch[i0]*1.25 >
                                                                   L_OLPIT - L_LPCLHD + Delay_tmp)
                                   StartPoint = (INT16)(LocPuls + ForPitch[i0]*0.5);
10
                                                   = (INT16)MIN(StartPoint+ForPitch[i0],
                                   EndPoint
                                                                           L_OLPIT - 8 - SINC_LIMIT);
                                    PPP_locat_max_puls (wspeech, StartPoint, EndPoint, 1,
                                                                                   &L, &P_SHP);
                                    if (L > L OLPIT - L_LPCLHD + Delay_tmp)
15
                                            I_sf = L_FRM - i0;
                                            L_Tg = (INT16)MAX(l_sf, LocPuls-T0 + 2*PulsLen);
                                   }
20
                            SRCH1 = 5;
                            L = L FRM - i0 + MAX(0, L_LPCLHD - (INT16)Dclay_tmp -
25 SINC_LIMIT_E - SRCH1);
                            L Tg = MIN(L_Tg, L);
                            L_Tg = MIN(L_Tg, MAX_L_TG);
                            Center = (INT16)MAX(LocPuls-T0-PulsLen, PulsLen/2);
                            Center = MIN(l_sf, Center);
30
                         Calculate the target : Tg[0,..,L_Tg-1], for the
                                   wspeech modification
35
                            cpy_dvector (targ_mem, LTm, 0, MAX_LAG-1);
                            L = MIN(L_Tg, L_FRM - i0 + L_FRM / 2);
                            cpy_dvector (ForPitch + i0, PitFunc, 0, L-1);
                            ini_dvector (PitFunc, L, L_Tg-1, PitFunc[L-1]);
40
```

```
LTP_excit_vari_pitch(SincWindows_E, LEN_SINC_E,
                                                         SINC_LIMIT_E, PitFunc, MAX_LAG,
                                                                         LTm, L_Tg, Tg);
 5
                                    Searching Range
10
                                      Peakness
                           StartPoint = (INT16)(LocPuls - ForPitch[i0]*0.5);
                                          = (INT16)MIN(StartPoint + ForPitch[i0],
15
                           EndPoint
                                                                 L OLPIT - 8 - SINC_LIMIT);
                           PPP locat_max_puls (wspeech, StartPoint, EndPoint, 1, &L,
                           &P_SHP);
                           PPP_locat_max_puls (Tg, (INT16)(HALFP+1),
                                                  (INT16)(L_Tg-HALFP-1), (INT16)1, &L, &P_SHP_Tg);
25
                                    Search range
                           SRCH1=MAX(MIN(Center*0.075+0.5, SRCH1), 1);
                           SRCH0 = -SRCH1;
30
                                   Influence of P_SHP
35
                           if (P_SHP > 1/4.0)
                                   SRCH1 = MIN(SRCH1, 3);
                                   SRCH0 = MAX(SRCH0, -3);
                           else if (P_SHP > 1/12.0)
40
```

```
645
                                   SRCH1 = MIN(SRCH1, 4);
                                   SRCH0 = MAX(SRCH0, -4);
5
                                  Influence of P_SHP_Tg
                           if (frame_class_pp >= 3)
10
                                  L = 4;
                           clse
                                   L = 3;
                            if (P_SHP_Tg > 1/3.0)
15
                                   SRCH1 = MIN(SRCH1, L);
                                   SRCH0 = MAX(SRCH0, -L);
                                   }
                            else if (P_SHP_Tg > 1/6.0)
20
                                   {
                                           SRCH1 = MIN(SRCH1, (L+1));
                                           SRCH0 = MAX(SRCH0, -(L+1));
                                   }
25
                            Influence of LocPuls and non-voiced speech
                            if ((L_Tg < LocPuls - T0 - HALFP) \parallel (ForPitch[i0] < 25)
30
                                                    || (LocPuls <= PulsLen + T0))
                                    if (P_SHP > 1/4.0)
                                           {
                                            SRCH1 = MIN(SRCH1, 1);
35
                                            SRCH0 = MAX(SRCH0, -1);
                                            }
                                    else
                                            SRCH1 = MIN(SRCH1, 2);
 40
```

```
646
                                            SRCH0 = MAX(SRCH0, -2);
                                    }
 5
                                    Influence of Delay_tmp
                            if (Dclay_tmp > 5)
10
                                    SRCH1 = MIN(SRCH1, 4);
                            if (Delay_tmp > 10)
                                    SRCH1 = MIN(SRCH1, 2);
                            if (Delay_tmp > 15)
                                    SRCH1 = 0;
15
                            if (Delay_tmp < -5)
                                    SRCH0 = MAX(SRCH0, -4);
                            if (Delay_tmp < -10)
                                    SRCH0 = MAX(SRCH0, -2);
                            if (Delay_tmp < -15)
                                    SRCH0 = 0;
                                 Searching for the best delay;
25
                            PPP_search_opt_shift_pp (frame_class_pp, ForPitch[i0+1_sf-1],
                                                                    wspeech, T0, Center, 1_sf, L_Tg, Tg,
                                                                            ModiRes, SRCH0, SRCH1,
30 &Delay,
                                                                                    &Rp_Opt);
                      /* Determine the delay at the end point: ModiRes[1 sf] */
35
                            if (Center > 1_sf)
                                    Delay *= (FLOAT64)l_sf/(FLOAT64)Center;
40
                            Delay = Delay_tmp + Delay;
```

```
if (fabs(Delay) > MAX_DELAY)
   #ifdef VERBOSE
                                   if (frame_class_pp >= 3)
5
                                           printf("Danger!!!, Delay_tmp>MAX_DELAY \n");
                                   clsc
                                           printf("Danger!!!!, Delay_tmp>MAX_DELAY \n");
   #endif
10
                                   T1 = T0 + L_Tg;
                                   PPP_mapping_residu (SincWindows_E, SINC_LIMIT_E,
                                                                                   LEN_SINC_E, wspeech,
   T0, T1,
                                                                                           L_Tg,
15
   ModiRes);
                            else
                                   Dclay_tmp = Delay;
20
                                Memorize the modified wspeech
                            PPP_update_LT_mem (MAX_LAG, targ_mem, l_sf, ModiRes);
25
                                     Periodicity
30
                            PPP periodicity_detect(ModiRes, Tg, L_Tg, &ltp_gain,
                                                           &pit_corr, smv_mode, nsr);
                            cpy_dvector (ModiRcs, ModiSig+i0, 0, 1_sf-1);
35
                            if (((P_SHP < 1.0/30) || (ltp_gain > 0.7) ||
                                           (pit\_corr > 0.6)) && (L\_Tg > LocPuls - T0 + HALFP)
                                                   && ((*VUV) == 2))
                                                            (*VUV) = 3;
40
```

```
648
```

```
if (pit_corr < min_Rp)
                                            min_Rp = pit_corr;
                            pit_corr = MAX(pit_corr, 0.0);
5
                            avg_Rp += pit_corr*l_sf;
10
                             if (fix_rate == RATE8_5K)
                                      for (i=0; i < N_SF4; i++)
                                              L = MIN(i0 + l\_sf, (i+1)*L\_SF4) - MAX(i0, i*L\_SF4);
15
                                              gp[i] += ltp\_gain*MAX(L, 0);
                                              Rp[i] += pit_corr*MAX(L, 0);
                                      }
                              elsc
 20-
                                      for (i=0; i < N_SF3; i++)
                                               L = MIN(i0 + l_sf, (i+1)*L_sF0) - MAX(i0, i*L_sF0);
                                               gp[i] += ltp_gain*MAX(L, 0);
 25
                                               Rp[i] += pit_corr*MAX(L, 0);
                                               }
                                       }
                               i0 += l_sf;
 30
                            Classification and output LTP gains
  35
               avg_Rp /= L_FRM;
```

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```
649
            if (fix_rate \approx= RATE8_5K)
                                     RATE 8.5 kbit/s
 5
                                        gains
10
                     for (i = 0; i < N_SF4; i++)
                             Rp[i] /= L_SF4;
                     for (i = 0; i < N_SF4; i++)
                             gp[i] /= L_SF4;
15
                     if (flat_flag == 1)
                              for (i = 0; i < N_SF4; i++)
                                      gp[i] = MAX(MIN(gp[i]*(0.75+0.25*Rp[i]), 1.2), 0);
20
                              }
                     cise
                              for (i = 0; i < N_SF4; i++)
                                      gp[i] = MAX(MIN(gp[i]*(0.7+0.3*avg_Rp), 1.2), 0);
25
                                    Correct classfication
30
                      if ((Last_Rp > 0.6) && (avg_Rp > 0.6) && (frame_class_pp > 0))
                               ltp_gain = (gp[0] + gp[1] + gp[2] + gp[3]) / 4.0;
35
                               cond0 = (fabs(gp[0]-ltp_gain) < 0.25);
                               cond1 = (fabs(gp[1]-ltp_gain) < 0.25);
                               cond2 = (fabs(gp[2]-ltp_gain) < 0.25);
                               cond3 = (fabs(gp[3]-ltp_gain) < 0.25);
40
```

650

```
cond_abs = cond0 && cond1 && cond2 && cond3;
                            cond0 = (gp[0] > 0.5);
                            cond1 = (gp[1] > 0.5);
5
                            cond2 = (gp[2] > 0.5);
                            cond3 = (gp[3] > 0.5);
                             cond = cond0 && cond1 && cond2 && cond3;
10
                             if ((cond_abs == 1) && (cond == 1) && (ltp_gain > 0.5)
                                             && (\min_{p > 0.5})
                                                      (*frame_class) = 6;
                             cond0 = (Rp[0] > 0.75);
15
                              cond1 = (Rp[1] > 0.75);
                              cond2 = (Rp[2] > 0.75);
                              cond3 = (Rp[3] > 0.75);
                              cond = cond0 && cond1 && cond2 && cond3;
20
                              if ((ltp_gain > 0.5) && (cond == 1))
                                      (*frame\_class) = 6;
                              }
 25
                      Last_Rp = Rp[N_SF4-1];
                      }
              elsc
  30
                                      RATE 4.0 kbit/s
                                                               */
                                         gains
  35
                        for (i = 0; i < N_SF3; i++)
                                Rp[i] = L_SF0;
```

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```
651
                     for (i = 0; i < N_SF3; i++)
                             gp[i] /= L_SF0;
                     for (i = 0; i < N_SF4; i++)
 5
                             gp[i] = MAX(MIN(gp[i]*(0.75+0.25*Rp[i]), 1.2), 0);
                     ltp_gain = (gp[0] + gp[1] + gp[2]) / 3.0;
10
                                  Correct classfication
                     if ((avg_Rp > 0.6) && (frame_class_pp > 0))
15
                             cond0 = (fabs(gp[0]-ltp\_gain) < 0.25);
                              cond1 = (fabs(gp[1]-ltp_gain) < 0.25);
                              cond2 = (fabs(gp[2]-ltp_gain) < 0.25);
                              cond_abs = cond0 && cond1 && cond2;
20
                              cond0 = (gp[0] > 0.4);
                              cond1 = (gp[1] > 0.4);
                              cond2 = (gp[2] > 0.4);
                              cond = cond0 && cond1 && cond2;
25
                              if ((cond_abs == 1) && (cond == 1) && (ltp_gain > 0.5)
                                              && (min Rp > 0.4))
                                                      (*frame_class) = 6;
30
                             }
                     Last_Rp = Rp[N_SF3-1];
35
                               Resct the delay
            (*Delay_pp) = Delay_tmp;
40
```

	652
	frame_class_pp_m = frame_class_pp;
	/**/
5	return;
	/**/
	}
10	/**/
	/*
	/**/ /**/
	/*====================================
15	

653 */ /* Conexant System Inc. 5 /* 4311 Jamboree Road /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. /*-----10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ */ 15 /* PROTOTYPE FILE : lib_ppp.h /*_______/ /*<u>-----</u>*/ /*----*/ 20 /*----*/ void PPP_init_lib (void); 25 void PPP_mapping_rcsidu (FLOAT64 [], INT16, INT16, FLOAT64 [], FLOAT64, FLOAT64, INT16, FLOAT64 []); (INT16, FLOAT64 [], INT16, FLOAT64 []); void PPP_update_LT_mem 30 void PPP_search_shift_pp (FLOAT64 [], FLOAT64, INT16, INT16, INT16, FLOAT64 [], FLOAT64 *, FLOAT64 *); 35 void PPP_search_opt_shift_pp (INT16, FLOAT64, FLOAT64, I, FLOAT64, INT16, INT16, INT16, FLOAT64 [], FLOAT64 [], INT16, INT16, FLOAT64 *, FLOAT64 *);

	/**====================================	655 	
		•	
	/*====================================	*/	===*/
	/* Conexant System Inc.	*/	
	/* 4311 Jamboree Road	*/	
	/* Newport Bcach, CA 92660	•	
	/*		
	/* Copyright(C) 2000 Conexant System Inc.	*/	•
	/* /* ALL RIGHTS RESERVED:	*/	
	* No part of this software may be reproduced in a	·	
	* means or used to make any derivative work (suc		
	* means or used to make any derivative work (suc * or adaptation) without the authorisation of Con-		
	/* or adaptation) without the authorisation of Com-		===*/
	/*====================================	*/	•
	/*====================================		===*/
	, - 		
	/*	*/	
	/*INCLUDE		
	/*		
	/*		
	W 1 . 3 . No	·	
	#include "typedef.h"		
	#include "main.h"		
	#include "const.h"		
	#include "ext_var.h"		
	#include "mcutil.h"		
	#include "gputil.h"		
	#include "lib_ppr.h"		
	#include "lib_fit.h"	•	
	_		
	#include "lib_lpc.h" #include "lib_ltp.h"		
	#include "lib_ltp.h" #include "lib_npn.h"	•	
	#include "lib_ppp.h"		
•	14	± 1	
	/*		
	/*FUNCTIONS	······································	
	/*		

```
656
 /* FUNCTION : PPR_filters_init ()
  /* PURPOSE : This function performs the initialisation
        for the post and pre processing.
                                                   */
  /* INPUT ARGUMENTS:
                                             */
                None.
  /* OUTPUT ARGUMENTS:
                _ None.
10 /*
   /* RETURN ARGUMENTS :
                 _ Nonc.
15
           PPR_filters_init (void)
    void
            INT 16 i, 1;
 20
             FLOAT64 x;
                Generate the window for Tilt Compensation
 25
              I = L_LPC-L_SF/3;
              for (i = 0; i < l; i++)
  30
                      x = cos(i*PI/(FLOAT64)l);
                      tilt_window[i] = 0.54-0.46 * x;
                      }
   35
               for (i = 1; i < L_LPC; i++)
                tilt_window[i] = cos((i-l)*Pl*0.49/(L_LPC-l));
                            Pre-processing High-pass filters
    40
```

```
657
            pre_flt_num [0] = -0.92724705/0.46363718;
            pre_flt_num[1] = +1.000000000,
 5
            pre_flt_den [0] = -1.9059465;
            pre_flt_den [1] = +0.9114024;
            pre_gain = 0.46363718;
10
                              Tilt compensation
15
            tc_mem_dec = 0.0;
            tc\_coeff = 0.0;
            tc_gain = 1.0;
20
                            Post-processing filters
            r_zero = 0.57;
25
            r_pole = 0.75;
            pst_scale = 1.0;
            pst_hp_mem = 0.0;
30
                             Post-processing filters
            pst_flt_num [0] = -1.8544941/0.92727435;
            pst_flt_num [1] = +1.000000000,
35
            pst_fit_den [0] = -1.9059465;
            pst_flt_den [1] = +0.9114024;
            pst_gain = 0.92727435*2.0;
40
```

					
		/*		·*/	
		return;			
5				4.1	
		/*		- */	•
	•	}			·
<i>f</i>	*			*/	
10					*/
					,
/	* FUN	CTION : PPR_silence_enh	an_init ()	*/	
,	/*			-* /	
,	/* PUR	POSE: This function perf	orms the initialis	ation */	
15	/ *	for the silence enhancen	nent in the input	*/	
	/ *	frame	*/		
	/*				
	/* INP	UT ARGUMENTS :		*/	
	/ *	_ None.	*/		
20	/*			*/	,
	/* OU	TPUT ARGUMENTS:		*/	
	/*	_ None.	*/		
	/*				
	/* RE	TURN ARGUMENTS :		*/	
25	/ *	_ None.	*/		*/
	/ * ===	_ None.	.=========	:========	
	void	PPR_silence_enhan_init (voi	d)		
		{			
30)	/*		*/	
		INT16 i;			•
					. •
		/*			
3:	5	/* Set-up the initial		*/	
		/*		*/	
		$zero_level = 8.0;$			
		$min_delta = 65534;$			
4	10				

```
659
           for (i = 0; i \le SE\_MEM\_SIZE; i++)
                   zero_rate [i] = 0.0;
                   low_ratc [i] = 0.0;
5
                   high_rate[i] = 0.0;
                   zeroed [i] = 1;
                  }
           12_{neg} = -24.0;
10
           11_{neg} = -8.0;
           11 pos = 8.0;
           12 \text{ pos} = 24.0;
           low_neg[0] = -32766.0;
15
           low_neg[1] = -32767.0;
           low_pos[0] = 32767.0;
           low_pos[1] = 32768.0;
20
        return;
25
   /* FUNCTION : PPR_silence_enhan ()
   /* PURPOSE : This function perfroms the enhancement of the */
                silence in the input frame.
                                                  */
   /* INPUT ARGUMENTS:
                 _(FLOAT64 []) x_in: input speech frame. */
35 /*
                 _(INT16 ) N : speech frame size. */
   /* OUTPUT ARGUMENTS:
                 _ (FLOAT64 []) x_out: output speech frame. */
```

```
660
  /* RETURN ARGUMENTS:
                None.
          PPR_silence_enhan (FLOAT64 x_in [], FLOAT64 x_out [], INT16 N)
           INT16 tmp;
           INT16 i, idle_noise;
10
           INT16 cond1, cond2, cond3, cond4;
           INT16 *hist;
           INT32 delta;
           FLOAT64 *min, *max;
15
                   = svector (0, SE_HIS_SIZE-1);
         hist
         max = dvector(0, 1);
                 = dvector (0,-1);
         min
20
                         Initialisation
 25
             min[0] = 32767.0;
             min[1] = 32766.0;
             max[0] = -32767.0;
             max[1] = -32766.0;
  30
                     Loop on the input sample frame
              for(i = 0; i < N; i++)
  35
                      tmp = (INT16) x_in[i];
   40
```

```
661
                           Find the 2 Max values in the input frame
                         */
                    if (tmp > max[0])
5
                            \max [1] = \max[0];
                            max [0] = tmp;
                    clse if ((tmp > max[1]) && (tmp < max[0]))
10
                           max[1] = tmp;
                           Find the 2 Min values in the input frame
15
                    if (tmp < min[0])
                           {
                            min[1] = min[0];
20
                            min[0] = tmp;
                    else if ((tmp < min[1]) && (tmp > min[0]))
                           min[1] = tmp;
25
                          Find the 2 Min positive values and the 2 Min
                    /*
                           abs. negative values in the input frame
                    if (tmp \ge 0)
30
                            if (tmp < low_pos[0])
                                    low_pos[1] = low_pos[0];
                                    low_pos[0] = tmp;
35
                            else if ((tmp < low_pos[1]) && (tmp > low_pos[0]))
                                   low_pos[1] = tmp;
                           }
                    else
40
```

```
662
                            if (tinp > low_neg[0])
                                    {
                                     low_neg[1] = low_neg[0];
                                     low_neg[0] = tmp;
5
                             else if ((tmp > low_neg[1]) && (tmp < low_neg[0]))
                                     low_neg[1] = tmp;
                             }
10
                     }
                  Calculate the difference between Max and Min
                                                                       */
15
              delta = (INT32)(max[0]-min[0]);
              -if-((delta < min_delta) && (max[0] > min[0]))
                                       = delta;
                       min_delta
                       if (min_dclta <= DELTA_THRLD)
  25
                                if ((\max[1] \ge 0.0) && (\max[0] \ge 0.0))
                                         11_{pos} = max[1];
   30
                                         12\_pos = max[0];
                                 else
                                         {
                                        . if (low_pos[0] < 32767.0)
   35
                                                  11_pos = low_pos[0];
                                          if (low_pos[1] < 32767.0)
                                                  12_{pos} = low_{pos}[1];
                                          }
    40
```

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```
663
                               if ((\min[0] < 0.0) && (\min[1] < 0.0))
 5
                                       12_{neg} = min[0];
                                       11\_neg = min[1];
                               clse
10
                                       if (low_neg[0] > -32766.0)
                                                11\_neg = low\_neg[0];
                                        if (low_neg[1] > -32766.0)
                                                12_neg = low_neg[1];
                                       }
15
                     }
20
                           Update zero level
25
             if (low_pos[0] < zero_lcvcl)
                     zero_level = low_pos[0];
                         Update the Histogramm
30
             for(i = 0; i < N; i++)
                      if ((x_in[i] \ge 12_neg) && (x_in[i] < 11_neg))
35
                              hist [0]++;
                      else if ((x_in[i] >= 11_neg) && (x_in[i] < 0.0))
                              hist [1]++;
                      else if ((x_in[i] \ge 0.0) && (x_in[i] \le 11_pos))
                              hist [2]++;
40
                      elsc if ((x_in[i] > 11_pos) && (x_in[i] <= 12_pos))
```

```
664
                            hist [3]++;
                    else
                            hist [4]++;
                    }
5
                          Update the History
            for(i = SE\_MEM\_SIZE-1; i > 0; i--)
10
                     {
                     zero_rate [i] = zero_rate [i-1];
                     low_rate [i] = low_rate [i-1];
                     high_rate [i] = high_rate [i-1];
                     zeroed [i] = zeroed [i-1];
15
                     }
                        Current Frame Rate Calculation
 20
              if (hist [2] == N)
                      zero_rate[0] = 1.0;
              clsc
                      zero_ratc[0] = (FLOAT64) hist [2] / (FLOAT64)N;
 25
              \inf ((hist[1] + hist[2]) == N)
                       low_rate[0] = 1.0;
               else
                       low_rate[0] = (FLOAT64)(hist[1] + hist[2]) / (FLOAT64)N;
  30
               if (hist[4] == N)
                       high_rate[0] = 1.0;
                elsc
                        high_rate[0] = (FLOAT64)hist[4] / (FLOAT64)N;
  35
                            Silence Frame Detection
    40
```

```
idlc_noise = 1;
             for (i = 0; i < SE\_MEM\_SIZE; i++)
5
                     if ((zcro_rate[i] < 0.55) || (low_rate[i] < 0.80) ||
                                               (high_rate[i] > 0.07))
                                                       idlc_noise = 0;
                     }
10
             cond1 = ((zcro_rate[0] \ge 0.95) && (high_rate[0] \le 0.03));
             cond2 = ((low_rate[0] >= 0.90) && (low_rate[1] >= 0.90) &&
                             (high_rate[0] \le 0.030));
15
             cond3 = ((low_rate[0] >= 0.80) && (low_rate[1] >= 0.90) &&
                             (high_ratc[0] \le 0.010) && (zeroed[1] == 1));
             cond4 = ((low_rate[0] >= 0.75) && (low_rate[1] >= 0.75) &&
                             (high_rate[0] \le 0.004) && (zeroed[1] == 1));
20
                   Modify the dignal if is a silence frame
25
             if (cond1 || cond2 || cond3 || cond4 || idlc_noise)
                     {
                      if (zeroed [1] == 1)
30
                                            Keep the Signal Down
                              ini_dvector(x_out, 0, N-1, zcro_level);
35
                              }
                      else
                                             Ramp Signal Down
40
```

```
for (i = 0; i \le SE_RAMP_SIZE; i++)
                                    x_{out}[i] = ((FLOAT64)(SE_RAMP_SIZE - 1 - i) * x_in[i] +
                                                                      (FLOAT64)i * zero_lcvcl) /
5
           (FLOAT64)(SE_RAMP_SIZE - 1);
                            ini_dvector (x_out, SE_RAMP_SIZE, N-1, zero_level);
                     zeroed [0] = 1;
10
            else if (zcrocd[1] == 1)
                    {
                                     Ramp Signal Up
15
                     for (i = 0; i \le SE_RAMP_SIZE; i++)
                             x_{out}[i] = ((FLOAT64)i * x_{in}[i] +
                                                      (FLOAT64)(SE_RAMP_SIZE - 1 - i) * zero_level) /
20
                                                               (FLOAT64)(SE_RAMP_SIZE - 1);
                     zeroed[0] = 0;
              else
                     zeroed[0] = 0;
 25
              free_svector (hist, 0, SE_HIS_SIZE-1);
 30
              free_dvcctor (max, 0, 1);
              free_dvector (min, 0, 1);
 35
           return;
              }
  40
```

```
667
  /*_______/*
  /* FUNCTION : PPR lowpass ()
5 /*----*/
  /* PURPOSE : This function perfroms the lowpass filtering */
            of the input frame.
  /* INPUT ARGUMENTS:
10 /* (INT16 ) 1_frm: speech frame size.
                                         */
      (INT16 ) smv_mode: SMV running mode.
      _(INT16 ) flat_flag: flat input speech flag.
  /* OUTPUT ARGUMENTS:
                                  */
15 /*
            None.
  /* INPUT/OUTPUT ARGUMENTS:
  /* _ (FLOAT64 []) x_in: input/output speech frame.
  /*_____
20 /* RETURN ARGUMENTS:
  void PPR_lowpass (FLOAT64 x_in[], INT16 1_frm, INT16 smv_mode, INT16 flat_flag)
25
         FLOAT64 x;
         INT16 i;
30
         /*_____*/
         if (flat_flag==0)
              {
              switch (smv_mode)
35
                    case 0: lp_flt_num[0] = 0.0;
                          break;
                    case 1: lp_flt_num[0] = 0.05;
                          break;
40
```

```
case 2: lp_flt_num[0] = 0.075;
                                    break;
                            }
                   }
5
            else
                    switch (smv_mode)
                            {
                            case 0: lp_flt_num[0] = -0.1;
                                    break;
10
                            case 1: lp_flt_num[0] = 0.0;
                                    break;
                            case 2: lp_flt_num[0] = 0.0;
                                    break;
                            }
15
                    }
            for (i = 0; i < l_frm; i++)---
                     x = x_in[i] + lp_flt_buf[0]*lp_flt_num[0];
                     lp_flt_buf[0] = x_in[i];
                     x_in[i] = x;
25
             return;
 30
 35
     /* FUNCTION : PPR_highpass ()
     /* PURPOSE : This function perfroms the highpass filtering */
             of the input frame.
 40 /*
```

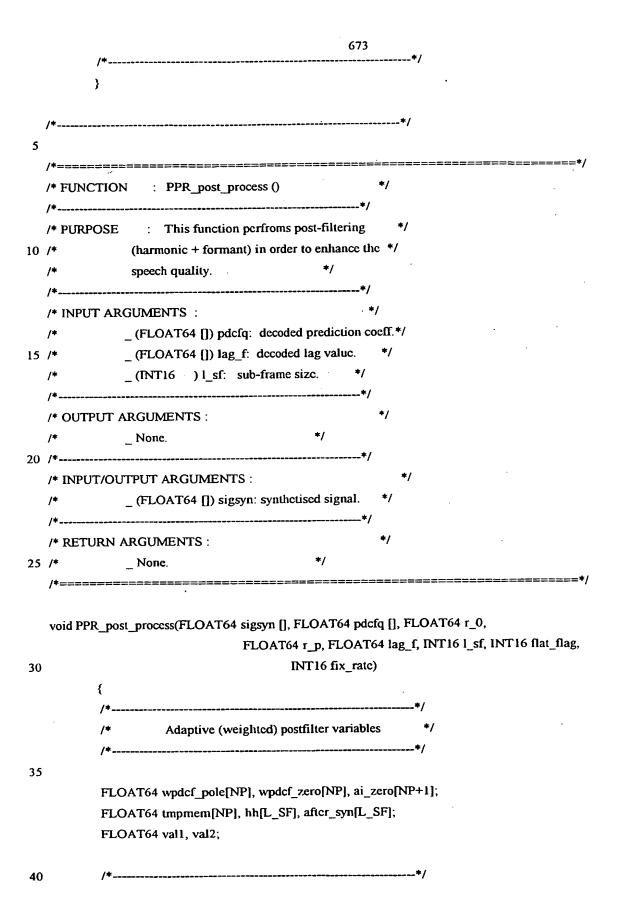
/*	669 */
/* INPUT ARGUMENTS :	*/
/* _(INT16) l_frm: speech frame size.	*/
/*5 /* OUTPUT ARGUMENTS :	*/ */
/*None. */	
/*	
/* INPUT/OUTPUT ARGUMENTS :	*/
/*(FLOAT64 []) x : input/output speech	
/* RETURN ARGUMENTS :	*/
/* _ None. */	
/*	
5 void PPR_highpass(INT16 l_frm, FLOAT64 x[]) {	
/*	*/
,	
PPR_ZeroPolcFilter (l_frm, x, pre_flt_buf_	z, prc_fit_buf_p,
)	prc_flt_num, pre_flt_den, pre_gain,
	PPR_FLT_ORDER);
/*	*/
5 return;	
	*/
}	
0 /*	*/
J /	,
/*====================================	
/* FUNCTION : PPR_pitch_postfilter ()	*/
/*5 /* PURPOSE : This function perfroms the pit	
/* postfiltering in order to enhance the	
/* periodicity. */	-
/+	*/
/* INPUT ARGUMENTS :	*/
0 /* (FLOAT64) lag_f: decoded lag v	/alue. */

```
/*
                 _(FLOAT64 ) PSF: pitch gain scale factor.*/
                 _ (INT16 ) l_sf: sub-frame size.
   /* OUTPUT ARGUMENTS :
                 None.
   /* INPUT/OUTPUT ARGUMENTS:
                 _ (FLOAT64 []) ext: input excitation signal.*/
10 /* RETURN ARGUMENTS:
           PPR_pitch_postfilter(FLOAT64 ext [], FLOAT64 lag_f, FLOAT64 PSF, INT16 l_sf)
15
                            Rp[2*SINC_LIMIT+8], x, y, gain, pit_step, pit_tab[25],
            FLOAT64
                  -----tmp[L_SF]; --
            INT16 i, IP1, IP2, P1, P2, pit_idx, LowL, HighL, L_Rp;
                          Searching range definition
25
            pit_step = 1.0 / 8.0;
            Ρl
                    = 0;
            P2
                    = 24;
            pit_tab[0] = MAX(min_pit, lag_f-1.5);
30
            for (i = 1; i \le P2; i \leftrightarrow)
                    pit_tab[i] = pit_tab[i-1] + pit_step;
                    pit_tab[i] = MIN(HI_LAG2, pit_tab[i]);
35
                    }
            IP1 = (INT16)pit_tab[P1];
            IP2 = (INT16)(pit_tab[P2]+1.0);
            IP2 = MAX(IP1, IP2);
40
```

```
LowL = IP1 - SINC_LIMIT - 1;
           LowL = MAX(0, LowL);
           HighL = IP2 + SINC LIMIT + 1;
           L Rp = HighL - LowL + 1;
5
   #ifdcf VERBOSE
           if (L Rp \geq 2*SINC LIMIT + 8)
                   nrerror("Memory Error: L_Rp>=2*SINC_LIMIT+8 !!\n");
10 #cndif
                           Integer correlation
15
           cpy_dvector (ext, buff_LTpost+MAX_LAG, 0, 1_sf-1);
           dot_dvcctor (buff_LTpost+MAX_LAG-LowL+1, buff_LTpost+MAX_LAG-LowL+1, &y,
                                                  0, 1_sf-1);
           for (i = LowL; i \le HighL; i++)
20
                    dot dvector (cxt, buff_LTpost+MAX_LAG-i, &x, 0, 1_sf-1);
                   v += buff LTpost[MAX_LAG-i] * buff_LTpost[MAX_LAG-i];
                    y == buff_LTpost[MAX_LAG-i+l_sf]*buff_LTpost[MAX_LAG-i+l_sf];
                    Rp[i-LowL] = x / sqn(MAX(y, 0.1));
25
                           Fine index searching
30
           LTP FineIndex search (pit_tab, P1, P2, LowL, L_Rp, Rp, &pit_idx);
                              LTP excitation
35
            LTP excit const pitch (pit tab[pit_idx], MAX_LAG, buff_LTpost, l_sf,
                                                           tmp, 0);
40
```

```
672
                            · Update residual buffer
            PPP\_update\_LT\_mem~(MAX\_LAG, buff\_LTpost, l\_sf, ext);
5
                         Compute the harmonic filter coefficient
             dot_dvector (ext, tmp, &x, 0, l_sf-1);
10
             dot_dvcctor (tmp, tmp, &y, 0, 1_sf-1);
             gain = x / MAX(y, 0.001);
              gain = MIN(1.0, gain);
              gain*= PSF;
15
                            Perform the harmonic filtering
20-
              for (i = 0; i < l_sf; i++)
                      tmp[i] = ext[i] + gain*tmp[i];
25
                                                               */
                                    AGC
              dot dvector (ext, ext, &x, 0, 1_sf-1);
               dot_dvector (tmp, tmp, &y, 0, 1_sf-1);
 30
               gain=sqrt(x / MAX(y, 0.001));
               for (i = 0; i < l_sf; i++)
                       ext[i] = tmp[i]*gain;
 35
               return;
  40
```

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```
674
                         Harmonic Filtering variables
           INT16 i, Lh, L2;
            FLOAT64 gain, numerator, denominator;
5
            FLOAT64 residu_w[L_SF];
                                                                */
                          Tilt compensation variables
10
            FLOAT64 r0, r1, k1, coeff, after_tilt[L_SF];
                        Compute the weighted LPC coefficients
15
            r zero = 0.75*r_zero + 0.25*r_0;
            -val1 = 1.0;
-20-
             val2 = 1.0;
             for (i = 0; i < NP; i++)
                     vall *= r_pole;
                     val2 *= r_zero;
 25
                     wpdcf_pole[i] = pdcfq[i] * vall;
                     wpdcf_zero[i] = pdcfq[i] * val2;
                     }
 30
                         Compute the residual signal
             LPC_ptoa (wpdcf_zero, ai_zero, NP);
             FLT_conv (sigsyn, ai_zero, NP+1, l_sf, residu_w);
 35
                                                                */
                              Update the memory
 40
```

```
675
           cpy_dvector(sigsyn+1_sf, sigsyn, 0, NP-1);
                           Pitch post-filter
            /*____*/
5
           if (1 sf == L_SF)
                   {
                   L2 = L_SF/2;
10
                   for (i = 0; i < N_SF2; i++)
                           PPR_pitch_postfilter (residu_w+i*L2, lag_f, r_p, L2);
                   }
           else
                   PPR_pitch_postfilter (residu_w, lag_f, r_p, l_sf);
15
                             Tilt compensation
20
                  Compute the impulse response of the zero-pole filter
            Lh = 22;
25
            ini_dvector (hh, 0, 1_sf-1, 0.0);
            LPC_ptoa (wpdcf_zero, hh, NP);
            ini_dvector (tmpmem, 0, NP-1, 0.0);
            FLT_allsyn(hh, (INT16)(Lh+1), wpdcf_pole, NP, hh, tmpmem);
30
                      Compute the first reflection coefficient
            dot_dvector (hh, hh, &r0, 0, Lh-1);
35
            dot_dvector (hh, hh+1, &r1, 0, Lh-2);
            k1 = -r1/MAX(r0, 0.0001);
        coeff = MAX(MTN(0.5*k1, 0.0), -0.1);
40
            if (k1 < 0.0)
```

```
676
                     if (flat_flag == 0)
                             coeff = 0.1;
                     else
                             coeff = 0.05;
5
                     }
                           Perform the tilt compensation
10
             after_tilt[0] = residu_w[0] + coeff*pst_hp_mem;
             for (i = 1; i < l_sf; i++)
                     after\_tilt[i] = residu\_w[i] + coeff*residu\_w[i-1];
15
                                Update the memory
20
              pst_hp_mem = residu_w[l_sf-1];
                                Synthesis filter
 25
              FLT_allsyn (after_tilt, l_sf, wpdcf_polc, NP, after_syn, PF_mem_syn);
 30
                                Adaptive gain control
  35
                         Compute the gain factor for the current subframe
               dot_dvector (sigsyn+NP, sigsyn+NP, &numerator, 0, 1_sf-1);
               dot_dvector (after_syn, after_syn, &denominator, 0, 1_sf-1);
  40
```

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```
gain = sqrt(numerator / MAX(denominator, 0.001));
            if (flat_flag == 0)
 5
                    gain *= 1.1;
            else
                    gain *= 1.15;
10
                            Perform the gain control
            for (i = 0; i < l_sf; i++)
                    pst_scale = 0.9*pst_scale + 0.1*gain;
15
                    after_syn[i] *= pst_scale;
                            High-pass filtering
20
            PPR_post_highpass (l_sf, after_syn);
            cpy_dvector (after_syn, sigsyn+NP, 0, 1_sf-1);
25
            return;
30
35
   /* FUNCTION : PPR_ZeroPoleFilter ()
   /* PURPOSE : This function perfroms the pole and zero */
                                                 */
40 /*
                  filtering.
```

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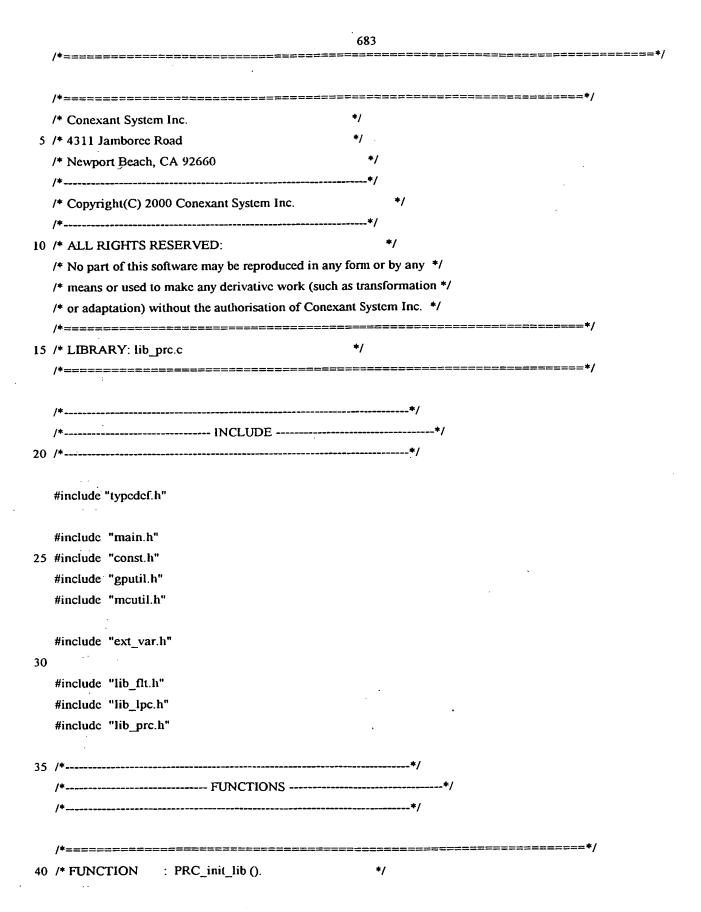
```
678
  /* INPUT ARGUMENTS :
                                                   */
               _(INT16 ) Len: signal size.
               _(FLOAT64 []) Zz : zero filter memory.
               _(FLOAT64 []) Zp : pole filter memory.
              __ (FLOAT64 []) b : numerator coefficients. */
               _(FLOAT64 []) a : denominator coefficients.*/
                                                    */
               _(FLOAT64 ) K : filter gain.
                                                   */
               _(INT16 ) order: filter order.
  /* OUTPUT ARGUMENTS:
   /*
                Nonc.
   /* INPUT/OUTPUT ARGUMENTS:
                _(FLOAT64 []) InOut: input output signal. */
15 /*
   /* RETURN ARGUMENTS:
                None.
20
   void PPR_ZcroPolcFilter (INT16 Len, FLOAT64 InOut[], FLOAT64 Zz[], FLOAT64 Zp[],
                                    FLOAT64 b[], FLOAT64 a[], FLOAT64 K, INT16 order)
            {
 25
             INT16 i, k;
             FLOAT64 S;
 30
             for (i = 0; i < Len; i++)
                     S = InOut[i];
                     for (k = 0; k < order; k++)
                             S += b[k]*Zz[k];
 35
                      for (k = order-1; k > 0; k--)
                             Z_{Z}[k] = Z_{Z}[k-1];
                      Z_{z}[0] = InOut[i];
                      InOut[i] = S;
  40
```

```
for (i = 0; i < Len; i++)
5
                 S = InOut[i];
                 for (k = 0; k < order; k++)
                       S = a[k]*Zp[k];
                 for (k = order-1; k > 0; k--)
                       Zp[k] = Zp[k-1];
10
                 Zp[0] = S;
                 InOut[i] = S*K;
                 }
15
          rcturn;
20
   25 /* FUNCTION : PPR_post_highpass ()
   /* PURPOSE : This function perfroms G.729 high pass
               filtering.
30 /* INPUT ARGUMENTS:
              _ (INT16 ) Len: signal size.
   /*
   /* OUTPUT ARGUMENTS:
             _ None.
                                          */
   /* INPUT/OUTPUT ARGUMENTS:
              _ (FLOAT64 []) InOut: input output signal.
   /* RETURN ARGUMENTS:
                                          */
            _ None.
40 /*
```

	680
/ * ===	======================================
	PPR_post_highpass (INT16 Len, FLOAT64 InOut[])
voia i	PR post_inghpass (invite zers)
	{ _/**/
5	/*
	PPR_ZeroPoleFilter(Len, InOut, pst_flt_buf_z, pst_flt_buf_p,
	pst_flt_num, pst_flt_den, pst_gain,
	PPR_FLT_ORDER);
10	/**/
	/*
	return;
	*/
15	/**/
	}
	· · · · · · · · · · · · · · · · · · ·
/*	*/
20-/*=	
	*/

681 */ /* Conexant System Inc. */ 5 /* 4311 Jamboree Road /* Newport Beach, CA 92660 /+____*/ /* Copyright(C) 2000 Conexant System Inc. /*----*/ 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ 15 /* PROTOYPE FILE: lib ppr.h /*______*/ /*____*/ /*----*/ 20 /*----*/ void PPR filters init (void); 25 void PPR_silence_enhan_init (void); (FLOAT64 [], FLOAT64 [], INT16); PPR_silence_enhan void PPR_ZeroPoleFilter (INT16, FLOAT64 [], FLOAT64 [], FLOAT64 [], 30 void FLOAT64 [],FLOAT64 [],FLOAT64, INT16); (FLOAT64 [], INT16, INT16, INT16); void PPR_lowpass (INT16, FLOAT64 []); 35 void PPR highpass (INT16, FLOAT64 []); void PPR post highpass 40

void	PPR_post_process	682 (FLOAT64 [], FLOAT64 [], FLOAT64, FLOAT64,		
VOIG	TTT_post_process	FLOAT64, INT16, INT16, INT16);		
void	PPR_pitch_postfilter	(FLOAT64 [], FLOAT64, FLOAT64, INT16);		
/*===	., ====================================			
/*/	/**/ /*=============================			
D				



/*						
	OSE : This function per			*/		
/ *	library global variables	. * <i>i</i>	1			
/*						
	ARGUMENTS :		*/			
	_ None.	*/	*/			
•	VER ADOLDAENTS .		*/			
	OUT ARGUMENTS : _ None.	*/				
	_ 110110.		*/			
	T/OUTPUT ARGUMENTS		*	1		
/*	_ None.	*/				
/*						
	JRN ARGUMENTS :		*/			
/*	_ None. ===========	*/			=======	=====*/
/*====						
Void 11	C_init_lib (void)					
	{					
)	/*					
	/* PRC_ol_cl	_GainNorm	*/			
)	/*	_GainNorm	*/			
/*	/*PRC_ol_cl	_GainNorm	*/			
/*	/* PRC_ol_cl	_GainNorm	*/			
/*	/*	_GainNorm	*/			·
/*	/*	_GainNorm	*/			
)* 5	/*	_GainNorm	*/	*/	-	
)* 5	/*	_GainNorm	*/	*/		
/* 5	/*	_GainNorm	*/	*/		,
/* 5	/*	_GainNorm	*/	*/		·
)* 5	/*	_GainNorm	*/	*/		
5	/*	_GainNorm	*/	*/		
0	/*	_GainNorm	*/	*/		
5	/*	_GainNorm	*/	*/		

```
685
  /* PURPOSE
                 : This function performs the Mode 0 energy
  /*
               manipulation.
5 /* INPUT ARGUMENTS:
           _(FLOAT64 ) nsr: Noise to Signal Ratio.
           _ (FLOAT64 ) sub_eng: sub-frame energy.
                                excitation signal.
           _(FLOAT64 []) res:
           _ (FLOAT64 **) unfcod: excitation vectors (ALG and */
   /*
                                      LTP). */
10 /*
                                 filtered excitation vectors */
           _(FLOAT64 **) fcod:
   /*
                               (ALG and LTP).
           _ (FLOAT64 []) Tgs: target signal.
           (INT16 ) exc_mode: excitation mode.
           _ (FLOAT64 ) beta_sub: smoothing factor.
15 /*
           (INT16 ) 1 sf: sub-frame size.
   /* OUTPUT ARGUMENTS:
                                             */
                None.
   /* INPUT/OUTPUT ARGUMENTS:
           _ (FLOAT64 []) gainQ: quantizied gains.
   /* RETURN ARGUMENTS:
25 /*
          PRC_GainsNorm_Gc_Gp (FLOAT64 nsr, FLOAT64 sub_eng, FLOAT64 res[],
   void
                                                 FLOAT64 gainQ [], FLOAT64 **unfcod,
                                                        FLOAT64 **fcod, FLOAT64 Tgs [],
30
                                                                INT16 exc_mode, FLOAT64 bcta_sub,
                                                                        INT16 l_sf)
35
                        lpc_gain, ol_g, cl_g, gain, x, y, E_ex, E_Tgs, Temp[L_SF];
           FLOAT64
           if (nsr > 0.125)
40
```

```
686
                                                            */
                                     Energy
5
                    E_ex = sub_eng / L_SF;
                    dot_dvector(Tgs, Tgs, &E_Tgs, 0, I_sf-1);
                    E_Tgs = 1_sf;
10
                                    Energy smooth
                     if (cl_Eg < 0)
                             {
15
                             ol Eg = E_ex;
                             cl_Eg = E_Tgs;
                     else
 20
                              ol_Eg = bcta_sub*ol_Eg + (1.0-beta_sub)*E_ex;
                              cl_Eg = beta_sub*cl_Eg + (1.0-beta_sub)*E_Tgs;
                              }
 25
                                     Open-loop gain
  30
                      wad_dvector (unfcod[0], gainQ[0], unfcod[1], gainQ[1],
                                                                       Temp, 0, l_sf-1);
                       dot_dvector (Temp, Temp, &x, 0, 1_sf-1);
  35
                       x /= l_sf;
                       x = sqrt(MAX(ol_Eg-5.0, 0)/MAX(x, 0.1));
                       gain = MIN(0.75*x, (1.2 / MAX(gainQ[0], 0.01)));
                       }
   40
```

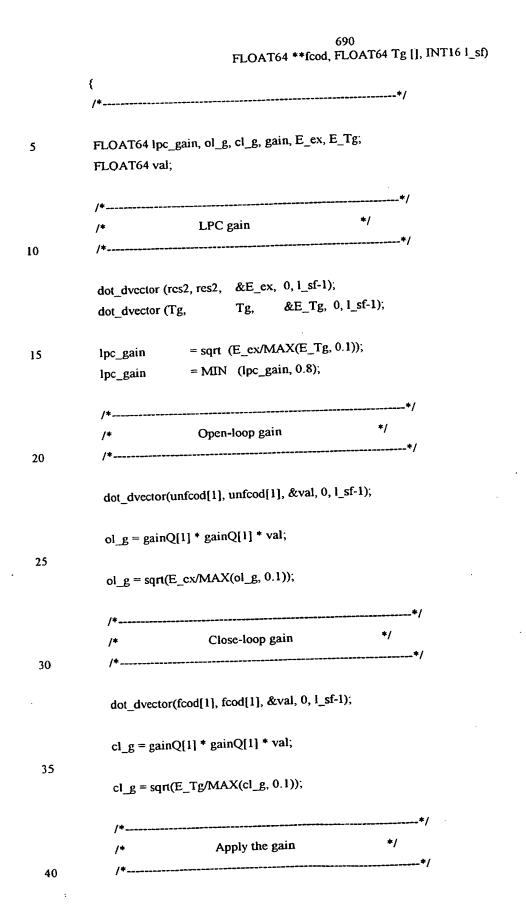
```
687
            else
                    {
                                                               */
                                     LPC gain
 5
                    dot_dvector (rcs, rcs, &E_ex, 0, l_sf-1);
                    E_ex = l_sf;
                     dot_dvector (Tgs, Tgs, &E_Tgs, 0, l_sf-1);
10
                     E_Tgs /= I_sf;
                                     = sqrt(E_cx / MAX(E_Tgs, 0.1));
                     lpc_gain
                                     = MIN(lpc_gain, m_lpc_gain);
                     lpc_gain
15
                                    Energy smooth
                     if (cl_Eg < 0)
20
                             ol_Eg = E_ex;
                             cl_Eg = E_Tgs;
                             }
25 ·
                     eise
                             {
                             ol Eg = beta_sub*ol_Eg + (1.0-beta_sub)*E_ex;
                             cl_Eg = beta_sub*cl_Eg + (1.0-beta_sub)*E_Tgs;
                             }
30
                                    Open-loop gain
35
                     wad_dvector (unfcod[0], gainQ[0], unfcod[1], gainQ[1],
                                                                       Temp, 0, 1_sf-1);
```

dot_dvector (Temp, Temp, &x, 0, 1_sf-1);

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```
x = I_sf;
                    ol_g = sqrt(MAX(ol_Eg-5.0, 0) / MAX(x, 0.1));
5
                                   Close-loop gain
10
                     wad_dvector (fcod[0], gainQ[0], fcod[1], gainQ[1],
                                                                       Temp, 0, 1_sf-1);
                     dot_dvector (Temp, Temp, &y, 0, 1_sf-1);
15
                      y = l_sf;
                      cl_g = sqn(MAX(cl_Eg-5.0, 0) / MAX(y, 0.1));
 20
                                     Apply the gain
                       if(exc_mode != 0)
 25
                               x = lpc_gain / MAX(m_lpc_gain, 0.1);
                               y = 1.0 - x;
                               gain = x*ol_g + y*cl_g;
                               gain = MIN(0.75*gain, 1.2/MAX(gainQ[0], 0.01));
                                gain = MIN((1 + lpc_gain), gain);
  30
                               }
                        elsc
                                gain = 0.75*MIN(ol\_g, cl\_g);
                                gain = MIN(gain, 1.2/MAX(gainQ[0], 0.01));
 . 35
                                }
                        }
   40
```

```
689
          gain = MAX(1.0, gain);
          gainQ[0] *= gain;
          gainQ[1] *= gain;
5
          rcturn;
10
15 /* FUNCTION : PRC_ol_cl_GainNorm_Gc ().
   /*----*/
  /* PURPOSE : This function performs the Mode 1 energy
            manipulation.
20 /* INPUT ARGUMENTS:
          _ (FLOAT64 []) res2: excitation signal.
          _(FLOAT64 **) unfcod: excitation vectors (ALG and */
                                  LTP). */
          _(FLOAT64 **) fcod: filtered excitation vectors */
                            (ALG and LTP). */
25 /*
          _ (FLOAT64 []) Tg:
                             target signal.
          _(INT16 ) exc_mode: excitation mode.
          _(INT16 ) l_sf: sub-frame size.
30 /* OUTPUT ARGUMENTS:
   /*
                                         */
              _ None.
   /* INPUT/OUTPUT ARGUMENTS:
          _ (FLOAT64 []) gainQ: quantizied gains.
35 /*------
   /* RETURN ARGUMENTS:
              _ None.
40 void PRC_GainNorm_Gc (FLOAT64 res2[], FLOAT64 gainQ [], FLOAT64 **unfcod,
```



```
lpc_gain
                        0.75;
                        lpc gain*ol_g + (1-lpc_gain)*cl_g;
        gain
                        MAX(1.0, 0.5*gain);
        gain
                        MIN(1+lpc_gain, gain);
5
        gain
                        gain;
        gainQ[1]
10
        return;
  /* FUNCTION : PRC_Ideal_Excit().
  /*____*/
20 /* PURPOSE : This function calculates the ideal fixed
           codebook excitation.
  /* INPUT ARGUMENTS :
       _(FLOAT64 []) Tg: target signal.
       _(FLOAT64 []) pdcfq: quantized prediction coeff. */
       _(FLOAT64 []) wpdcf_dcn: precetual filter denominator */
                  coefficients.
       _(FLOAT64 []) wpdcf_num: precetual filter numerator */
                  coefficients.
30 /*
       _ (lNT16 ) l_sf: sub-frame size.
  /* OUTPUT ARGUMENTS :
       _ (FLOAT64 []) rcs2: ideal excitation signal.
35 /* INPUT/OUTPUT ARGUMENTS:
  /* RETURN ARGUMENTS:
```

```
void PRC_Ideal_Excit(FLOAT64 Tg [], FLOAT64 pdcfq [], FLOAT64 wpdcf_den [],
                        FLOAT64 wpdcf_num [], FLOAT64 rcs2 [], INT16 l_sf)
          {
5
          FLOAT64 ai_zero[NP+1], tmpmem[NP+1], tmp[L_SF+NP];
10
           ini_dvector (tmp, 0, NP-1, 0.0);
           cpy_dvector (Tg, tmp+NP, 0, l_sf-1);
                         (pdcfq, ai_zero, NP);
           LPC ptoa
                         (tmp, ai_zcro, NP+1, l_sf, res2);
           FLT_conv
15
           ini_dvector (tmpmcm, 0, NP-1, 0.0);
           FLT_allsyn (res2, l_sf, wpdcf_num, NP, tmp+NP, tmpmcm);
            LPC_ptoa (wpdcf_den, ai_zero, NP);
20
            FLT_conv (tmp, ai_zero, NP+1, l_sf, res2);
            return;
 25
            }
     /* FUNCTION : PRC_TargetSignal ().
  35 /* PURPOSE : This function calculates the target signal.
     /* INPUT ARGUMENTS:
            _ (FLOAT64 []) wpdcf_num: precetual filter numerator */
                           coefficients.
      /*
            _(FLOAT64 []) wpdcf_den: precetual filter denominator */
   40 /*
```

```
693
                         coefficients.
          _ (FLOAT64 []) pdcfq: quantized prediction coeff. */
          _ (FLOAT64 []) OriSig: original signal
          _(FLOAT64 []) dif_mcm: memory for synthesis filter */
          _(INT16 ) target_mcm: memory for taget synthesis.
          _(INT16 ) l_sf: sub-frame size.
   /* OUTPUT ARGUMENTS :
          _(FLOAT64 []) rcs: residual signal.
10 /*
          _(FLOAT64 []) Tgs: target signal.
   /* INPUT/OUTPUT ARGUMENTS:
                                              */
                None.
15 /* RETURN ARGUMENTS:
                _ None.
           PRC_TargetSignal(FLOAT64 wpdcf_num [], FLOAT64 wpdcf_den [],
   void
                           FLOAT64 pdcfq [], FLOAT64 OriSig[], FLOAT64 res[],
20
                           FLOAT64 Tgs [], FLOAT64 dif_mem [],
                           FLOAT64 target_mem [], INT16 1_sf)
25
            FLOAT64 ai_zcro[NP+1], tmpmem[NP];
            /* The residual signal is located at ext+MAX_LAG for use in the */
                         close-loop pitch search
30
            LPC_ptoa (pdcfq, ai_zero, NP);
            FLT conv (OriSig, ai_zero, NP+1, 1_sf, res);
35
                  Filter the residual signal with the synthesis filter
                     NOTES: reverse the order of the memory
40
```

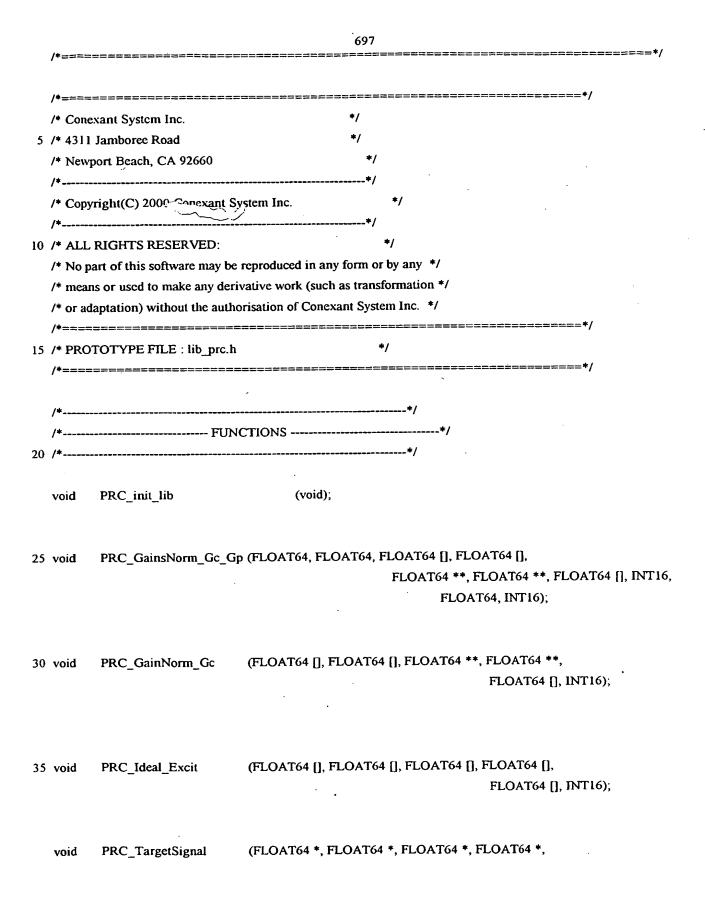
	rev_dvector (dif_mem, tmpmem, 0, NP-1);	
	FLT_allsyn(res, I_sf, pdcfq, NP, dif_mem+NP, tmpmcm)	· •
5	/*	*/
	/* Perceptual weighting filter */	•
	/*	. */
	LPC_ptoa (wpdcf_num, ai_zero, NP);	
10	<pre>FLT_conv (dif_mem, ai_zero, NP+1, l_sf, Tgs);</pre>	
	/*	
	/* NOTES: reverse the order of the memory	*/
	/*	- */
15		•
	rev_dvector (target_mem, tmpmem, 0, NP-1);	
	FLT_allsyn (Tgs, l_sf, wpdcf_den, NP, Tgs, tmpmem);	
	·	* /
	/*	'/
20		
	retum;	
	/*/*	·*/
		•
	}	
25	/*	*/
	/*	
	/*=====================================	- ====================================
	/* FUNCTION : PRC_average_rate (). */	
30	/**/	
30	/* PURPOSE : This function calculates the SMV average	*/
	/* bit rate. */	
	/**/	
	/* INPUT ARGUMENTS : */	
35	/* _(INT16) codec_rate: current frame bitrate. */	
,	/**/	
	/* OUTPUT ARGUMENTS : */	
	/* None. */	
	/**/	
40	/* INPUT/OUTPUT ARGUMENTS :	*/

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```
(FLOAT64 *) avg_rate : average bit rate.
  /* RETURN ARGUMENTS :
               None.
                          ______
   void PRC_average_rate (INT16 codec_rate, FLOAT64 *avg_rate)
10
          INT16 bits = 0;
                  Set bits incl. overhead in order to measure the
                        average SMV bit-rate
15
           switch (codec_rate)
                  {
                  case RATE8_5K: bits = RATE_1_1;
20
                         break;
                  case RATE4_0K: bits = RATE_1_2;
                         break;
25
                  case RATE2_0K: bits = RATE_1_4;
                         break;
                  case RATE0_8K: bits = RATE_1_8;
30
                         break;
                  default: nrerror ("Invalid codec rate !!!\n");
                         break;
                  }
35
           (*avg\_rate) = ((*avg\_rate)*(FLOAT64)(frm\_count-1) + (FLOAT64)bits)/(double)frm\_count;
40
```

WO 01/22402

	return;
	/**/
5	}
	/**/
	/**
10	/**/ /**



699 /* Conexant System Inc. 5 /* 4311 Jamboree Road /* Newport Bcach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of France Telecom and */ /* Conexant System Inc. */ /* LIBRARY: lib_pwf.c /*_____*/ 20 /*----*/ #include "main.h" #include "typedef.h" 25 #include "const.h" #include "gputil.h" #include "ext_var.h" 30 #include "lib_lpc.h" #include "lib_flt.h" #ifdef DIAG_SMV 35 #include "lib_dia.h" #endif 40 /*----*/

/* FU	NCTION : PW	VF_init_lib ().		*/		
/* PU	RPOSE : This	s function initialis	se the global	variables	*/	
/*	of the PW	<u>-</u>	*/			
-				*/ `		
	PUT ARGUMENT	'S :		*/		
	_ None.		*/			
/*				*/		
/* OL	TTPUT ARGUMEN	YTS:		*/		
	_ None.		*/			
/*				*/		
/* IN	PUT/OUTPUT AR	GUMENTS:			*/	
	_ None.		*/			
/*				*/		
/* RE	TURN ARGUMEN	NTS:		*/		
/ * ===	None.	:	*/			
/ * ===	PWF_init_lib (void)				
/*===	PWF_init_lib (void { /*)			*/	
/*===	PWF_init_lib (void { /*	wspeech_to_sp			*/ */	
/*===	PWF_init_lib (void { /*)			*/ */	.=====
/*===	PWF_init_lib (void { /*/* /*	wspeech_to_sp			*/ */	
/*===	PWF_init_lib (void { /* /* /* r_pole_ws = 0.	wspeech_to_sp			*/ */	.=====
/*===	PWF_init_lib (void { /*	wspeech_to_sp			*/ */	
/*===	PWF_init_lib (void { /* /* /* r_pole_ws = 0.	wspeech_to_sp			*/ */	
/*===	PWF_init_lib (void) { /*	wspeech_to_sp	eech		*/ */ */	
/*===	PWF_init_lib (void) { /*	wspeech_to_sp	eech		*/ */ */	
/*===	PWF_init_lib (void { /*	wspeech_to_sp	eech		*/ */ */	
/*===	PWF_init_lib (void) { /*	wspeech_to_sp	eech		*/ */ */	
/*===	PWF_init_lib (void { /*	wspeech_to_sp	peech		*/ */ */	
/ * ===	PWF_init_lib (void { /*	wspeech_to_sp	peech		*/ */ */	

```
701
   /* FUNCTION : PWF_speech_to_rcsidu ().
   /*____*/
   /* PURPOSE : This function perform a filtering operation to */
             pass from the input signal domain to the
 5 /*
            perceptually weighted domain residual.
   /* INPUT ARGUMENTS:
      _(FLOAT64 []) sigpp:
                           input signal.
      _(FLOAT64 **) pdcf:
                           prediction coeff. matrix.
10 /*
      _(FLOAT64 ) r_zero:
                           band-expantion oefficient
      /* OUTPUT ARGUMENTS:
      _(FLOAT64 []) residu:
                           perceptually weighhed domain
15 /*
                    residual.
      _(FLOAT64 **) wpdcf_zcro: band-expanded prediction coeff. */
                    matrix.
  /* INPUT/OUTPUT ARGUMENTS:
20 /* None.
  /* RETURN ARGUMENTS:
   /* Nonc.
                                        */
25
  void PWF_speech_to_residu (FLOAT64 sigpp[], FLOAT64 residu[], FLOAT64 **pdcf,
                                    FLOAT64 **wpdcf zero, FLOAT64 r zero)
                   <u>____</u>*/
30
          INT16 i, j, i_s;
          FLOAT64 ai_zero[NP+1], val;
          /*____*/
              Generate the perceptual weighting filter zeros coefficients */
35
          for (i = 0; i \le N SF4; i++)
                val = 1.0;
40
```

```
702
                    for (j = 0; j < NP; j++)
                            vai *= r_zero;
                            wpdcf_zero[i][j] = pdcf[i][j]*val;
 5
                   }
                         Generate the weighted residual
10
            i_s = 0;
            for (i = 0; i < N_SF4; i++)
15
                    LPC_ptoa (wpdcf_zero[i], ai_zcro, NP);
                 FLT_conv(sigpp+L_PP-L_FRM-L_LPCLHD+i_s-NP, ai_zero, NP+1, L_SF4,
                                                            residu+L\_OLPIT-L\_FRM-L\_LPCLHD+i\_s);
20
                    i_s += L_SF4;
25
            LPC_ptoa(wpdcf_zero[i], ai_zero, NP);
            FLT_conv(sigpp + L_PP - L_LPCLHD - NP, ai_zero, NP+1, L_LPCLHD,
                                                                                    residu + L_OLPIT -
30 L_LPCLHD);
            rcturn;
35
40
```

```
703
   /* FUNCTION : PWF residu to wspeech ().
   /* PURPOSE : This function perform a filtering operation to */
               pass from the perceptually weighted domain
              residual to the perceptually weighted domain
               signal.
   /* INPUT ARGUMENTS:
       _(INT16 ) flat_flag: flat input speech flag.
        _(FLOAT64 []) residu:
                                 residual signal.
        _(FLOAT64 **) refl:
                                partial corr. coeff. matrix.
        _(FLOAT64) r_zero:
                                 band-expantion ocfficient
15 /* OUTPUT ARGUMENTS:
                                  perceptually weighhed domain
   /*
        _ (FLOAT64 []) wspeech:
                                              */
   /*
                        signal.
                                 prediction coeff. matrix.
        _(FLOAT64 **) pdcf:
        _(FLOAT64 **) wpdcf_pole: band-expanded prediction coeff. */
20 /*
                        matrix.
                                  first adaptive order lowpass
        _(FLOAT64 []) LP_CF:
                                              */
                        coefficient.
   /* INPUT/OUTPUT ARGUMENTS:
        (FLOAT64 []) wspeech_mem: synthesis filter memory.
        _(FLOAT64 *) Z1_ws:
                                  first adaptive order lowpass
                        memory.
   /* RETURN ARGUMENTS:
        None.
   void PWF_residu_to_wspeech (INT16 flat_flag, FLOAT64 residu[],
                                           FLOAT64 wspecch[],FLOAT64 **refl, FLOAT64 **pdcf,
                                            FLOAT64 **wpdcf pole, FLOAT64 *LP_CF,
35
                                                  FLOAT64 wspeech mem[], FLOAT64 *Z1_ws)
            INT16 i, k, j, i_s, i0;
40
```

```
FLOAT64 val, x, y, tmpmcm[NP];
                Generate the perceptual weighting filter poles coefficients */
 5
             for (i = 0; i \le N_SF4; i++)
                     {
                     val = 1.0;
10
                     for (j = 0; j < NP; j++)
                              val *= 0.5;
                              wpdcf_pole[i][j] = pdcf[i][j]*val;
15
                     }
             if (flat_flag==0)
                    y = 0.2;
20
             else
                     y = 0.0;
25
             for (i = 0; i \le N_SF4; i++)
             LP_CF[i] = y;
30
             /* Generate the weighted speech for openloop pitch lag search */
             i_s = 0;
             for (i = 0; i < N_SF4; i++)
35
                     FLT\_allsyn \ (residu + L\_OLPIT - L\_FRM - L\_LPCLHD + i\_s, L\_SF4,
                                               wpdcf_pole[i], NP, wspeech + L_OLPIT - L_FRM -
                                                       L_LPCLHD + i_s, wspeech_mem);
                      i_s += L_SF4;
40
```

```
cpy_dvector (wspeech_mem, tmpmem, 0, NP-1);
           FLT\_allsyn \ (residu + L\_OLPIT - L\_LPCLHD, \ L\_LPCLHD, \ wpdcf\_pole[i], \ NP,
                                                                           wspeech+L_OLPIT-L_LPCLHD,
5
   tmpmem);
                           Adaptive low pass
10
            i_s = 0;
            for (i = 0; i < N_SF4; i++)
15
                    for (k = 0; k < L_SF4; k++)
                            i0 = L\_OLPIT - L\_FRM - L\_LPCLHD + i\_s;
                            x = wspeech[i0 + k] + LP_CF[i]*(*Z1_ws);
                            (*Z1_ws) = wspeech[i0 + k];
20
                            wspeech[i0 + k] = x;
                    i_s += L_SF4;
25
            y = (*Z1_ws);
            for (i = 0; i < L\_LPCLHD; i++)
                    x = wspeech[L_OLPIT-L_LPCLHD+i] + LP_CF[N_SF4]*y;
                    y = wspecch[L_OLPIT-L_LPCLHD+i];
                    wspeech[L_OLPIT-L_LPCLHD+i] = x;
30
                    }
35
            return;
```

```
*/
  /* FUNCTION : PWF_wspeech_to_speech ().
5 /* PURPOSE : This function perform a filtering operation to */
            pass from the perceptually weighted domain
            signal to the signal.
  /* INPUT ARGUMENTS:
10 /* _(FLOAT64 **) wpdcf_zero: band-expanded prediction coeff. */
                    matrix numarator.
   /*
      _(FLOAT64 **) wpdcf_pole: band-expanded prediction coeff. */
                    matrix denominator.
   /*
                            first adaptive order lowpass */
      __(FLOAT64 []) LP_CF:
                                      */
15 /*
                    coefficient.
   /* OUTPUT ARGUMENTS :
   /* _ (FLOAT64 []) ModiSig: _ pitch preprocessed signal.
   /*-----
20 /* INPUT/OUTPUT ARGUMENTS:
   /* _ (FLOAT64 *) Z1_ws_2: first adaptive order lowpass */
                                       */
                   memory.
   /* RETURN ARGUMENTS :
25 /* None.
   void PWF_wspeech_to_speech (FLOAT64 ModiSig[], FLOAT64 **wpdcf_zero,
                                          FLOAT64 **wpdcf_pole, FLOAT64 LP_CF [],
                                                FLOAT64 *Z1_ws_2)
 30
           FLOAT64 tmp_ws[L_FRM+NP], ai_zero[NP+1];
           INT16 i, k, i_s;
 35
           cpy_dvcctor (tmp_ws_m, tmp_ws, 0, NP-1);
           i_s = 0;
 40
```

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```
707
            for (i = 0; i < N_SF4; i++)
                     for (k = 0; k < L_SF4; k++)
                             tmp_ws[NP + i_s + k] = ModiSig[i_s + k] -
 5
                                                                                        LP_CF[i]*(*Z1_ws_2);
                             (*Z1_ws_2) = tmp_ws[NP + i_s + k];
                             i_s += L_SF4;
10
                    }
            cpy_dvector(tmp_ws+L_FRM, tmp_ws_m, 0, NP-1);
15
            i_s = 0;
            for (i=0; i<N_SF4; i++)
                     LPC_ptoa (wpdcf_pole[i], ai_zero, NP);
                     FLT_conv (tmp_ws+i_s, ai_zero, NP+1, L_SF4, ModiSig+i_s);
20
                     FLT\_allsyn \ (ModiSig+i\_s, \ L\_SF4, \ wpdcf\_zcro[i], \ NP, \ ModiSig+i\_s,
                                                       ModiSig_m);
                     i_s += L_SF4;
25
             return;
30
35
```

		708	-*/
			-· <i>,</i>
	/+=====================================	*/	
	/* Conexant System Inc.	*/	
5	/* 4311 Jamboree Road	*/	
	/* Newport Beach, CA 92660	*/	
	/+		
	/* Copyright(C) 2000 Conexant Sys	stem Inc. */	
	/*	*/	
10	/* ALL RIGHTS RESERVED:	*/	
	/* No part of this software may be r	eproduced in any form or by any */	
		ative work (such as transformation */	
	/* or adaptation) without the author		
	/* Conexant System Inc.	*/	
15	/*		
	/* PROTOTYPE FILE : lib_pwf.h	*/	
	/*	*/	
	/* IMPLEMENTATION :	[26/05/2000] */	
	/* Conexant System Inc.	*/	
20	/* Media Access Department	*/	
	/* Speech Technology Developmer	*/	
	/*		
	/* Last Version	[26/05/2000] */	
	/* C. Murgia	*/	
25	/*	*/	
23	/* PURPOSE : Perceptuel Weight		
	· ·	*/	
	,		
	/*	*/	
30	/*FIN	CTIONS*/	
30	/*		
	,	·	
	and DUF into the	(void);	
	void PWF_init_lib	(void),	
25	void PWF_specch_to_residu	(FLOAT64 ∏, FLOAT64 ∏, FLOAT64 **, FLOAT64 **,	
رر	void i wi _specen_to_resida	(Leonton II), Leonton III	
	FLOA	Γ64);	
	void PWF_residu_to_wspeech	(INT16, FLOAT64 [], FLOAT64 [], FLOAT64 **,	
40		FLOAT64 **, FLOAT64 **, FLOAT64	Π.
40	•	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	٠.

709 FLOAT64 *);

•	void PWF_wspccch_to_speech (FLOAT64 [], FLOAT64 **, FLOAT64 **, FLOAT64 [],
5	FLOAT64 *);
	/*====================================
	/**/
	/*====================================
10	

	710
*======================================	
/*=====================================	
/* Conexant System Inc.	*/
/* 4311 Jamborce Road	*/
/* Newport Beach, CA 92660	*/
/*	•
/* Copyright(C) 2000 Conexant System Inc.	*/
/*	
/* ALL RIGHTS RESERVED:	*/
/* No part of this software may be reproduced in a	
/* means or used to make any derivative work (suc	ch as transformation */
/* or adaptation) without the authorisation of Con-	•
/*====================================	*/
/*====================================	•
, 	
/*	*/
/* INCLUDE	•
/*	
	 ,
#include "typedef.h"	
#include "main.h"	
#include "const.h"	
#include "gputil.h"	
#include "mcutil.h"	
#include "ext_var.h"	
. -	
#include "lib_qlsf.h"	
/*	*/
/* FUNCTIONS	*/
/*	
•	
/*	
/* FUNCTION : LSF_Q_init_lib ().	*/
/*	·
•	
/* PURPOSE : This function initialise the gl	
/* for the LSF_Q library.	*/

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```
711
  /* INPUT ARGUMENTS:
                                           */
              None.
5 /* OUTPUT ARGUMENTS:
                                           */
            _ None.
  /* INPUT/OUTPUT ARGUMENTS:
               None.
10 /*----
  /* RETURN ARGUMENTS:
15 void LSF_Q_init_lib (void)
          INT16 i, j, k;
20
        MS_08k[0] = LMS1_08k;
        MS_08k[1] = LMS2_08k;
        MS_08k[2] = LMS3_08k;
25
        MS_{40k}[0] = LMS1_{40k};
        MS_40k[1] = LMS2_40k;
        MS_{40k[2]} = LMS3_{40k};
30
        MS_85k[0] = LMS1_85k;
        MS_85k[1] = LMS2_85k;
        MS_85k[2] = LMS3_85k;
        MS_85k[3] = LMS4_85k;
35
           last_qlsf[0] = 0.049793355045123;
           last_qlsf[1] = 0.071543733408561;
           last_qlsf[2] = 0.115002634382656;
40
```

```
712
            last_qlsf[3] = 0.164845365833111;
            last_qlsf[4] = 0.211800568342986;
            last_qlsf[5] = 0.252341845504398;
            last_qlsf[6] = 0.303571730115036;
 5
            last_qlsf[7] = 0.341160910229804;
            last qlsf[8] = 0.387682253964778;
            last_qlsf[9] = 0.418740763446287;
10
            Mean[0] = 0.049793355045123;
            Mean[1] = 0.071543733408561;
            Mean[2] = 0.115002634382656;
            Mcan[3] = 0.164845365833111;
15
            Mean[4] = 0.211800568342986;
            Mcan[5] = 0.252341845504398;
            Mean[6] = 0.303571730115036;
            Mean[7] = 0.341160910229804;
            Mean[8] = 0.387682253964778;
            Mean[9] = 0.418740763446287;
20
                  Table of candidates for 0.8k LSF quantization
25
            ini svector(stage_cand_08k, 0, 2, 8);
                  Table of candidates for 4.0k LSF quantization
30
             ini_svector(stage_cand_40k, 0, 3, 10);
                  Table of candidates for 8.5k LSF quantization
35
             ini_svector(stage_cand_85k, 0, 3, 10);
 40
```

```
713
                          Copy the MA-VQ Tables
             for (i = 0; i < MAXLTT_08k; i++)
 5
                     for (j = 0; j < LMSMAX_08k; j++)
                             for (k = 0; k < MAXLNp; k++)
                                      lsf_cb_08k[i][j][k] = CBes_08k[i][j][k];
            for (i = 0; i < MAXLTT_40k; i++)
                     for (j = 0; j < LMSMAX_40k; j++)
10
                             for (k = 0; k < MAXLNp; k++)
                                      lsf_cb_40k[i][j][k] = CBes_40k[i][j][k];
             for (i = 0; i < MAXLTT_85k; i++)
              for (j = 0; j < LMSMAX_85k; j++)
15
               for (k = 0; k < MAXLNp; k++)
                                      lsf_cb_85k[i][j][k] = CBes_85k[i][j][k];
20
             for (i = 0; i < NP; i++)
                     lsf_new[i] = (i+0.5)*0.5/NP;
             for (i = 0; i < NP; i++)
                     lsf_mid[i] = (i + 0.5)*0.5 / NP;
25
             for (i = 0; i < NP; i++)
                     lsf_old[i] = (i+0.5)*0.5/NP;
30
             for (i = 0; i < NP; i++)
                     lsfq_old[i] = (i+0.5)*0.5/NP;
             for (i = 0; i < NP; i++)
                     lsfq\_old\_dcc[i] = (i+0.5)*0.5/NP;
35
             return;
40
```

}		
/*	*/	
/*=====================================		====*/
	*/	• •
**		
/* PURPOSE : This function quantize the LSF unsig ar	1 MA */	
/* residual quntization algorithm. */ /**	<i>l</i>	
	*/	
/* _ (FLOAT64 []) lsf : LSF vector. *.	/	
/**	<i>!</i>	
/* OUTPUT ARGUMENTS :	*/ -	
/* _ (FLOAT64 []) qntlsf: quantized LSF vector.		
/* _ (INT16 []) channel_idx: bitstream buffer.	*/	
/*(INT16 []) stage_cnd: prooning information. /**		
/* INPUT/OUTPUT ARGUMENTS :	*/	
/* _ None. */		
/*	1	
/* RETURN ARGUMENTS :	*/	
/* _ Nonc.		*/
		·
void LSF_Q_lsfqnt (FLOAT64 lsf [], FLOAT64 qntlsf [], l	INT TO CHAINCI_IUX (),	INT16:
{		
/*	*/	
INT16 flip_total;		
INT16 k, l, p;		
INT16 Pbest[1];		
INT16 indices[LTT_85k],		
FLOAT64 weight[NP];		
FLOAT64 e_lsf[LP_40k*NP];		
FLOAT64 tmp_qe_qlsf[NP];		

```
Weights calculation
 5
           LSF_Q_lsf_to_weight (lsf, weight,rate);
10
                        Mean subtraction from lsf
           dif_dvector (lsf, Mean, lsf, 0, NP-1);
15
                      Vector Prediction and Quantization */
        switch (rate) {
20
         case RATE0_8K:
           for (p=0; p < LP_08k; p++) {
            for (1=0; 1 < NP; 1++) {
             e_lsf[p*NP+l] = lsf[l];
             for (k=0; k < LQMA_08k; k++) {
25
               e_{lsf[p*NP+l]} = B_{08k[p][k][l]*qes[k][l];
              }
            }
           }
           LSF_Q_Qnt_e(e_lsf,wcight,LP_08k,tmp_qe_qlsf,Pbest,l,
30
                  indices,lsf_cb_08k,MS_08k,stage_cand_08k,LTT_08k);
               for (1 = 0; 1 < LTT_08k; 1++)
                   channel_idx[1] = (INT16)indices[1];
35
          break;
          case RATE4_0K:
            for (p=0; p < LP_40k; p++) {
             for (1=0; 1 < NP; 1++) {
40
```

```
716
```

```
e_lsf[p*NP+l] = lsf[l];
               for (k=0; k < LQMA_40k; k++) {
                e_{lsf[p*NP+l]} = B_{40k[p][k][l]*qes[k][l];
               }
              }
5
             }
             LSF\_Q\_Qnt\_e(e\_lsf,weight,LP\_40k,tmp\_qe\_qlsf,Pbcst,1,
                    indices, lsf\_cb\_40k, MS\_40k, stage\_cand\_40k, LTT\_40k);
                for (1 = 0; 1 < LTT_40k; 1++)
10
                    channel_idx[l] = (INT16)indices[l];
                 channel_idx[LTT_40k] = (INT16)Pbest[0];
            break;
15
            case RATE8_5K:
            case RATE2_0K:
              for (p=0; p < LP_85k; p++) {
                for (l=0; 1 < NP; l++) {
                 c_lsf[p*NP+l] = lsf[l];
20
                 for (k=0; k \le LQMA_85k; k++) {
                  e_{ls}[p*NP+1] = B_{85k}[p][k][1]*qes[k][1];
                 }
                }
               }
 25
              LSF_Q_Qnt_e(e_lsf,wcight,LP_85k,tmp_qe_qlsf,Pbest,1,
                      indices,lsf_cb_85k,MS_85k,stage_cand_85k,LTT_85k);
                  for (1 = 0; 1 < LTT_85k; 1++)
                       channel_idx[l] = (lNT16)indices[l];
 30
              break;
             default:
                printf(" Invalid rate !! \n");
  35
                exit(0);
              }
  40
```

```
717
                  Note: all the delayed decision information are available
            ini_dvector (qntlsf, 0, NP-1, 0.0);
5
        switch (rate) {
          case RATE0_8K:
              for (l=0; l < NP; l++) {
                for (k=0; k < LQMA_08k; k++) {
10
                 qntlsf[l] += B_08k[0][k][l]*qes[k][l];
                }
              }
           break;
15
           case RATE4_0K:
              for (l=0; l < NP; l++) {
                 for (k=0; k < LQMA_40k; k++) {
                   qntlsf[l] += B_40k[Pbest[0]][k][l]*qes[k][l];
                  }
20
               }
           break;
           case RATE8_5K:
           case RATE2_0K:
25
              for (l=0; 1 < NP; 1++) {
                for (k=0; k \le LQMA_85k; k++) {
                  qntlsf[1] += B_85k[0][k][1]*qes[k][1];
                 }
              }
30
           break;
           dcfault:
              printf(" Invalid rate !! \n");
              exit(0);
35
           }
             add_dvector(qntlsf, tmp_qe_qlsf, qntlsf, 0, NP-1);
                                                                qntlsf, 0, NP-1);
             add_dvector(qntlsf, Mean,
40
```

```
flip_total = 0;
            if(qntlsf[0] \le 0.0)
                    flip_total++;
5
            for (k = 1; k < NP; k++)
                     if(qntlsf[k] < qntlsf[k-1])
                             flip_total++;
                    }
10
                           simulate decoder Isf-erasure
15
             if (flip_total > 1)
                      wad_dvector(last_qlsf, 0.9, Mean, 0.1, qntlsf, 0, NP-1);
    #ifdef VERBOSE
                      printf("\nWARNING frame %ld: Encoder flipped %d lsf pairs\n",
20
                                               frm_count, flip_total);
    #endif
                     }
25
             LSF_Q_Order (qntlsf);
             LSF_Q_Space_lsf (qntlsf, 0);
30
                           Shifting the prediction states
          switch (rate) {
            case RATE0_8K:
 35
            case RATE4_0K:/*MA order is 4 for 0.8k and 4.0k */
                   for (k = LQMA_40k-1; 0 < k; k-)
                           cpy_dvector (qes[k-1], qes[k], 0, NP-1);
 40 .
            break;
```

```
case RATE8_5K:
         case RATE2_0K:/*MA order is 2 for 8.5k and 2.0k */
               for k=LQMA_40k-1; LQMA_85k \le k; k--) {
5
              ini_dvector(qes[k],0, NP-1,0.0);
            }_
            for (k=LQMA_85k-1; 0 < k; k--) {
             cpy_dvector(qes[k-1],qcs[k],0, NP-1);
          . }
10
          break;
         default:
            printf(" Invalid rate !! \n");
            exit(0);
15
          }
             cpy_dvector (tmp_qe_qlsf, qes[0], 0, NP-1);
                       Add the Mean vector to the LSF
20
           add_dvector (lsf, Mcan, lsf, 0, NP-1);
25
           return;
30
                                                        */
35 /* FUNCTION : LSF_Q_lsf_decode ().
   /* PURPOSE : This function decodes the LSF vector.
   /*____*/
   /* INPUT ARGUMENTS:
                                                     */
           _(INT16 []) channel_idx: indices.
```

```
720
                            Bad Farme Indicator.
          _(INT16 ) bfi:
                              Bad Farme counter.
          _ (INT16 ) count:
  /*
          _(FLOAT64 []) bfi_lpcg: lpc gain of previous frame.*/
5 /* OUTPUT ARGUMENTS:
         _(FLOAT64 []) qntlsf: decoded LSF vector.
  /* INPUT/OUTPUT ARGUMENTS:
               _ None.
  /* RETURN ARGUMENTS :
               None.
15 void LSF_Q_lsf_decode(INT16 bfi_caution, INT16 *exp_flg,FLOAT64 qntlsf [],
                    INT16 channel_idx [], INT16 bfi, INT16 count, INT16 rate)
               */
20
           INT16 prd_idx;
           INT16 flip_total;
           INT16 lsf_caution;
           INT16 k, l;
           FLOAT64 tmp_qe_qlsf[NP];
25
                         Not frame erasure
30
            prd_idx = 0;
            if (bfi == 0)
                   lsf caution = 0;
                   if (ratc == RATE4_0K)
 35
                          prd_idx = channel_idx[3];
                             Generate the codebook vector
 40
```

```
ini_dvcctor(tmp_qe_qlsf, 0, NP-1, 0.0);
                     switch (rate)
 5
                              case RATE0_8K:
                                              for (1 = 0; 1 < LTT_08k; 1++)
                                                       {
                                                       k = channel_idx[l];
                                                        add_dvector (tmp_qe_qlsf, lsf_cb_08k[l][k],
10
            tmp_qe_qlsf, 0, NP-1);
                                                       }
                                      break;
15
                case RATE4_0K:
                                      for (1 = 0; 1 < LTT_40k; 1++)
20
                                               k = channel_idx[l];
                                               add\_dvector\ (tmp\_qe\_qlsf,\ lsf\_cb\_40k[l][k],
            tmp_qe_qlsf, 0, NP-1);
25
                                      break;
                case RATE8_5K:
                case RATE2_0K:
30
                                      for (1 = 0; 1 < LTT_85k; 1++)
                                                k = channel_idx[1];
                                                add_dvector (tmp_qe_qlsf, lsf_cb_85k[1][k],
35
            tmp_qe_qlsf, 0, NP-1);
                                               }
                                      break;
40
```

```
default: nrerror(" Invalid rate !! \n");
                                      break;
                             }
 5
                                Generate the predicted Isf vector
                     ini_dvector(qntlsf, 0, NP-1, 0.0);
10
                     switch (rate)
                              case RATE0_8K:
                                      for (1 = 0; 1 < NP; 1++)
                                                for (k = 0; k < LQMA_08k; k++)
15
                                                        qntlsf[1] += B_08k[0][k][1]*qes_dec[k][1];
                  break;
                              case RATE4_0K:
                                      for (1 = 0; 1 < NP; 1++)
20
                                               for (k = 0; k < LQMA_40k; k++)
                                                        qntlsf[1] += B_40k[prd_idx][k][1]*qes_dec[k][1];
                  break;
                               case RATE8_5K:
25
                               case RATE2_0K: `
                                       for (1 = 0; 1 < NP; 1++)
                                                for (k = 0; k < LQMA_85k; k++)
                                                        qntlsf[1] += B_85k[0][k][1]*qes_dcc[k][1];
30
                  break;
                               default: nrerror("Invalid rate !!");
                  break;
35
                      add_dvector (qntlsf, tmp_qe_qlsf, qntlsf, 0, NP-1);
                                                                          qntlsf, 0, NP-1);
                      add_dvector (qntlsf, Mcan,
 40
```

```
723
                     flip_total = 0;
                     if (qntlsf[0] \le 0.0)
                             flip_total++;
                     for (k = 1; k < NP; k++)
 5
                              if (qntlsf[k] < qntlsf[k-1])
                                      flip_total++;
                              }
10
                               Isf-erasure, synchronized with encoder
                     if (flip_total > 1)
15
                              {
                              wad_dvector (last_qlsf, 0.9, Mean, 0.1, qntlsf, 0, NP-1);
   #ifdef VERBOSE
                              printf ("WARNING frame %ld: Decoder flipped %d lsf pairs\n",
                                               frm_count, flip_total);
20
   #endif
                              }
25
                      LSF_Q_Order (qntlsf);
                      LSF_Q_Space_lsf (qntlsf, lsf_caution);
              if( count <= 2 && count>=1 && bfi_caution == 1 )
30
                     {
                 lsf_caution = 0;
                 exp_flg = 0;
                 for (k = 0; k < NP - 1; k++)
                      if( 8000.0*(qntlsf[k+1] - qntlsf[k]) < 75.0)
35
                      lsf_caution = 2;
                      exp_flg = 1;
    #ifdef VERBOSE
                                       printf(" LSF_CAUTION1 HAPPENED at FRAME %Id %hd %f \n",
40
```

```
frm_count, k,8000.0*(qntlsf[k+1] - qntlsf[k]));
   #endif
                                               }
                                      }
5
                     else
                              lsf_caution = 0;
                 *exp_flg =0;
10
                }
                     LSF_Q_Space_lsf(qntlsf,lsf_caution);
15
            clse
                                      Frame crasure
20
                              Generate the average past error lsf vector
25
               (*exp_fig) = 0;
                               Shift previous LPC slightly towards mean
30
                      wad_dvector (last_qlsf, 0.9, Mean, 0.1, qntlsf, 0, NP-1);
                      LSF_Q_Order (qntlsf);
                      lsf_caution = 5;
 35
                      LSF_Q_Space_lsf (qntlsf, lsf_caution);
                       ini_dvector (tmp_qe_qlsf, 0, NP-1, 0.0);
                       switch (rate)
 40
```

```
725
                             {
                             case RATE0_8K:
                                      for (1 = 0; 1 < NP; 1++)
                                              for (k = 0; k < LQMA_08k; k++)
                                                      tmp_qc_qlsf[1] += B_08k[0][k][1]*qes_dec[k][1];
5
                                     break;
                             case RATE4_0K:
                                     for (1 = 0; 1 < NP; 1++)
                                              for (k = 0; k < LQMA_40k; k++)
10
                                                      tmp_qe_qlsf[l] += B_40k[0][k][l]*qes_dcc[k][l];
                                     break;
                             case RATE8_5K:
                             case RATE2_0K:
15
                                    for (1 = 0; 1 < NP; 1++)
                                              for (k = 0; k < LQMA_85k; k++)
                                                      tmp_qe_qlsf[l] += B_85k[0][k][l]*qes_dec[k][l];
                                      break;
20
                              default: nrerror ("Invalid rate !!");
                                      brcak;
25
                                                      tmp_qe_qlsf, tmp_qe_qlsf, 0, NP-1);
                     dif_dvector(qntlsf,
                                                              tmp_qc_qlsf, 0, NP-1);
                     dif_dvector(tmp_qe_qlsf, Mean,
30
                     }
                            Shifting the prediction states
35
             cpy_dvector (qntlsf, last_qlsf, 0, NP-1);
             switch (rate)
40
```

```
726
                           MA order is 4 for 0.8k and 4.0k
                 case RATE0_8K:
5
                 case RATE4_0K:
                        for (k = LQMA_40k-1; 0 < k; k--)
                               cpy_dvector (qcs_dec[k-1], qcs_dec[k], 0, NP-1);
                        break;
10
                           MA order is 2 for 8.5k and 2.0k
                 case RATE8_5K:
15
                 case RATE2_0K:
                        for (k = LQMA_40k-1; LQMA_85k \le k; k--)
                               ini_dvector(qes_dec[k],0, NP-1,0.0);
                        for (k = LQMA_85k-1; 0 < k; k--)
20
                               cpy\_dvector(qes\_dec[k-1],qes\_dec[k],0,\,NP-1);
                        break;
                  default: nrerror ("Invalid rate !!");
25
                        break;
                 }
          cpy_dvector (tmp_qc_qlsf, qes_dcc[0], 0, NP-1);
30
           return;
35
```

```
727
  /* FUNCTION : LSF_Q_Order ().
  /*_____
  /* PURPOSE : This function reorder the decoded LSF vector. */
  /*-----*/
5 /* INPUT ARGUMENTS:
  /* __None.
  /* OUTPUT ARGUMENTS:
  /*
            None.
10 /*-----
  /* INPUT/OUTPUT ARGUMENTS:
         _(FLOAT64 []) qlsf: decoded LSF vector.
  /* RETURN ARGUMENTS:
                                    */
15 /*
            None.
  void LSF_Q_Order (FLOAT64 qlsf [])
20
         INT16 i, j;
         FLOAT64 temp;
25
         for (i = NP-1; i \ge 0; i--)
30
               for (j = 0; j < i; j++)
                     if (qlsf[j] > qlsf[j+1])
                           {
                                     = qlsf[j];
                           temp
35
                           qlsf[j] = qlsf[j+1];
                           qlsf[j+1]
                                       = temp;
                           }
                     }
```

	728
	/**/
	}
	/**/
5	,
,	return;
	/**/
	}
10	
/	*/
	· · · · · · · · · · · · · · · · · · ·
	/*====================================
	/* FUNCTION : LSF_Q_Space_lsf (). */
	/**/
	/* PURPOSE : This function preserve the minimum spacing */
	Detween the decoded Est vester.
	/* (range: 0<=val<=0.5). */
•	/*
	/* _ (INT16 []) lsf_caution: flag for LSF caution. */
	/**/
	/* OUTPUT ARGUMENTS: */
	/* _ None. */
25	/**/
	/* INPUT/OUTPUT ARGUMENTS: */
	/* _ (FLOAT64 []) lsf: decoded LSF vector. */
	/**/
	/* RETURN ARGUMENTS : */
30	
	/*=====================================
	void LSF_Q_Space_lsf (FLOAT64 lsf [], INT16 lsf_caution)
	{
35	/**/
	INT16 i;
	FLOAT64 lsf_min, min_dist;
	/**/
40	/*

```
lsf_min = 0.0;
           min_dist = 0.0;
5
        if(lsf_caution == 0)
            lsf min = min_dist = 0.00625; /* At least 50 Hz Spacing */
        else if(lsf_caution == 1)
            lsf_min = min_dist = 0.01250; /* At least 100 Hz Spacing */
10
        else if(lsf_caution == 2)
            lsf_min = min_dist = 0.015625; /* At least 125 Hz Spacing */
        else if(lsf_caution == 3)
            lsf_min = min_dist = 0.01875; /* At least 150 Hz Spacing */
        else if(lsf_caution == 4)
15
            lsf_min = min_dist = 0.03125; /* At least 250 Hz Spacing */
        else if(lsf_caution == 5)
            lsf_min = min_dist = 0.0075; /* At least 60 Hz Spacing */
20
           for (i = 0; i < NP; i++)
                   {
                   if (lsf[i] < lsf_min)
                          lsf[i] = lsf_min;
25
                   lsf_min = lsf[i] + min_dist;
30
           return;
35
    /* FUNCTION : LSF_Q_lsf_to_weight ().
```

```
730
  /* PURPOSE : This function calculates the proper weight
             factor of each LS value.
  /* INPUT ARGUMENTS:
                                                 */
          _(FLOAT64 []) lsf: LSF vector.
5 /*
  /* OUTPUT ARGUMENTS:
          _(FLOAT64 []) weight: error weights.
10 /* INPUT/OUTPUT ARGUMENTS:
  /*
               _ Nonc.
   /* RETURN ARGUMENTS:
              None.
   void LSF_Q_lsf_to_weight (FLOAT64 lsf[], FLOAT64 weight[], INT16 rate)
              ·----*/
20
           INT16 j, i;
           INT16 idx1, idx2;
           FLOAT64 p[NP+1], q[NP+1];
           FLOAT64 p_poly, q_poly;
25
           FLOAT64 inv_pow_spect;
           FLOAT64 p_coeff, q_coeff, pi_arg, x;
30
           pi_arg = 2*PI;
           p[0] = 1.0;
           q[0] = 1.0;
           p[2] = 1.0;
           q[2] = 1.0;
35
           p[1] = cos(lsf[1] * pi_arg);
           p[1] = -2.0 * p[1];
           q[1] = cos(lsf[0] * pi_arg);
           q[1] = -2.0 * q[1];
 40
```

```
731
             for (i = 2; i \le NP/2; i++)
 5
                      idx1 = 2 * i - 1;
                      idx2 = 2 * i;
                      p_coeff = cos (lsf[idx2 - 1] * pi_arg);
10
                      p_coeff = -2.0 * p_coeff;
                      q_coeff = cos (lsf[idx2 - 2] * pi_arg);
                      q_coeff = -2.0 * q_coeff;
                      for (j = i; j \ge 2; j--)
15
                              q[j] = q[j] + q[j-1] * q_coeff + q[j-2];
                              p[j] = p[j] + p[j-1] * p_coeff + p[j-2];
                              q [idx2 - j] = q [j];
                              p [idx2 - j] = p [j];
20
                      p [1]
                             = p[1] + p_coeff;
                      p[idx1] = p[1];
25
                      q[1] = q[1] + q_coeff;
                      q[idx1] = q[1];
                      p [idx2] = 1.0;
                      q [idx2] = 1.0;
30
             /* Calculating the (1 / (power spectrum)) and the proper weight */
35
             for (i = 0; i < NP; i += 2)
                      p_poly = LSF_Q_cosine_polynomial(p, pi_arg*lsf[i]);
                      x = cos(pi_arg*lsf[i]);
40
```

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```
732
                  inv_pow_spect = 4*sqr(p_poly)*(2.-2.*x);
                  weight[i] = pow(inv_pow_spect/4.,-0.4);
                  }
5
           for (i = 1; i < NP; i += 2)
                   {
                   q_poly = LSF_Q_cosine_polynomial(q,pi_arg*lsf[i]);
                   x = cos(pi_arg*lsf[i]);
10
                   inv_pow_spect = 4*sqr(q_poly)*(2.+2.*x);
                   weight[i] = pow(inv_pow_spcct/4.,-0.4);
                   }
        if (rate == RATE0_8K) {
15
         /* different weights for different frequencies. 10/24/99, Eyal */
         for (i=4; i < NP; i++) {
           weight[i] *= -0.16*(FLOAT64)i+1.64; /* 1.0 at i=4 an 0.2 at i=9 */
          }
20
        }
             return;
 25
     /* FUNCTION : LSF_Q_cosine_polynomial ().
     /* PURPOSE : This function calculates the cosine polynomial. */
                                                       */
                  Note that coef[0] should be 1.0.
  35 /*-----
      /* INPUT ARGUMENTS:
               _(INT16 []) coef: coefficients.
               _(INT16 ) omega: frequency.
   40 /* OUTPUT ARGUMENTS:
```

```
733
             None.
  /* INPUT/OUTPUT ARGUMENTS:
             None.
5 /*-----
  /* RETURN ARGUMENTS:
         _(FLOAT64) sum: polynomial cosinus modulated sum. */
10 FLOAT64 LSF_Q_cosine_polynomial (FLOAT64 coef[], FLOAT64 omega)
         INT16 j;
15
         FLOAT64 sum, x;
         sum = cocf[NP/2]/2.;
20
         for (j = 0; j < NP/2; j++){
          x = \cos((NP/2-j)*omega);
          sum += cocf[j] * x;
25
         return (sum);
30
         }
*/
  /* FUNCTION : LSF_Q_Qnt_e ().
  /* PURPOSE This function quantizes the prediction error of */
            the lsf prediction.
```

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```
734
  /* INPUT ARGUMENTS:
                              prediction residual.
                      (]) e:
        _ (FLOAT64
        _(FLOAT64 []) w: LSP weight factor.
                     ) DIn: number of input candidates
        _ (INT16
  /*
                       (always 1 in the ITU-T 4kbit/s).*/
5 /*
                     ) DOut: number of output candidates
        _(INT16
  /*
                        (always 1 in the ITU-T 4kbit/s).*/
  /*
        _(FLOAT64 [][][]) CBe: codebooks.
  /*
                     []) MS: size of codebooks.
         _ (INT16
  /*
                     []) stage_cnd: prooning information.
10 /*
        _ (INT16
   /* OUTPUT ARGUMENTS:
                     *) Pptr: best predictor.
         _(INT16
         _ (FLOAT64 []) qe: quantized prediction residual. */
                      *) cluster:best indices.
         (INT16
15 /*
   /* INPUT/OUTPUT ARGUMENTS:
                                              */
                _ None.
20 /* RETURN ARGUMENTS:
    void LSF_Q_Qnt_e (FLOAT64 e [], FLOAT64 w [], INT16 DIn, FLOAT64 qe[], INT16 *Pptr,
                           INT16 DOut, INT16 cluster [],
25
                                   FLOAT64 ***CBe, INT16 MS[],
                                          INT16 *stage_cnd, INT16 ltt)
 30
             INT16 ptr_back[LTT_85k][MAX_CAND_LSFQ];
             INT16 best_indx[LTT_85k][MAX_CAND_LSFQ];
             INT16 N_indx[LTT_85k];
             INT16 ptr;
 35
             INT16 i;
                            d_data [LTT_85k][MAX_CAND_LSFQ*NP];
             FLOAT64
  40
```

```
LSF_Q_New_ML_search (e,w, Dln, d_data[0], (INT16)stage_cnd[0],
                                                          bcst_indx[0], ptr_back[0], CBc[0], MS[0]);
 5
             for (i = 1; i < ltt-1; i++)
                     {
                      LSF_Q_New_ML_search (d_data[i-1], w, (INT16)stage_cnd[i-1],
                                                                            d_data[i], (INT16)stage_cnd[i],
                                                                                    best_indx[i], ptr_back[i], CBe[i],
10
                                                                                              MS[i]);
                     }
             LSF_Q_New_ML_search (d_data[ltt-2], w, (INT16)stage_cnd[ltt-2],
                                                         d_data[ltt-1], DOut, best_indx[ltt-1],
15
                                                                  ptr_back[ltt-1], CBe[ltt-1], MS[ltt-1]);
20
          N_{indx[ltt-1]} = best_{indx[ltt-1][0]};
          ptr = ptr_back[ltt-1][0];
          for (i=1tt-2; i \ge 0; i--)
           N_{indx[i]} = best_{indx[i][ptr]};
           ptr = ptr_back[i][ptr];
25
          }
              /* this is the pointer to the best predictor */
          *Pptr = ptr;
30
          for (i=0; i < ltt; i++) {
           cluster[i] = N_indx[i];
          }
35
          ini_dvector (qe, 0, NP-1, 0.0);
          for (i=0; i < ltt; i++) {
           add_dvector (qc, CBe[i][N_indx[i]], qe, 0, NP-1);
          }
```

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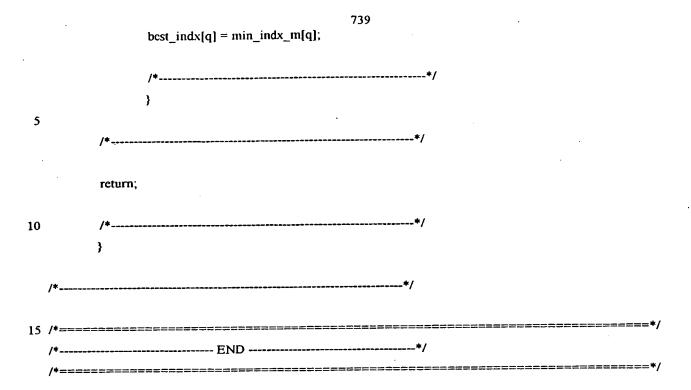
	return;	
	/**/	
	/*	
	}	
5		
	/**/	•
•	/*	
	/*=====================================	* /
10	/* FUNCTION : LSF_Q_Ncw_ML_search ().	
	/* PURPOSE : This function performs the ML search. The data */	
	/* pointer points to a J vectors array (each of */	
	/* length NP, concatenated), the diff_data */	
15		
	/* lengts NP, concatenated). The diff_data holds, */	
	/* for each vector in data, the K smallest */	
	/* quantization error vector, using the given */	
	/* code book. */	
20	/**/	
	/* INPUT ARGUMENTS : */	
	/* _ (FLOAT64 []) d_data: prediction residual. */	
	/* _ (FLOAT64 []) weight: LSP weight factor. */	
	/* _ (INT16) J: number of input candidates. */	
25	/* _ (INT16) K: number of output candidates. */	
	/* _ (FLOAT64 [][]) codc: codebook. */	
	/* _(INT16) MQ: size of codebook. */	
	/**/ */	
	/* OUTPUT ARGUMENTS.	
30	/* _ (FLOAT64 []) new_u_data. New production restaura	
	/* _ (INTIO []) best_flidx. array of the optimization	
	indexes.	
	/* _ (INT16 []) ptr_back: best candidates of past. /	
2.4	5 /* INPUT/OUTPUT ARGUMENTS : */	
3:	/* _ None. */	
	/**/	
	/* RETURN ARGUMENTS : */	
	/* None. */	
4	.0 /*====================================	==*/

```
void LSF_Q_New_ML_search (FLOAT64 d_data [], FLOAT64 weight [], INT16 J,
                                FLOAT64 new_d_data [], INT16 K, INT16 best_indx [],
                                        INT16 ptr_back [], FLOAT64 **code,
                                               INT16 MQ)
5
          INT16 idx1, idx2;
10
          INT16 m,l,p,q;
          FLOAT64 sum[LMSMAX_85k*MAX_CAND_LSFQ];
          FLOAT64 min[MAX_CAND_LSFQ];
          INT16 min_indx_p[MAX_CAND_LSFQ];
          INT16 min_indx_m[MAX_CAND_LSFQ];
15
                                                    */
                          Initialisation
20
           for (q = 0; q < K; q++)
                  min[q] = DBL_MAX;
25
           for (p = 0; p < J; p++)
                  for(m=0; m < MQ; m++)
30
                          sum[p*MQ+m] = 0.0;
                          for (1 = 0; 1 < NP; 1++)
                                 sum[p*MQ+m] += weight[l]*sqr(d_data[p*NP+l]-code[m][l]);
                         }
35
                  }
           for (q = 0; q < K; q++)
                  {
40
```

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```
738
                  for (p = 0; p < J; p++)
                                  for (m=0; m < MQ; m++)
5
                                                  if (sum[p*MQ+m] < min[q])
                                                           min[q] = sum[p*MQ+m];
                                                           min_indx_p[q] = p;
                                                           min_indx_m[q] = m;
10
                                                           }
                                           }
                            }
15
                     idx1 = min_indx_p[q]*MQ+min_indx_m[q];
                     sum[idx1] = DBL\_MAX;
 20
  25
              for (q = 0; q < K; q++)
                       for (l = 0; l < NP; l++)
   30
                                idx1 = min_indx_p[q]*NP + 1;
                                idx2 = min_indx_m[q];
                                new_d_data[q*NP+1] = d_data[idx1]-code[idx2][1];
    35
                         ptr_back[q] = min_indx_p[q];
     40
```

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/ / Conexant System Inc. 5 /* 4311 Jamborce Road */ /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ 15 /* PROTOTYPE FILE : lib_q_lsf.h /*----*/ (void); void LSF_Q_init_lib (FLOAT64 [], FLOAT64 [], INT16 *, INT16); 25 void LSF_Q_lsfqnt (FLOAT64 []); void LSF_Q_Order (FLOAT64 [], FLOAT64 [], INT16); void LSF_Q_lsf_to_weight 30 (FLOAT64 [], FLOAT64); FLOAT64 LSF_Q_cosine_polynomial (FLOAT64 [], INT16); void LSF_Q_Space_lsf (INT16, INT16 *, FLOAT64 [], INT16 [], 35 void LSF_Q_lsf_dccode INT16, INT16, INT16); (FLOAT64 [], FLOAT64 [], INT16, FLOAT64 [], INT16 [], INT16 , INT16 [], FLOAT64 ***, void LSF_Q_Qnt_e 40

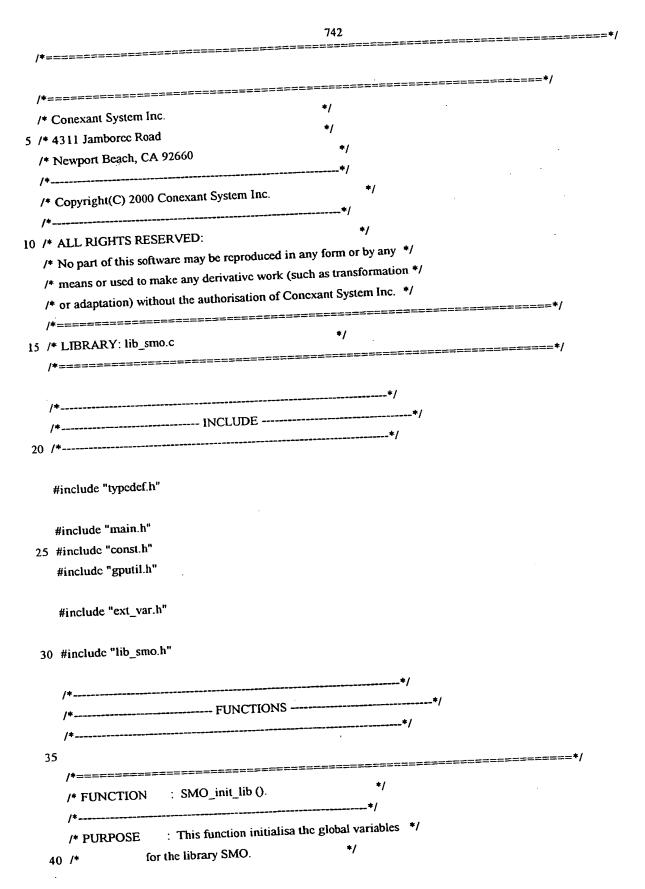
INT16 [], INT16 *, INT16);

 $void\ LSF_Q_New_ML_search$

(FLOAT64 [], FLOAT64 [], INT16, FLOAT64 [],
INT16, INT16 [], INT16 [],
FLOAT64 **, INT16);

5

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```
743
   /* INPUT ARGUMENTS:
               _ None.
                                            */
 5 /* OUTPUT ARGUMENTS:
   /* __ None.
                                            */
   /* INPUT/OUTPUT ARGUMENTS:
               _ None.
10 /*-----
   /* RETURN ARGUMENTS:
               _ None.
15 void SMO_init_lib (void)
                         SMO_lsf_smooth_est
20
           N_{mode\_sub\_est} = 0;
           N_{modc_frm} = 0;
                          SMO_initial_analysis
25
           consec low = 0;
           consec_high = 0;
30
           consec_vad_0 = 0;
           updatcs_noise = 0;
           updates_speech = 0;
           calls = 0;
35
           lcv_reset = 0;
           ma_max_noise = 0.0;
           ma_max_speech = 0.0;
           ma_cp = 0.0;
           buffer_p = buffer_smo+HI_LAG2;
40
```

	/**/	
	/* SMO_rcfined_analysis */	
	/**/	
	N_mode_sub_ref = N_MODE_SUB_START;	
	consec = 0;	•
	updates = 0;	
	$ma_max = 0.0;$	
	/**/	
	/*	
	roturn'	
	return;	
	/**/	
	}	
/*	*/	
/*	INCTION : SMO_lsf_smooth_cst (). */*/ TRROGS: This function classify teh input signal frame: */	
/* /* PU /*	TRPOSE : This function classify teh input signal frame: */ file. */	
/*/ /* PU /*	TRPOSE : This function classify teh input signal frame: */ file. */	
/*/ /* PU /*	TRPOSE : This function classify teh input signal frame: */ file. */ PUT ARGUMENTS : */	
/* /* PU /* 5 /* /* IN /*	INCTION: SMO_ISI_SHOOM_EST (). */ IRPOSE: This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS: */ (INT16:) Vad: VAD of current frame. */	
/* /* PU /* 5 /* /* IN /*	INCTION : SMO_ISI_SHOOM_Est ():	
/* /* PU /* 5 /* /* IN /* /*	INCTION: SMO_ISI_SHOOM_Est (). IRPOSE: This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS: */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */	
/* /* PU /* 5 /* /* IN /* /* /*	INCTION: SMO_ISI_SHOOM_est (). IRPOSE: This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS: */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) lsf : curent frame LSF coefficients.*/	
/* /* PU /* 5 /* /* IN /* /* /* /* /* /*	IRPOSE : This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS : */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) lsf : curent frame LSF coefficients.*/	
/* /* PU /* 5 /* /* IN /* /* /* /* 0 /* /* O'	INCTION: SMO_ISI_SHOOM_est (). IRPOSE: This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS: */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) lsf : curent frame LSF coefficients.*/ _ TUTPUT ARGUMENTS: */	
/* /* PU /* 5 /* /* IN /* /* /* /* /* /* /* /* /* /* /* /* /*	INCTION: SMO_ISI_SHOOM_Est (). IRPOSE: This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS: */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) lsf : curent frame LSF coefficients.*/ UTPUT ARGUMENTS: */	
/* /* PU /* 5 /* /* IN /* /* /* /* /* /* /* /* /* /*	IRPOSE : This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS : */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) lsf : curent frame LSF coefficients.*/ UTPUT ARGUMENTS : */ _ (FLOAT64) beta_frm: estimated LSF smoothing */ factor. */	
/* /* PU /* 5 /* /* IN /* /* /* /* /* 0 /* /* /* /* 35 /*	IRPOSE : This function classify teh input signal frame: */ file. */ IPUT ARGUMENTS : */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) Isf : curent frame LSF coefficients.*/ UTPUT ARGUMENTS : */ _ (FLOAT64) beta_frm: estimated LSF smoothing */	
/* /* PU /* 5 /* /* IN /* /* /* /* /* /* /* /* 35 /*	IRPOSE : This function classify teh input signal frame: */ file. */ PUT ARGUMENTS: */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) lsf : curent frame LSF coefficients.*/ _ (FLOAT64) beta_frm: estimated LSF smoothing */ factor. */	
/* /* PU /* 5 /* /* IN /* /* /* /* /* 35 /* /* IT /*	INCTION : SMO_ISI_SMOUN_est (). ### File.	
/* /* PU /* 5 /* /* IN /* /* 0 /* /* /* O' /* /* /* /* /* /* /* /* /* /* /* /* /*	INCTION : SMO_ISI_SMOUN_est (). ### PUT ARGUMENTS : */ _ (INT16) Vad : VAD of current frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (INT16) PastVad: VAD of previous frame. */ _ (FLOAT64) refl : first reflection coefficient. */ _ (FLOAT64 []) Isf : curent frame LSF coefficients.*/ */ UTPUT ARGUMENTS : */ _ (FLOAT64) beta_frm: estimated LSF smoothing */ factor. */ NPUT/OUTPUT ARGUMENTS : */ _ None. */	

```
745
   void SMO_lsf_smooth_est (INT16 Vad, INT16 PastVad, FLOAT64 refl,
                            FLOAT64 lsf [], FLOAT64 beta_frm [])
 5
            INT16 i, i_sf;
            INT16 mode_frm, N_sub_and;
            FLOAT64 dSP, dSP_avg, dSP_int;
10
                         Algorithm initialisation
15
            if (frm_count == 1)
                    cpy_dvcctor (lsf, lsf_old_smo, 0, NP-1);
                    cpy_dvector (lsf, ma_lsf, 0, NP-1);
                    ini_dvector (dSP_buf, 0, DSP_BUFF_SIZE-1, 0.0);
20
                    }
                          Calculate subframe smoothing
25
            for (i_sf = 0; i_sf < N_SF4; i_sf++)
                    if(Vad == 1 || PastVad == 1)
30
                            {
                                         Speech/spike mode
35
                             N_{mode\_sub\_est} = 0;
                            }
                     N_sub[i_sf] = N_mode_sub_cst;
                     if(Vad == 0 \&\& PastVad == 0)
40
```

```
746
                             {
                                          Unvoiced/non-speech mode
5
                             N_mode_sub_est++;
                              if(N_{mode_sub_est} > 4)
                                     N_{mode\_sub\_est} = 4;
                             }
10
                     }
                             Calculate frame smoothing
15
             for (dSP = 0.0, dSP_{avg} = 0.0, i = 0; i < NP; i++)
                      dSP += (lsf[i] - lsf\_old\_smo[i])*(lsf[i] - lsf\_old\_smo[i]);
                      dSP\_avg += (lsf[i]-ma\_lsf[i])*(lsf[i]-ma\_lsf[i]);
20
             for(dSP_int = 0.0, i = DSP_BUFF_SIZE-1; i > 0; i--)
                      dSP_buf[i] = dSP_buf[i-1];
25
                      dSP_int
                                       += dSP_buf[i];
                      }
              dSP_buf[0] = dSP_avg;
 30
              dSP_int += dSP_buf[0];
              modc_frm = 0;
              N sub and = 1;
 35
              for (i = 0; i < N_SF4; i++)
                       {
                       if (Vad == 1 || PastVad == 1)
                               mode_frm = 3;
                       if(N_sub[i] == 0)
 40
```

```
747
                            N_sub_and = 0;
                    }
5
            if(mode\_frm == 3 \parallel N\_sub\_and == 0 \parallel refl > R1lim)
                    {
                     N_{mode_frm} = 0;
                     *beta_frm = 0.0;
10
                     ini_dvector (dSP_buf, 0, DSP_BUFF_SIZE-1, 0.0);
                    }
            elsc if((dSP > dSP1 \parallel dSP_int > dSP_int1) && N_mode_frm > 0)
                     N_{modc_frm} = 0;
15
                     *beta_frm = 0.0;
                     ini_dvector (dSP_buf, 0, DSP_BUFF_SIZE-1, 0.0);
20
                    }
            else if(dSP > dSP2 && N_mode_frm > 1)
                     N_{modc_frm} = 1;
25
            if(mode_frm == 0)
                    {
                     N_mode_frm++;
                     if (N_mode_frm > 5)
30
                             N_{mode_frm} = 5;
                     *beta_frm = (FLOAT64)((N_mode_frm-1)*(N_mode_frm-1)) /
                    16.0*BETA_FRM;
35
                              Buffer parameters
40
            cpy_dvector (lsf, lsf_old_smo, 0, NP-1);
```

```
for (i = 0; i < NP; i++)
                 ma_lsf[i] = (*beta_frm)*ma_lsf[i] + (1.0-(*beta_frm))*lsf[i];
5
          return;
10
   /* FUNCTION : SMO_refined_analysis ().
15 /*----*/
                  : This function performs the refined analysis of */
   /* PURPOSE
               the energy evolution and calculates the
               smoothing factor for the energy manipulation. */
20 /* INPUT ARGUMENTS:
            _(FLOAT64 []) res : input frame.
    /*
            _(INT16 ) speech_mode: result of the initial */
                                            */
                           analysis.
            _(FLOAT64 []) pitch_corr : pitch correlation values. */
            _(FLOAT64 []) lsf : curent frame LSF
 25 /*
                            coefficients.
     /* OUTPUT ARGUMENTS:
             _(FLOAT64 *) exc_mode: smoothing mode.
             _(FLOAT64 *) beta_sub: estimated smoothing factor.*/
  30 /*
     /* INPUT/OUTPUT ARGUMENTS :
                                              */
                  None.
  35 /* RETURN ARGUMENTS:
                  _ None.
      void SMO_refined_analysis (FLOAT64 res [], INT16 speech_mode,
                                           FLOAT64 pitch_corr [], INT16 *exc_mode,
   40
```

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```
749
                                                   FLOAT64 *bcta_sub, INT16 l_sf)
5
           INT16 i;
           FLOAT64 max, max_mes, val;
                        Find the max excitation value
10
           for(max = -DBL_MAX, i = 0; i < l_sf; i++)
                    val = (res[i] > 0.0 ? res[i] : -res[i]);
15
                    if (val > max)
                           max = val;
                   }
20
            max_mes = max/(ma_max + EPSI);
            if (speech_mode == 1)
25
                   {
                               speech/spike mode
                    N_mode_sub_ref = N_MODE_SUB_START;
30
                    (*beta_sub) = 0.0;
                    (*exc_mode) = 1;
                   }
            else
35
                    if(max_mes < 1.75)
40
```

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```
750
                                    Unvoiced/non-speech mode
                           N_mode_sub_ref++;
                           if (N_mode_sub_ref > 4)
5
                                  N_mode_sub_ref = 4;
                                Smooth energy if sustained unvoiced/non-speech */
10
                           if (N_mode_sub_ref > 0)
                                   (*beta_sub) = BETA_SUB*(FLOAT64)((N_mode_sub_ref-1)*
           (N_mode_sub_rcf-1))/9.0;
15
                            elsc
                                    (*beta_sub) = 0.0;
                            (*exc_mode) = 0;
                            }
 20
                    elsc
                                         speech/spike mode
  25
                             N_mode_sub_ref = N_MODE_SUB_START;
                              (*beta_sub) = 0.0;
                              (*exc_mode) = 1;
                              }
  30
                      if(max_mes <= 0.5)
                               if (conscc < CONSEC_MAX)
                                      consec++;
   35
                              }
                       clse
                               consec = 0;
    40
```

```
751
                             Update moving average of maximum
                  if((*exc_mode == 0 \&\& (max_mcs > 0.5 || consec > CONSEC_MAX-1)) ||
5
                          (updates <= UPDATE_MAX && pitch_corr[1] < 0.60 &&
                                          pitch_corr[0] < 0.65)
                                          {
                                                  ma_max = 0.9*ma_max + 0.1*max;
10
                                                  if (updates <= UPDATE_MAX)
                                                          updates++;
                                          }
15
            return;
20
25
   /* FUNCTION : SMO_initial_analysis ().
   /* PURPOSE : This function performs the initial analysis of */
                the energy evolution.
30 /*
   /* INPUT ARGUMENTS:
            _ (FLOAT64 []) signal: input frame.
            _(INT16 ) Vad : VAD of current frame.
            _(FLOAT64 ) lag : sub-frame lag value.
35 /*
            _(FLOAT64 ) refl : first reflection coefficient */
                            analysis.
            _(FLOAT64 []) lsf : curent frame LSF coefficients. */
40 /* OUTPUT ARGUMENTS:
```

```
752
           _ (FLOAT64 []) pitch_corr : pitch correlation values. */
  /*
           _(FLOAT64 *) speech_mode : smoothing mode (1/0). */
  /*
  /* INPUT/OUTPUT ARGUMENTS:
                None.
5 /*
  /* RETURN ARGUMENTS:
10
   void SMO_initial_analysis(FLOAT64 signal [], INT16 Vad,
                       FLOAT64 lag, FLOAT64 refl, INT16 *speech_mode,
                       FLOAT64 *pitch_corr, INT16 l_sf)
15
            INT16 i, j;
            INT16 lag1, lag2, start;
            INT16 cond1, cond2, cond3, cond4;
20
            FLOAT64 max, max_mcs, val, energy;
            FLOAT64 cp1, cp2, cp, SNR_max, alpha_speech;
            FLOAT64 deci_max_mes, deci_ma_cp;
            FLOAT64 update_max_mes;
             FLOAT64 update_ma_cp_speech;
 25
             FLOAT64 max_group[MAX_GRP_SIZE], min_max, end_max;
             FLOAT64 y, yx, slope, sum;
             FLOAT64 val1, val2, val3;
 30
                    Update counter for consecutive non-speech subframes
             if(Vad == 0)
 35
                               Counts to max 2 frames (8 subframes)
                      if (consec\_vad\_0 < 8)
  40
```

```
753
                          consec_vad_0++;
                  }
           cisc
                  consec_vad_0 = 0;
5
                Estimate speech to noise ratio for adaptation of thresholds */
           if (updates_noise >= 20 && updates_speech >= 20)
10
                  {
                   SNR_max = ma_max_speech/(ma_max_noise + EPSI);
                   SNR max = (SNR_max > 32.0 ? 32.0 : SNR_max);
15
           else
                   SNR_max = 3.5;
           if (SNR_max < 1.75 && ADAPT_THRESHOLDS)
                   {
                   deci_max_mes = 1.30;
20
                   deci_ma_cp = 0.70;
                   update_max_mes = 1.10;
                   update_ma_cp_speech = 0.72;
25
            else if(SNR_max < 2.5 && ADAPT_THRESHOLDS)
                   deci_max_mes = 1.65;
                   deci_ma_cp = 0.73;
30
                   update_max_mes = 1.30;
                   update_ma_cp_speech = 0.72;
                   }
35
            elsc
                            Start-up and non-adaptive thresholds
40
```

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```
754
                     deci_max_mcs = 1.75;
                                              = 0.77;
                     deci_ma_cp
                     update_max_mes = 1.30;
5
                     update_ma_cp_speech = 0.77;
                                                                */
                             Update signal buffer
10
             for (i = 0; i < HI\_LAG2; i++)
                     buffcr_smo[i] = buffer_smo[i+l_sf];
15
             for (i = 0; i < l_sf; i++)
                     buffer_p[i] = signal[i];
                      Calculate pitch correlation of the speech signal
20
             energy = 0.0;
             for (i = 0; i < l_sf; i++)
                     energy += (INT16)buffer_p[i]*(INT16)buffer_p[i];
25
             lag1 = (INT16)lag;
              if (lag1 == HI_LAG2)
                      lag2 = lag1-1;
 30
              else
                      lag2 = lag1+1;
              if (energy == 0.0)
                      cp = 0.0;
 35
              clse
                      {
                                                                 &val1, 0, l_sf-1);
                       dot_dvector (buffer_p, buffer_p-lag1,
                                                                  &val2, 0, 1_sf-1),
                       dot_dvector (buffer_p, buffer_p,
                       dot_dvector (buffer_p-lag1, buffer_p-lag1, &val3, 0, l_sf-1);
 40
```

```
cpl = val1 / (sqrt(val2 * val3) + EPSI);
                      dot_dvector (buffer_p, buffcr_p-lag2,
                                                                &val1, 0, l_sf-1);
                      dot_dvector (buffer_p, buffer_p,
                                                                &val2, 0, l_sf-1);
 5
                      dot_dvector (buffer_p-lag2, buffer_p-lag2, &val3, 0, l_sf-1);
                      cp2 = val1 / (sqrt (val2 * val3) + EPSI);
10
                      cp = (cp1 > cp2) ? cp1 : cp2;
                     }
             ma_cp = 0.9*ma_cp + 0.1*cp;
15
                            Find the max in pitch period
             start = (lag2 > l_sf) ? (l_sf-lag2) : 0;
20
             sum = 0.0;
             max = -MAXFLT;
             for (i = start; i < l_sf; i++)
25
                      val = (buffer_p[i] > 0.0) ? buffer_p[i] : -buffer_p[i];
                                       Mean Update
30
                      sum += val;
                                       Max Update
35
                      if (val > max)
                              max = val;
40
```

10

```
for (i = 0; i < SMO_BUFF_SIZE-1; i++)
{
    buffer_max_smo[i] = buffer_max_smo[i+1];
    buffer_sum_smo[i] = buffer_sum_smo[i+1];
}</pre>
```

buffer_sum_smo [SMO_BUFF_SIZE-1] = sum;
buffer_max_smo [SMO_BUFF_SIZE-1] = max;

/* Find sum excluding current subframe, and max in groups of */
20 /* 3 subframes */

```
sum = 0.0;
for(i = 0; i < MAX_GRP_SIZE; i++)

25

{
    max_group[i] = buffer_max_smo[3*i];
    for (j = 1; j < 3; j++)
    {
        /*_____*/</pre>
```

30 /* sum Update */
/*----*/

if $(3*i+j < SMO_BUFF_SIZE-1)$

sum += buffer_sum_smo[3*i+j];

/*-----*/
/* Update subgroup max */

if (buffer_max_smo[3*i+j] > max_group[i])

```
757
                                            max_group[i] = buffer_max_smo[3*i+j];
                           }
                   }
 5
            sum *= 1.7857e-03;
                       Find minimum among first 4 subgroups
10
            min_max = max_group[0];
            for (i = 1; i \leq MAX\_GRP\_SIZE-1; i++)
15
                    {
                    if(max_group[i] < min_max)</pre>
                                    min_max = max_group[i];
                    }
20
                       Extract the maximum of last subgroup
            cnd_max = max_group[MAX_GRP_SIZE-1];
25
                    Find slope of MSE linear fit to the 5 maximums
30
            yx = 0.0;
            y = 0.0;
            for (i = 0; i < MAX\_GRP\_SIZE; i++)
35
                    yx += (FLOAT64)i * max_group[i];
                    y += max_group[i];
                    }
            slope = 0.1*(yx - 2.0*y);
40
```

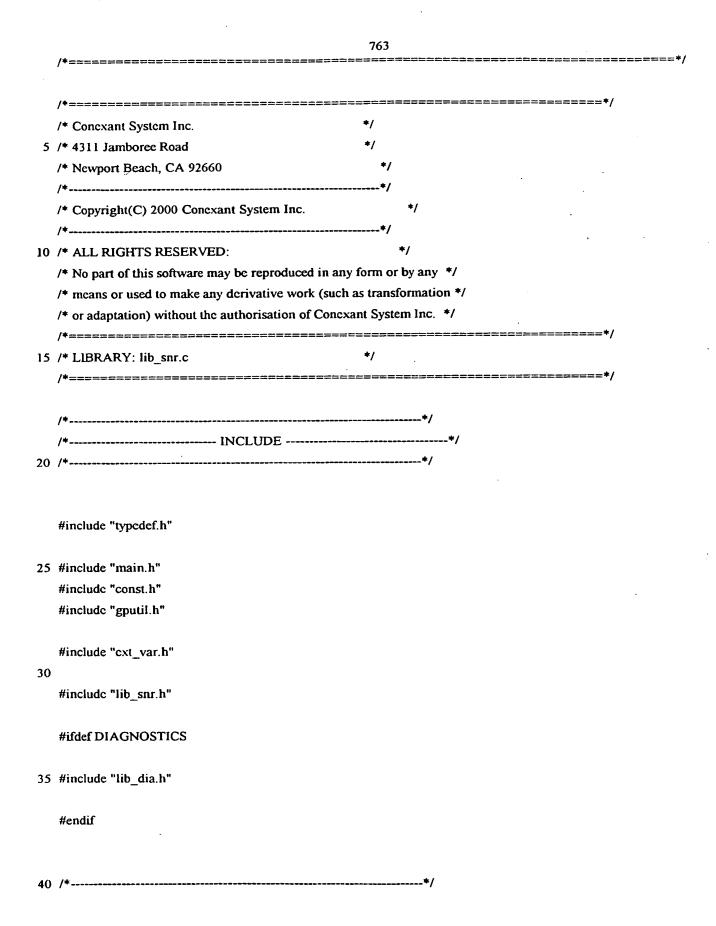
```
758
                                                            */
                           Classify subframe
            if (((max_mes < deci_max_mes && ma_cp < deci_ma_cp) \parallel (Vad == 0)))
5
                   *speech_mode = 0;
            else
                   *speech mode = 1;
10
            /* Measure consecutive low level (compared to noise) subframes */
            if(updates_noisc == 21 && max_mes <= 0.3)
15
                    if(consec_low < HL_COUNT_MAX)
                            consec_low++;
                    }
            elsc
                    consec_low = 0;
20
                  Reset noise update if significant decrease compared to
                            the noise level
25
            if(consec_low == HL_COUNT_MAX)
                     updates_noise = 0;
                     lev reset = -1;
30
                    }
             /* Measure consecutive stationary high level (compared to noise) */
                          subframes likely to be noise
35
             if( (updatcs_noise >= 20 || lev_reset == -1) && max_mes > 1.5 &&
                    ma_cp < 0.70 \&\& cp < 0.85 \&\& refl < -0.40 \&\&
                             end_max < 50.0*min_max && max < 35.0*sum && slope > -100.0 &&
 40
```

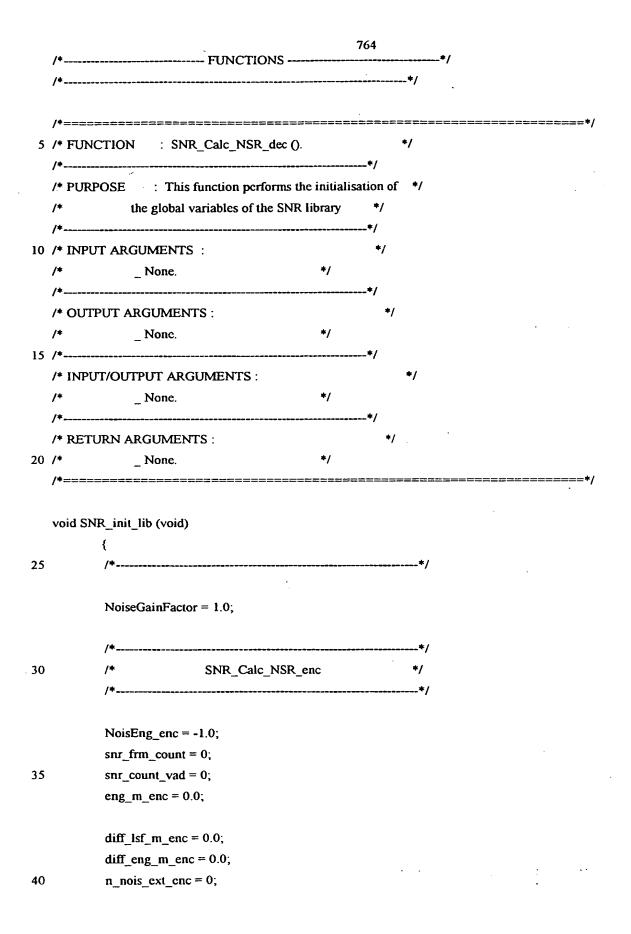
```
759
                                    slope < 120.0)
                                    if(consec_high < HL_COUNT_MAX)
                                            consec_high++;
5
                                    }
            else
                   consec_high = 0;
10
                   Reset noise update if significant stationary increase */
                      in noise level subframes likely to be noise */
            if ((consec_high == HL_COUNT_MAX) && (end_max < 6.0*min_max) &&
15
                           (max < 5.0*sum))
                    updates_noise = 20;
                    lev_reset = 1;
20
                                                             */
                          Update of noise max
25
                                                               */
                        1. condition: regular update
            cond1 = ((max_mes < update_max_mes) && (ma_cp < 0.60) &&
30
                           (cp < 0.65) && (max_mcs > 0.3));
                        2. condition: vad conditioned update
35
            cond2 = (consec_vad_0 == 8);
40
                        3. condition: start-up/resct update
```

```
760
            cond3 = (updates_noise \le 20 \&\& ma_cp \le 0.70 \&\& cp \le 0.75 \&\&
                            refl < -0.40 && end_max < 5.0*min_max &&
                                     (lev_reset != -1 || (lev_reset == -1 && max_mes < 2.0)));
5
                         4. condition: pitch correlation
10
            cond4 = (ma_cp > update_ma_cp_speech);
            if(calls == 3 && (cond1 || cond2 || cond3))
                    {
15
                             Update moving average of noise maximum
                     ma_max_noise = 0.9*ma_max_noise + 0.1*max;
20
                     if (updates_noise <= 20)
                             updates_noise++;
                     clsc
25
                             lcv_reset = 0;
30
                             Update of speech max
             clse if (calls == 3 && cond4)
35
                     {
                              Update moving average of speech maximum
                     if (updates_speech <= 60)
40
```

```
761
                    alpha_speech = 0.95;
              else
                    alpha_speech = 0.999;
              ma_max_speech = alpha_speech + ma_max_speech +
5
                                                                  (1.0-
  alpha_speech)*max;
              if (updates_speech <= 60)
                         updates_speech++;
10
15
        if (calls < 3)
              calls++;
20
        pitch_corr[0] = cp;
        pitch_corr[1] = ma_cp;
25
        rcturn;
```

	702		*/
,	/*====================================		/
,	/*=====================================	======================================	
1	/* Conexant System Inc. */		
,	/* 4311 Jamborec Road */		
	/* Newport Beach, CA 92660 */		
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	/* or adaptation) without the authorisation of Conexant Syste	em Inc. */	
	/*====================================		
	/* PROTOTYPE FILE : lib_smo.h	*/	
	/*====================================		
	•		
	/*	*/	
	/*FUNCTIONS		
	/*		
J	,		
	void SMO_init_lib (void);		
	<u> </u>		
	void SMO_lsf_smooth_cst (INT16, INT16, FLOAT	764, FLOAT64 [], FLOAT64 []);	
5			
	void SMO_initial_analysis(FLOAT64 [], INT16, FLOAT64	, FLOAT64, INT16 *,	
	, , , , , , , , , , , , , , , , , , ,		
	FLOAT64 *, INT16);		
٥	void SMO_refined_analysis (FLOAT64 *, INT16, Fl	LOAT64 *, INT16 *, FLOAT64 *,	
•	void bino_totilled_unarjoid (0 = 0 = 0 , , , ,		INTI
	/*====================================		===*/
	/*END		
_	/*====================================		===*/





	/*	*/
	return;	
5	•	
	/*	*/
	}	
	/*	*/
10		
	/+	*
	/* FUNCTION : SNR_Calc_NSR_enc ().	*/
	/* PURPOSE : This function performs the SNR e	Sumation at /
15	mic chooder .	*/
	/*	
	/* INPUT ARGUMENTS :	*/
	/* _ (FLOAT64 []) syn: synthetized signal.	
	/* _ (FLOAT64 []) lsfq: quntiscd LSF vector	
20	- `	*/
	/* _ (INT16) fame_class: input frame fame_	_class. */
	/*(INT16) l_frm: frame size.	*/
	/*	*/
	/* OUTPUT ARGUMENTS :	*/
25	/* _ (FLOAT64 *) NSR: estimated NSR at	the encoder */
	/+	*/
	/* INPUT/OUTPUT ARGUMENTS :	*/
	/* None. */	
	/*	*/
30	/* RETURN ARGUMENTS :	*/
-	/* None. */	
	/*=====================================	
	,	
	void SNR_Calc_NSR_enc (FLOAT64 syn[], FLOAT64	AT64 lefall INT16 V2d
20	- -	ame_class, FLOAT64 *NSR, INT16 1_frm)
35		mie_ciass, recator har, intro i_iiii)
	{	**
	/*	-
	FLOAT64 snr, eng, diff_lsf, diff_eng, val1,	val2;
40	INT16 i, vad_nsr;	

```
if (frame_class < 3)
 5
                    n_nois_ext_enc++;
            else
                     n_nois_ext_cnc=0;
             if (Vad == 0)
                    snr_count_vad++;
10
             clse
                     snr_count_vad=0;
             dot_dvector (syn, syn, &cng, 0, l_frm-1);
15
            if (snr_frm_count==0)
                     (*NSR) = 0.0;
20
                     vad_nsr = 1;
                     eng_m_enc = eng;
                     }
            else
25
                     diff_lsf=0;
                     for (i = 0; i < NP; i++)
                     diff_lsf += fabs(lsfq[i]-lsfq_mem_enc[i]);
30
                     diff_lsf *= 100;
                     diff_lsf_m_enc = diff_lsf_m_enc*0.75 + diff_lsf*0.25;
35
                     diff_eng = fabs(eng-eng_old_enc)/MAX(eng+eng_old_enc, 50*l_frm);
                     diff_eng_m_enc = diff_eng_m_enc*0.75 + diff_eng*0.25;
```

```
767
                    snr = cng/MAX(eng_m_enc, 0.1);
                    if (Vad == 0)
                            {
 5
                            if (eng_m_enc < 0.1)
                                    eng_m_enc = eng;
                            clse
                                    eng_m_enc = 0.75*eng_m_enc + 0.25*eng;
                            }
10
                    vad_nsr = 1;
                    if ((snr_count_vad > 15) && (snr_count_vad < 30) &&
15
                                    (diff_lsf_m_enc < 10) && (diff_lsf < 10) &&
                                             (diff_eng_m_enc < 0.25) \&\& (diff_eng < 0.25) \&\&
                                             (snr < 10))
                                                     vad_nsr=0;
20
                    else
                            if (((snr_count_vad > 15) || (snr_frm_count < 100)) &&
                                             (n_nois_ext_enc>10) && (diff_lsf_m_enc<7) &&
                                              (diff_lsf<7) && (diff_eng_m_enc<0.2) &&
                                                     (diff_eng<0.2) && (snr<8))
25
                                                             vad_nsr=0;
                            }
30
                    if ((NoisEng_enc < 0.0) && (snr_frm_count > 500))
                            vad_nsr = Vad;
35
                    if (vad_nsr == 0)
                             if (NoisEng_enc < 0.0)
                                    NoisEng_enc = eng;
                             elsc
40
```

```
NoisEng_enc = NoisEng_enc*0.85 + eng*0.15;
                            NoisEng_enc = MIN(NoisEng_enc, eng);
                           }
 5
                    val1 = MAX(NoisEng_enc - 100*1_frm, 0);
                    val2 = MAX(eng, 0.1);
                    (*NSR) = MIN(sqrt(val1 / val2), 1.0);
10
15
            snr_frm_count = MIN(snr_frm_count+1, 1000);
            for (i = 0; i < NP; i++)
                   lsfq_mem_enc[i] = lsfq[i];
20
            eng_old_enc = eng;
25
            return;
30
```

••	/69 ====================================
*	*/
* Conexant System Inc.	*/
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/*	=======================================
/* PROTOTYPE FILE : lib_snr.h	*/
_ /*====================================	=======================================
9	
/*	·*/
/* FUNCTIONS	*/
/*	
void SNR_init_lib (void);	
, , , , , , , , , , , , , , , , , , ,	
void SNR_Calc_NSR_enc (FLOAT64 [], FLOAT	T64 [], INT16, INT16, FLOAT64 *, INT16);
York Division (20.110 (), 20.11	- 47
/*	
/*	
/TEND	18000

	770
/*====================================	
/*=====================================	=======================================
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/* or adaptation) without the authorisation of C	Conexant System Inc. */
/*=====================================	
/* LIBRARY: lib_swb.c	*/
	=======================================
•	
/*	*/
/*	
/*INCLUDE	
/*	
#include "typedef.h"	
#include "main.h"	
#include "lib_swb.h"	
/*	+/
/* FUNCTIONS	*/
/*	
,	
	*/
/* FUNCTION: byte_swap_int16().	*/
/*	
/* PURPOSE : Byte swap a 16 bit integer nu	
/*	 +/
/* INPUT ARGUMENTS:	*/
/* _ (INT16) input: data to be swap	pped. */
/*	
/* OUTPUT ARGUMENTS :	*/
, 0011011210012211101	

	/* _ None	*/	//1			
	/*/* INPUT/OUTPUT ARGUMENTS :	·	*/	*/		
	/* _ None	*/				
5	/*		*/			
	/* RETURN ARGUMENTS :			*/		
	/* _ (INT16) output: data to be swap	-			=======	=*/
	/-=====================================					,
10	INT16 byte_swap_int16 (INT16 input)					
	{					
	/*			+/	1	
	INT16 output;					
15	/*			*	ı	
	/·			,		
	output = (input $>> 8 & 0x00ff$)^(inpu	ıt << 8);			
20	/*			+,	1	
	return output;					•
	/*			*	1	
25	}				•	
23	,					
	/*			+/		
	/*=====================================			=====		 =*/
30	/* FUNCTION: byte_swap_int32().		:	* /		
	/*		*/			
	/* PURPOSE : Byte swap a 32 bit integer poi			*/		
	/* Verified for swapping between SGI			Linux. '	*/	
	/+					
35	/* INPUT ARGUMENTS:	sad.	*,			
	/*(INT32) input: data to be swapp			′		
	/* OUTPUT ARGUMENTS :			*/		
	/* _ None	*/				
40	/*		*/			•

	772	
	/* INPUT/OUTPUT ARGUMENTS : */	
	/* _ None */	
	/**/	
	/* RETURN ARGUMENTS : */	
5		
_	/*====================================	*/
	/· 	٠.
	INT32 byte_swap_int32 (INT32 input)	
	{ .	
10	/**/	
	INT16 i;	
	INT32 output;	
	arrad ouiper,	
	/**/	
15	/*	
	•	
	for $(i = 0; i < 4; i++)$	
	*((char *)(&output) + i) = *((char *)(&input) + 3 - i);	
20	/**/	
	return output;	
	·	
	/**/	
	·	
25	}	
	/**/	
	/*====================================	*/
30	/* FUNCTION: byte_swap_int64(). */	
	/**/	
	/* PURPOSE : Byte swap a 64 bit integer point number. */	
	/* Verified for swapping between SGI Unix and PC Linux. */	
	/**/	
35	/* INPUT ARGUMENTS : */	
	/* _ (INT64) input: data to be swapped. */	
	/**/	
	/* OUTPUT ARGUMENTS : */	
	/* None */	
40	/**/	
40	1	

```
773
  /* INPUT/OUTPUT ARGUMENTS:
          _ None
                              */
  /* RETURN ARGUMENTS:
       _(INT64 ) output: swapped data.
  INT64 byte swap int64 (INT64 input)
       /*____*/
10
       INT16 i;
       INT64 output;
        /*_____*/
15
       for (i = 0; i < 8; i++)
            ((char *)(\&output) + i) = *((char *)(\&input) + 3 - i);
20
       return output;
25
  30 /* FUNCTION : byte swap_float32 ().
  /* PURPOSE: Byte swap a 32 bit floating point number.
        Verified for swapping between SGI Unix and PC Linux. */
  /+...
35 /* INPUT ARGUMENTS:
        _(FLOAT32 *) input: data to be swapped.
  /*-----
  /* OUTPUT ARGUMENTS :
       _ (FLOAT32 *) output: swapped data.
```

		774 */	
	/* INPUT/OUTPUT ARGUMENTS :	'/	
	/* _ None	*1	
		*/	
	/* RETURN ARGUMENTS :	- /	
5			
	/*=====================================	:======================================	======+/
		•	•
	void bytc_swap_float32 (FLOAT32 *input, FLOAT32 *	output)	
	{		
10	/*	*/	
	INT16 i;		
	/*	*/	
15			
	for $(i = 0; i < 4; i++)$		
	*((char *)(output) + i) = *((char *)(in	put) + 3 - i);	
	· / / / / / /	- ,	
	/+	*/	
20	•		
20	return;		
	icium,		
	/*	*/	
	}		
25			
	/*		
	/*		======*/
	/* FUNCTION: byte_swap_float64().	*/	
30	/*	 */	
	/* PURPOSE : Byte swap a 64 bit floating point numb	er. */	
	/* Verified for swapping between SGI Unix and	IPC Linux. */	`
	/*	*/	
	/* INPUT ARGUMENTS :	*/	
35	/*(FLOAT64_) input: data to be swapped.	*/	
	/*	*/	
	/* OUTPUT ARGUMENTS :	*/	
	/* _(FLOAT64) output: swapped data.	*/	
	/*	*	
40		•	
40	/* INPUT/OUTPUT ARGUMENTS:	*/	

		775	
/*	_ None	*/	
/*		*/	
/* RE	TURN ARGUMENTS:	*/	
/*	_ None.	*/	
5 /*===			=======================================
	,	•	_
void	byte_swap_float64(FLOAT6	64 *input, FLOAT64 *output)	
	{		
	/*	*/	
0			
	INT16 i;		
	••••••••••••••••••••••••••••••••••••••		
	/*	*/	
	•	·	
5	for $(i = 0; i < 8; i++)$	·	
5		+ i) = *((char *)(input) + 7 - i);	
	((Chai)(Output)	(cia)(input) · / 1),	
	/±		
	/*************************************		
0			
0	return;		
	44	*1	
		*/	
	}		
5 /*		*/	
/*===			
•		D*/	
/*===			
0			

/ / Conexant System Inc. 5 /* 4311 Jamboree Road /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ 15 /* PROTOYPE FILE: lib_swb.h /*____*/ /*----*/ INT16 byte_swap_int16(INT16); INT32 byte_swap_int32(INT32); INT64 byte_swap_int64(INT64); 25 byte_swap_float32(FLOAT32 *, FLOAT32 *); void void byte_swap_float64(FLOAT64 *, FLOAT64 *); 30 /*----- END -----

		711
/	* <u>====================================</u>	
/	*======================================	========*/
/	* Conexant System Inc.	*/
5 /	* 4311 Jamboree Road	*/
1	Newport Beach, CA 92660	*/
1		·*/
1	* Copyright(C) 2000 Conexant System Inc.	*/
/	*	·*/
) /	* ALL RIGHTS RESERVED:	*/
1	* No part of this software may be reproduced in a	ny form or by any */
1	* means or used to make any derivative work (suc	ch as transformation */
/	* or adaptation) without the authorisation of Con-	exant System Inc. */
/	*======================================	
5 /	* LIBRARY: lib_vad.c	*/
1	*======================================	
/	*	***************
/	* INCLUDE	*/
0 /	*	*/
	finclude "typedef.h"	
,	include typeder.ii	
5 4	finclude "main.h"	
	#include main.ii #include "const.h"	
	Finclude "gputil.h"	
	Finclude "moutil.h"	
	finclude "ext_var.h"	
0		
	#include "lib_lpc.h"	
1	#include "lib_vad.h"	
	*	
	/*FUNCTIONS	
,	/ *	·*/
,	/+	=======================================
,	* FUNCTION : VAD_init_lib ().	*/
0 ,	/*	+/

```
778
  /* PURPOSE : This function performs initialisation of the */
             global variables of the VAD library.
  /*_____
  /* INPUT ARGUMENTS:
5 /*
               _ None.
  /* OUTPUT ARGUMENTS:
               Nonc.
10 /* INPUT/OUTPUT ARGUMENTS:
  /*
               None.
   /* RETURN ARGUMENTS:
   /*
              _ None.
   void VAD_init_lib (void)
          {
20
           INT16 i, j;
           ini_svector (lag_buf, 0, LTP_BUFF_SIZE-1, 20);
25
           ini_dvector (pgain_buf, 0, LTP_BUFF_SIZE-1, 0.7);
           Vad
                       = 1;
30
           ini_svector (flag_vad_mem, 0, 1, 1);
35
           flag = 1;
           count_sil = 0;
           count_ext = 0;
           dec3_flg_mem = 0;
40
```

```
pitch_gain_mean = 0.5;
            for (i = 0; i < VAD\_MEM\_SIZE; i++)
 5
                   for (j = 1; j < NP; j++)
                           vad_lsf_mem[i][j] = vad_lsf_mem[i][j-1] + 0.05;
            min_energy = MAX_ENERGY;
            mean\_energy = 0.0;
10
            mean_max = 0;
            mcan_s_energy = 0.0;
            ini_dvcctor(prev_cml_lsf_diff, 0, VAD_MEM_SIZE-1, 0.0);
                                           0, VAD_MEM_SIZE-1, 0.0);
15
            ini_dvector(prev_energy,
            snr = 0.0;
            onset_flg = 0;
            count_onset = 0;
20
            count_noise = 0;
            return;
25
30
   /* FUNCTION
                     : VAD_voicc_detection ().
                   : This function performs Voice Activity Detection. */
   /* PURPOSE
   /* INPUT ARGUMENTS:
           _(FLOAT64 []) x :
                                input frame.
           _(FLOAT64 []) rc:
                                reflection coefficients.
           _(FLOAT64 ) pderr: energy of residual.
           _ (INT16 []) lag_mem: history of pitch lag.
40 /*
```

```
_ (FLOAT64 []) pitch_gain_mem: history of pitch gain. */
   /*
   /*
          (FLOAT64 []) lsf: lsf vector.
          _ (FLOAT64 []) rxx: autocorrelation coefficients. */
   /*
   /*
          _(INT64) frm_count: frame counter.
          _(INT16 ) flag_vad_mem_1: past vad decision (n-1). */
5 /*
          _(INT16 ) flag_vad_mem_2: past vad decision (n-2). */
   /*
   /* OUTPUT ARGUMENTS :
          _ (INT16 *) flag_vad: vad decision.
   /* INPUT/OUTPUT ARGUMENTS:
                                             */
   /*
                None.
   /* RETURN ARGUMENTS:
15 /*
                None.
   void VAD_voice_detection(FLOAT64 x [], FLOAT64 rc [], FLOAT64 pderr,
                          INT16 lag_mem [], FLOAT64 pitch_gain_mem [],
                          FLOAT64 lsf [], FLOAT64 rxx [], INT64 frm_count,
20
                          INT16 *flag_vad, INT16 flag_vad_mem [])
25
           FLOAT64 pitch_gain_avg;
           INT16 decision;
           INT16 i;
30
           INT16 pflag, pflag1, pflag2;
           INT16 dec3_flg;
           FLOAT64 lags_std;
           FLOAT64 max_frame, val;
35
           FLOAT64 w1, w2;
           FLOAT64 norm_energy;
           FLOAT64 nnorm energy;
           FLOAT64 *sum2_p, *sum2_a, *temp;
           FLOAT64 norm cml lsf diff;
           FLOAT64 spd;
40
```

```
FLOAT64 vad_partial_lpc_gain, vad_lpc_gain;
            FLOAT64 nthrs;
            FLOAT64 cml_lsf_diff = 0.0;
            FLOAT64 *sum1_lsf, *sum2_lsf;
 5
                             Memory allocation
            sum1_lsf = dvector (0, NP-1);
10
            sum2 lsf = dvector (0, NP-1);
15
         dccision = NOISE;
            dec3 flg = 0;
            max_framc = -(FLOAT64)INT_MAX;
            for (i = 0; i < L_FRM; i++)
20
                     val = fabs(x [i]);
                     if (val > max_frame)
                            max_frame = val;
25
                                                                       */
                      Calculate the oder VAD_NP prediction error
30
             vad_partial_lpc_gain = 1.0;
            for(i = 0; i < VAD\_LPC\_ORDER; i++)
                    vad_partial_lpc_gain *= (1.0 - rc[i]*rc[i]);
35
         vad_lpc_gain = vad_partial_lpc_gain;
         for (i=VAD_LPC_ORDER; i<NP; i++)
            vad_lpc_gain *= (1.0-sqr(rc[i]));
40
```

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```
vad_lpc_gain = -10.0*log10(vad_lpc_gain+EPSI);
                    Calculate the average prediction gain and the lag
                            standard deviation
5
           VAD_mean_std_calc (lag_mem, pitch_gain_mem, &lags_std, &pitch_gain_avg);
           pitch_gain_mean = 0.8*pitch_gain_mean + 0.2*pitch_gain_avg;
10
            /*<u>-----</u>*/
                      Sct-up the classification flags
15
            if (pitch_gain_mean > 0.7)
                   pflag2 = 1;
            else
                   pflag2 = 0;
20
            if ( (lags std < 1.30) && (pitch_gain_mean > 0.45))
                   pflag1 = 1;
            else
                   pflag1 = 0;
25
            pflag = (flag_vad_mem [0] || pflag2) && pflag1;
30
                        Calculate the signals energies
                                                              */
            val = rxx[0] / (FLOAT64)L_LPC;
            norm_energy = 10.0 * log10 (vad_partial_lpc_gain * val + EPSI);
35
            norm_energy = MAX (0.0, norm_energy);
            nnorm_energy = 10.0 * log10 (val + EPSI);
            nnorm_energy = MAX (10.0, nnorm_energy);
40
```

```
783
                   Calculate the weighted sum of lsf coefficeints
 5
            add_dvector (lsf,
                                   &vad_lsf_mem[0][0], sum1_lsf, 0, NP-1);
            add_dvector (sum1_lsf, &vad_lsf_mem[1][0], sum1_lsf, 0, NP-1);
            wad_dvector(sum1_lsf, 0.25, &vad_lsf_mem[2][0], 0.25, sum1_lsf,
10
           0, NP-1);
            add_dvector (&vad_lsf_mem[0][0], &vad_lsf_mem[1][0], sum2_lsf,
           0, NP-1);
15
            wad dvector (sum2 lsf, 0.3333, &vad_lsf_mcm[2][0], 0.3333, sum2_lsf,
           0, NP-1);
20
                         min_energyimum energies tracking
            if (frm count < 65)
25
                    if ( norm_energy < min_energy )
                            min_energy
                                            = norm_energy;
                            prev_min_energy = norm_energy;
30
                    if ((frm_count % VAD_MTN_MEM_SIZE) == 0)
                            i = frm_count / VAD_MIN_MEM_SIZE - 1;
35
                            min_energy_mcm [i] = min_energy;
                                                   = MAX_ENERGY;
                            min_energy
                   }
```

```
if ((frm_count % VAD_MIN_MEM_SIZE) == 0)
                  {
                   prev_min_energy = min_energy_mem[0];
                   for (i = 1; i < VAD_MIN_MEM_SIZE; i++)
5
                           if (min_energy_mcm[i] < prev_min_energy )
                                  prev_min_energy = min_energy_mem[i];
                          }
                   }
10
           if( frm_count >= 65 )
                   if( (frm_count % VAD_MIN_MEM_SIZE ) == 1)
15
                           min_energy = prev_min_energy;
                           next_min_energy = MAX_ENERGY;
                   if( norm_energy < min_energy )
20
                          min_energy = norm_energy;
                   if( norm_energy < next_min_energy )
                           next_min_energy = norm_energy;
25
                   if( (frm_count % VAD_MIN_MEM_SIZE) == 0)
                           for (i = 0; i < VAD_MIN_MEM_SIZE-1; i++)
                                  min_energy_mem[i] = min_energy_mem[i+1];
30
                           min_energy_mem[VAD_MIN_MEM_SIZE-1] = next_min_energy;
                           prev_min_energy = min_energy_mem[0];
                           for (i = 1; i < VAD\_MIN\_MEM\_SIZE; i++)
35
                                   if (min_energy_mem[i] < prev_min_energy )
                                           prev_min_energy = min_energy_mem[i];
                                   }
                           }
40
```

```
}
               <u>-----</u>*/
                   First INIT FRAME frames parameters calculation
5
           if (frm count <= INIT_FRAME)
                   {
                   if (pflag ==0 &&
               ( ((max_frame/mean_max < 12.0) && (nnorm_energy <= 30.0) &&
10
                 (nnorm_energy-norm_energy < 9.0) &&
                 (rc[0] < -0.55 \&\& vad_lpc_gain < 5.0) \parallel
                 (vad_lpc_gain) < 2.75)) || (nnorm_energy <= 10.0) ||
                 ( (nnorm_encry \le 25.0) \&\& ((rc[0] \le -0.55 \&\&
15
                  vad_lpc_gain < 5.0 (vad_lpc_gain) < 2.75)&&
                  (\max_frame/mcan_max < 12.0))))
                           decision = NOISE;
                   else
                           decision = VOICE;
20
                   mean energy = (mean energy*( (FLOAT64)(frm_count-1)) +
                                                           norm_energy) / (FLOAT64) (frm_count);
                   mean_max = (mcan_max*( (FLOAT64)(frm_count-1)) +
25
                                                          max frame)/(FLOAT64) (frm_count);
                   w1 = (FLOAT64) (frm_count -1);
                   w2 = 1.0;
30
                   wad_dvector (mcan_lsf, w1, lsf, w2, mcan_lsf, 0, NP-1);
                   wl = 1.0 / (FLOAT64) frm_count;
                   sca_dvector (mcan_lsf, w1, mean_lsf, 0, NP-1);
35
                   cpy dvector (mean_lsf, norm_mcan_lsf, 0, NP-1);
                   }
40
```

```
First INIT FRAME frames final VAD flag set-up
            (*flag vad) = decision;
 5
                            Parameters calculation
10
            if (frm_count >= INIT_FRAME )
                    {
                    if (frm_count == INIT_FRAME)
                            mean_s_energy = MAX(mean_energy - 12.0,0.0);
15
                             Spectral Distortion Calculation
                    sum2_p = dvector(0, NP-1);
20
                    sum2_a = dvector(0, NP);
                    temp = dvector(0, NP-1);
                    LPC lsftop (sum2 lsf, sum2_p, NP);
25
                    LPC_ptoa (sum2_p, sum2_a, NP);
30
                    VAD_itakura_saito (rxx, sum2_a, &spd, NP);
                    spd /= (EPSI + pderr);
35
                    dif_dvector (lsf, norm_mean_lsf, temp, 0, NP-1);
            dot_dvector (temp, temp, &norm_cml_lsf_diff, 0, NP-1);
40
                    dif dvector (sum1 lsf, mean lsf, temp, 0, NP-1);
```

```
787
            dot_dvector (temp, temp, &cml_lsf_diff, 0, NP-1);
                     free_dvector(sum2_p, 0, NP-1);
5
                     free_dvector(sum2_a,
                                                        0, NP-1);
                      free_dvector(temp,
10
                      if((frm\_count == INIT\_FRAME) \parallel ((prev\_cml\_lsf\_difl[0] < 0.5c-4)
                                                                                                     &&
   (cml_lsf_diff > 0.1e-3)))
                                      cml_lsf_diff_filt = cml_lsf_diff;
15
                      else
                                      cml_lsf_diff_filt = 0.4*cml_lsf_diff +
   0.6*cml_lsf_diff_filt;
20
                      dec3 flg = 0;
25
                      if (snr \le 5.0)
                              nthrs = 3.0;
                      clsc if (snr <= 10.0)
                              nthrs = 4.7;
                      else
30
                              nthrs = 5.5;
                      if (pflag == 1 && nnorm_energy > 10.0)
35
                              decision = VOICE;
                      elsc if (( norm_energy >= mean_s_energy + 6.5) ||
                      (cml_lsf_diff > 1000.0c-6))
                 {
40
                               decision = VOICE;
```

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```
788
                              count_ext = 0;
                              }
                      elsc if( (norm_energy - mean_s_energy) < nthrs)
                              decision = NOISE;
 5
                      else
                              {
                              decision
                                               = VOICE;
                              dec3_flg
                                               = 1;
                              count_ext
                                               = 0;
10
                              }
                      if ( snr \le 5.0)
15
                              onset_trhsd = 6.0;
                      else if ( snr <= 10.0)
                              onsct_trhsd = 8.0;
                      else
                              onset_trhsd = 10.0;
20
               if ((nnorm_energy > 0.5*(prev_energy[1] + prev_energy[0])+
                  onset_trhsd) || ((nnorm_energy > 35.0) &&
25
                  (spd > 2.5) || (norm_cml_lsf_diff > 0.008) ||
                  (norm_energy-mean_s_energy>10.0)))) {
                  onset_flg = 1;
                  flag = 1;
               }
30
               clsc {
                if (nnorm_energy <= 10.0) {
                  onset_flg =0;
                }
               }
35
               if (onsct_flg){
                 count_onsct++;
               }
               if ( count_onset == 4){
40
                 onset_flg = 0;
```

```
789
               count onset = 0;
               count_ext = 4;
              }
 5
              if (onset_fig == 0 && pflag == 0 &&
                (( (max_frame/mean_max < 12.0) && (nnorm_energy <= 30.0)
                && (nnorm_energy-norm_energy < 9.0) &&
                ((rc[0] < -0.55 \&\& vad_lpc_gain < 5.0))
10
                (vad_lpc_gain) < 2.75)) || (nnorm_energy <= 10.0)
                || ( (nnorm_cnergy <= 25.0) && ( (rc[0] < -0.55 &&
                vad_{pc}gain < 5.0 (vad_{pc}gain) < 2.75) &&
                (\max_{\text{frame/mcan_max}} < 12.0))))
15
                                     decision = NOISE;
                                     flag =0;
            else
20
                             if ( (prev_energy[0] - nnorm_energy > 10.0) &&
                 (nnorm\_energy < 20.0) \&\& (max\_framc/mcan\_max < 12.0))
                                     decision = NOISE;
                                     flag = 0;
25
                             else
                 if (frm_count <= 65)
30
                 decision = VOICE;
             if (pflag == 0 && count_noise > 8 && (nnorm_energy < 30.0) &&
               (\max_frame/mean_max < 15.0) &&
35
               (norm_energy - mcan_s_energy < 6.5))
                             decision = NOISE;
                             flag = 0;
                             }
40
```

```
790
             else
                   {
                            if (pflag ==0 && count_noise > 4 && cml_lsf_diff < 900.0c-6
                 && (norm_cml_lsf_diff < 3500.0e-6) && (onset_flg == 0)
                 && (max_frame/mean_max < 15.0) &&
 5
                (norm_energy - mean_s_energy < 6.0))
                  {
                   decision = NOISE;
                   flag =0;
10
                  }
                            clse if ((nnorm_energy> 25.0) && (flag_vad_mcm [1] == VOICE) &&
                   (flag_vad_mem [0] == VOICE) && (decision == NOISE) &&
                   (norm_cml_lsf_diff > 4000.0e-6))
                     decision = VOICE;
15
                            }
              if (decision == NOISE)
                 count noise++;
20
              elsc
                 count_noise=0;
25
                     if ((norm_cml_lsf_diff > 0.0003) &&
                             ( nnorm_energy - prev_energy[2] \leq -12.0))
                            count_ext = 4;
30
                     if (flag == 1)
35
                   if (((nnorm_energy> 25.0) &&
                      (flag_vad_mem [1] == VOICE) &&
                       (flag_vad_mem [0] == VOICE) &&
                       (decision == NOISE)) || (onset_flg))
```

```
791
                                                        {
                                                         count_ext++;
                                                         decision = VOICE;
                         if(count_ext <= 26-0 && snr <= 0.0)
 5
                          flag = 1;
                         else if(count_ext <= 22-0 && snr <= 5.0)
                          flag = 1;
                         else if(count_ext <= 20-0-0 && snr <= 6.5)
10
                          flag = 1;
                         clse if(count_cxt <= 16-0-2 && snr <= 8.0)
                          flag = 1;
                         clsc if(count_ext <= 14-2-2 && snr <= 11.0)
                          flag = 1;
                         elsc if(count_ext <= 10-2-2 && snr <= 14.0)
15
                          flag = 1;
                         clse if(count_cxt <= 8-2-2 && snr <= 17.0)
                          flag = 1;
                         else if(count_ext <= 3 && snr <= 21.0)
20
                        \cdot flag =1;
                         else if(count_ext \leq 1 && snr \geq 21.0)
                          flag = 1;
                                                         elsc
25
                                                                  flag = 0;
                                                                  count_ext=0;
                                                         }
30
                     else
                               flag = 1;
35
                               count_ext = 0;
                              }
                      if (decision == NOISE)
40
```

```
count_ext=0;
                                  Compute mean_energy
 5
                    if (norm_energy > min_energy + 12.0)
                            mean_energy = 0.9 * mean_energy + 0.1 * norm_energy;
10
                                    Compute snr
                    snr = mean_energy - mean_s_energy;
15
                                 Compute the new mean_s_energy
20
                    if (((norm_energy < MAX(mcan_s_energy,min_energy) + 3.5)
                                                                                    && (!pflag)) || decision
   == NOISE)
                            {
25
                            mcan_max = MAX(EPSI,0.95 * mean_max + (1.0-0.95)* max_frame);
                            count_sil++;
                            if (count_sil < INIT_COUNT)</pre>
30
                                    {
                                    wl
                                           = 0.75;
                                    w2 = 0.60;
                            elsc if (count_sil < INIT_COUNT + 10)
35
                                    {
                                    wl
                                           = 0.85;
                                    w2 = 0.65;
                                    }
                            else if (count_sil < INIT_COUNT + 20)
40
```

```
793
                                            = 0.85;
                                    wl
                                    w2 = 0.65;
                            clsc
 5
                                            = 0.65;
                                    wl
                                    w2 = 0.65;
                                    }
                            wad_dvector (mean_lsf, w2, lsf, (1.0 - w2), mean_lsf,
10
           0, NP-1);
                            if (nnorm_energy > 18.0)
                                    mean_s_energy = w1 * mean_s_energy +
                                                             (1.0- w1) * norm_energy;
15
                            mean_s_energy = MAX(mean_s_energy, 0.0);
20
                    wad_dvector (norm_mean_lsf, 0.85, lsf, (1.0 - 0.85),
                                                                                      norm_mean_lsf, 0, NP-1);
25
                             Current frames final VAD flag set-up
                    (*flag_vad) = decision;
30
                    if ((frm_count >64) && (mean_s_energy +10.0 < min_energy ) &&
                            (!pflag))
35
                mean_s_energy = MAX(0.5*( min_energy+mean_s_energy),0.0);
                mcan_s_energy = 0.75*mean_s_energy +
                                                     0.25*min_cnergy;
                mean_s_energy = MAX(mean_s_energy, 0.0);
40
```

```
794
                             count_sil = 0;
                    else if ((frm_count >64) && (mean_s_energy > min_energy + 4.0))
                             mean_s_energy = MAX(min_energy, 0.0);
 5
                             count_sil = 0;
                            }
10
                                                                         */
                   Save parameters for the processing of the next frame
15
             sfr_dvector (prev_energy, prev_energy, 1, 0, VAD_MEM_SIZE-1);
             prev_energy[0] = nnorm_energy;
             sfr_dvcctor (prev_cml_lsf_diff, prev_cml_lsf_diff, 1, 0, VAD_MEM_SIZE-1);
             prev_cml_lsf_diff[0] = cml_lsf_diff;
20
                                             vad_lsf_mem[2], 0, NP-1);
             cpy_dvector (vad_lsf_mem[1],
                                              vad_lsf_mem[1], 0, NP-1);
             cpy_dvector (vad_lsf_mem[0],
                                                              vad isf mem[0], 0, NP-1);
             cpy_dvector (lsf,
25
             dec3_flg_mem = dec3_flg;
30
             free_dvector (sum1_lsf, 0, NP-1);
             free_dvector (sum2_lsf, 0, NP-1);
 35
              rcturn;
             }
 40
```

```
795
  /* FUNCTION : VAD_itakura_saito ().
5 /*----*/
  /* PURPOSE : This function calculate the Itakura Saito
  /*
          distance.
  /* INPUT ARGUMENTS:
10 /* _(FLOAT64 []) r : input signal autocorrelation.
  /* _ (FLOAT64 []) a : LPC filter coefficients.
  /* _(INT16 ) P : filter order.
  /*_____
  /* OUTPUT ARGUMENTS :
15 /* _(FLOAT64 *) isd : output Itakura Saito distance.
                                          */
  /* INPUT/OUTPUT ARGUMENTS:
     None.
20 /* RETURN ARGUMENTS : Nonc.
  VAD_itakura_saito (FLOAT64 r [], FLOAT64 a [], FLOAT64 *isd, INT16 P)
  void
25
        INT16 k, m;
        FLOAT64
                   sum;
        FLOAT64
                  r_a[20];
30
        for (k = 0; k \le P; k++)
35
              sum = 0.0;
              for(m = 0; m \le P-k; m++)
                   sum = sum + a[m]*a[m+k];
              r_a[k] = sum;
             }
40
```

```
*/
        sum = 0.0;
5
        for (m = 1; m \le P; m++)
           sum = sum + r[m]*r_a[m];
        (*isd) = (r[0]*r_a[0] + 2*sum);
10
        rcturn;
15
20
  /* FUNCTION : VAD_mean_std_calc ().
25 /* PURPOSE : This function calculate average prediction
  /* gain and the lag standard deviation
  /* INPUT ARGUMENTS:
        _ (INT16 []) lags_mem: history of pitch lag. */
       [ (FLOAT64 []) pitch_gain_mem: history of pitch gain. */
30 /*
  /* OUTPUT ARGUMENTS :
        (FLOAT64 *) l_std: lag standard deviation. */
      __ (FLOAT64 *) pg_mean: average prediction gain. */
   /* INPUT/OUTPUT ARGUMENTS:
                                    */
             _ Nonc.
   /* RETURN ARGUMENTS:
40 /*
           _ None.
```

797 void VAD mean std calc (INT16 lags_mem [], FLOAT64 pitch_gain_mcm [], FLOAT64 *1 std, 5 FLOAT64 *pg_mcan) */ INT16 i; 10 FLOAT64 val, sum1, sum2; sum1 = 0.0;15 sum2 = 0.0; $for(i = 0; i < LTP_BUFF_SIZE; i++)$ sum1 += (FLOAT64) lags_mem [i]; sum2 += pitch_gain_mem [i]; 20 sum1 = sum1 / (FLOAT64)LTP_BUFF_SIZE; 25 (*pg_mean) = sum2 / (FLOAT64)LTP_BUFF_SIZE; 30 $(*I_std) = 0.0;$ for(i = 0; $i < LTP_BUFF_SIZE$; i++) 35 { = (FLOAT64) lags_mem [i] - sum1; $(*l_std) += val*val;$ $(*l_std) = sqrt((*l_std) / (FLOAT64)(LTP_BUFF_SIZE - 1));$ 40

	/*	 798 */	
	return;		
5	/*	 */	
	/*	 */	
10	/*		

		199
	/+	
	/* Conexant System Inc.	*/
5	/* 4311 Jamboree Road	*/
	/* Newport Beach, CA 92660	*/
	/*	************
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	/*	*/
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	/* means or used to make any derivative work (su	ich as transformation */
	/* or adaptation) without the authorisation of Con	nexant System Inc. */
	/*=====================================	
5	/* PROTOTYPE FILE : lib_vad.h	*/
		=======================================
	,	
	/+	*/
	/* FUNCTIONS	
^	/*	
J	/	·
	'void VAD_init_lib (void);	
	voia v. 22 (v. 2007),	
	void VAD_voice_detection (FLOAT64 [], FLOAT	T64 (I. FLOAT64, INT16 (I. FLOAT64 *,
5		
,	LONIOT [], LECTION [], INTO 1, I	(),
	void VAD_itakura_saito (FLOAT64 [], FLOAT	CALL ELOATEA * INTLE):
	Void VAD_itaktira_salto (FLOATO4 [], FLOAT	of [], I LOXIOT , INVIO),
_		C C CATCA + C CATCA +).
)	void VAD_mean_std_calc(INT16 [], FLOAT64 [[], FLOA164 *, FLOA164 *);
	/*END	
	/	
5		

	800	==========
		=======*/
nexant System Inc.	*/	
11 Jamborec Road	*/	
ewport Beach, CA 92660	*/	-
ppyright(C) 2000 Conexant System Inc.	*/	
LL RIGHTS RESERVED:	*/	
part of this software may be reproduced in an	y form or by any */	
eans or used to make any derivative work (such	n as transformation */	
adaptation) without the authorisation of Cone	xant System Inc. */	·======*/
=======================================	*/	
ROTOTYPE FILE : main.h	•	=======*/
	*/	
INCLUDE		
	,	
clude <stdio.h></stdio.h>		
clude < stdlib. h>		
clude < math. h >		•
clude <string.h></string.h>		
clude <malloc.h></malloc.h>		
clude <crrno.h></crrno.h>		
clude <crmo.h> clude <memory.h></memory.h></crmo.h>		
		·
clude <memory.h></memory.h>		•
clude < memory.h > clude < float.h > clude < limits.h >	•	
clude <memory.h> clude<float.h></float.h></memory.h>		
clude < memory. h > clude < float. h > clude < limits. h >		
clude < memory. h > clude < float. h > clude < limits. h >	*/	
clude < memory. h > clude < float. h > clude < limits. h >	*/	
clude < memory. h > clude < float. h > clude < limits. h >	*/	

	DEFINE -			
5 /*			*/	
#define	EXIT_FAILURE 1			
#define	NB_MAX_CHAR		128	
#define	NB_ALGO_MAX		100	
#define	MAXSENTENCE		100	
#definc	NB_MAX_VEC	100		
/ *	•			
#define PI	3.1415926535897931	15997963468	8544185161590576171875	
#define TV	WO_PI 6.2831853071795862:	31995926937	708837032318115234375	
*/				
#define P	1 3.141592654	,		
				:==
/*	END		*/	

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802 /* Conexant System Inc. 5 /* 4311 Jamboree Road /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 10 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ 15 /* MAIN PROGRAM : main_dec.c */ #include "typedcf.h" #include "main.h" #include "const.h" 25 #include "glb_var.h" #include "mcutil.h" #include "gputil.h" 30 #include "decoder.h" #include "lib_io.h" #include "lib_ini.h" #include "lib_cpr.h" 35 #ifdef DIAG_SMV #include "lib_dia.h" 40

#end	•		
•	MAIN		=*/
5 /*==			=*/
•			
int r	main(int argc, char *argv[])	•	
{	/*	*/	
10	/* Pointers to I/O files */		
10	/*	*/	
	FILE *fp_speech_out;		
	FILE *fp_bitstream;	•	
15			
	/*	*/	
	/* I/O buffers */	*/	
	/· 	,	
20	FLOAT64 dcc_sigout[L_FRM];		
	INT16 i, switch_flag=1;		
	INT16 *s_y;		
	INT16 serial[PACKWDSNUM];		
25	/*	*/	
	/* Print copyright information	*/	
	/*	*/	
30 #ifd	lef VERBOSE		
	print_copyright ();		
#en	ais.		
35	idii		
J-	/*	*/	
	/* Check the number of input parameters	*/	
	/*	*/	
40	if (argc < 3)		

WO 01/22402 804 #ifdef VERBOSE printf("\n%s bitstream_file decoded_speech_file \n\n", argv[0]); #endif exit (1); } Open IO files fp_bitstream = file_open_rb (argv [1]); fp speech_out = file_open_wb (argv [2]); if (argc == 4)switch_flag = atoi(argv[3]); 20 #ifdef VERBOSE : %s\n",argv[0]); printf("\nExecutable printf("Input bitstream : %s\n", argv[1]); printf("Output speech : %s\n", argv[2]); 25 #ifdef BYTE_SWAP_OUTPUT printf("Output is byte-swapped\n"); 30 #endif #endif

Open DIAGNOSTICS files

BNSDOCID: <WO _ 0122402A1_l_>

40

35

#ifdef DIAGNOSTICS

5

10

DIA_dec_open_files (); #endif 5 Memory Allocation 10 $s_y = svector(0, L_FRM-1);$ INI_allocate_memory (); 15 Algorithm Initialization INI_init_decoder(); 20 25 while ((i = fread(serial, sizeof(short), PACKWDSNUM, fp_bitstream)) == PACKWDSNUM) frm_count++; 30 #ifdef VERBOSE printf ("[%ld frame(s), %6.3f sec.]\r", frm_count, (FLOAT64)(frm_count*L_FRM)/(FLOAT64)FS); 35 #endif Decoding Bitstream 40

```
806
                    dec_smv_frame(serial, dcc_sigout, switch_flag);
                    IO_writesamples (fp_speech_out, dec_sigout, s_y, L_FRM);
 5
                    }
                                                               */
                             Memory Deallocation
10
            free_svector (s_y, 0, L_FRM-1);
            INI_dcallocate_memory ();
15
                              Close 10 files
            fclose (fp_bitstream);
            fclose (fp_speech_out);
20
   #ifdef DIAGNOSTICS
                            Close DIAGNOSTICS files
25
             DIA_dec_close_files ();
30 #endif
             printf("\n");
 35
             rcturn 0;
             }
 40
```

807
/**/
/*====================================
/**/
/*=====================================

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/ 	808
/* Conexant System Inc.	*/
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/* means or used to make any derivative work (su	ich as transformation */
/* or adaptation) without the authorisation of Con	nexant System Inc. */
/*=====================================	=======================================
5 /* MAIN PROGRAM : main_enc.c	*/
/*=====================================	=======================================
/*	*/
/* INCLUDE	
	*/
#include "typedef.h"	
#include "main.h"	
#include "const.h"	
25 #include "glb_var.h"	
#include "mcutil.h"	
#include "gputil.h"	
30 #include "encoder.h"	
#include "lib_ini.h"	
#include "lib_io.h"	
#include "lib_cpr.h"	
35 #include "lib_ppr.h"	
#include "lib_sns.h"	
#ifdef DIAG_SMV	
40 #include "lib dia.h"	

		MAIN			
		=======================================			*******
					•
ain (int a	rgc, char *	'argv[])			·
{					
/*	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			*/	
/*		I/O Files	*/		
/*				*/	
FILE	*fp_speec	ch in			
	*fp_bitstr	_			
	·- : *fp_signa				•
	*fp_mode				
/*				*/	
/*		Variables	*/	•	
-				*/	
	AT64 *px;				
FLO	AT64 avg_	_rate;			
INT	l6 flat_fla	aσ=0·			
	6 switch				
	6 input_				
	signalin				
INT	l6 Voicin	ngClass=0;			
				*/	
/*					
/* /*		I/O buffers	*/		

	/**/
	/* Print copyright information */
	/**/
5	
#ifdef \	VERBOSE
	print_copyright ();
10 #endif	
10 #elidii	
	/**/
	/* Parameters Set-up */
	/**/
15	
	INI_parameters_setup (argc, argv, &fp_speech_in, &fp_bitstream,
	&fp_mode, &fp_signaling, &smv_mod
#:ca~e:	DIAG SMV
#IIde1 1	DIAG_SMV
	/**/
	/* Open DIAGNOSTICS files */
	/**/
25	
	DIA_enc_open_files ();
#endif	
#Clidii	
30	/**/
	/* Memory Allocation */
	/**/
	$s_x = svector(0, L_FRM-1);$
35	$s_y = svector(0, L_FRM-1);$
	sig_in = dvcctor (0, L_FRM-1);
	sign_buf = dvector (0, L_FRM+ENH_DELAY-1);
	sig ppr = dvector (0, L_PP-1);
40	sig out = dvector (0, L_FRM-1);

	serial = svector (0, PACKWDSNUM-1);
	INI_allocate_memory ();
5	
	/**/ /* Algorithm Initialization */
	/* <u></u> */
10	INI_init_encoder();
	/**/
	/* Read the input signal */
15	/+
15	px = sig_in+L_FRM-L_LPCLHD-ENH_DELAY;
	IO_readsamples(fp_speech_in, s_x, px, L_LPCLHD+ENH_DELAY);
20	/**/
	/* Silence enhancement of the Input signal frame */
	/**/
	PPR_silence_enhan (px, px, L_LPCLHD+ENH_DELAY);
25	
	/**/
	/* High pass filter at 80 Hz */
	/
30	PPR_highpass (L_FRM, sig_in);
	cpy_dvcctor(sig_in, signn_buf+ENH_DELAY, 0, L_FRM-1);
	/**/
35	/* Input Speech Enhancement */
	/**/
	SNS_modified_noise_suprs(VoicingClass, sig_in);
	SNS_modified_noise_suprs(VoicingClass, sig_in+L_FRM/2);
40	

```
812
        cpy_dvcctor (sig_in+L_FRM-L_LPCLHD, sig_ppr+L_PP-L_LPCLHD, 0, L_LPCLHD-1);
        /*____*/
                Low pass filter for tilt compensation
5
        px = sig_ppr+L_PP-L_LPCLHD;
        PPR_lowpass (px, (INT16)(L_LPCLHD), smv_mode, FlatSp_Flag);
10 /*------*
  /* parabonoparabonoparabonoparabona ANALYSIS PART parabonarabonoparabonoparabonopara */
  15
        input_samples = L_FRM;
        while (input_samples == L_FRM)
20
              frm_count++;
25
  #ifdef VERBOSE
              printf ("[%ld frame(s), %6.3f sec.]\r", frm_count,
30
        (FLOAT64)(frm_count*L_FRM)/FS);
  #endif
                      Reading the signaling information
35
              if (fp_signaling != NULL)
                   fread(&signaling, sizeof(char), 1, fp_signaling);
              else
                   signaling = (char) 0;
```

```
813
                                Read the input signal
                                                                       */
                   /*_____*/
                   input_samples = (INT16)IO_rcadsamples (fp_speech_in, s_x, sig_in,
 5
                   L FRM);
10
                         Silence enhancement of the Input signal frame
                   PPR_silence_enhan (sig_in, sig_in, input_samples);
15
                         Zero Padding if necessary for the last frame
                   ini_dvector(sig_in, input_samples, L_FRM-1, 0.0);
20
                   PPR_highpass (L_FRM, sig_in);
                   cpy dvector(signn_buf+L_FRM, signn_buf, 0, ENH_DELAY-1);
                   cpy_dvcctor(sig_in, signn_buf+ENH_DELAY, 0, L_FRM-1);
25
                   if (FlatSp_Flag == 1)
                                  MIN_GAIN = -14.0;
30
                               Input Speech Enhancement
                   SNS modified_noise_suprs(VoicingClass, sig_in);
                   SNS_modified_noisc_suprs(VoicingClass, sig_in+L_FRM/2);
35
                           Update the pre-processed speech buffer
40
```

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```
814
                    cpy_dvector (sig_ppr+L_FRM, sig_ppr,
                                                             0, L_PP-L_FRM-1);
                    cpy_dvector (sig_in, sig_ppr+L_PP-L_FRM, 0, L_FRM-1);
                             Low pass filter for tilt compensation
 5
                    px = sig_ppr+L_PP-L_FRM;
                    PPR_lowpass (px, L_FRM, smv_mode, FlatSp_Flag);
10
   #ifdef SCRATCH_PAD_MEM
                    ini_dvcctor(sig_in, 0, L_FRM-1, -1.0);
                    ini_dvcctor(signn_buf, 0, L_FRM-1, -1.0);
15
   #endif
                    enc_smv_frame (sig_ppr, serial, sig_out, smv_mode,
                                                            switch_flag, &FlatSp_Flag, signaling,
                                                                    &avg_rate, &VoicingClass);
20
   #ifdef DIAG_SMV
                    IO_writesamples (fdia_sp_enc, sig_out, s_y, L_FRM);
25
   #endif
30
                    fwrite(serial, sizeof(short), PACKWDSNUM, fp_bitstream);
                    }
                            Print the Average bit rate
35
            printf("\n\t\tavcrage_ratc = %f\n", avg_rate);
                                                               */
                             Memory Deallocation
40
```

```
815
           free svector (s_x, 0, L_FRM-1);
           free_svector (s_y, 0, L_FRM-1);
 5
           free_dvector (sig_in, 0, L_FRM-1);
           free_dvector (signn_buf, 0, L_FRM+ENH_DELAY-1);
                                        0, L_PP-1);
           free_dvector (sig_ppr,
                                          0, L_FRM-1);
           free_dvector (sig_out,
10
           free_svector (serial, 0, PACKWDSNUM-1);
           INI_deallocate_memory ();
15
                            Close IO files
           fclose(fp_speech_in);
20
           fclose(fp_bitstream);
           if (fp_signaling != NULL)
                   fclosc(fp_signaling);
25 #ifdef DIAG_SMV
                          Close DIAGNOSTICS files
30
           DIA_enc_close_files ();
   #endif
35
   #ifdcf VERBOSE
            printf("\n");
40
```

#endif

/*-----*/

return 0;

/*----*/

/*----*/

10

/*-----*/

END -----*/

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	017	•
	/*====================================	۲,
	/**/	
	/* Conexant System Inc. */	
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	/* Newport Beach, CA 92660 */	
	/* * /	
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	/* means or used to make any derivative work (such as transformation */	
	/* or adaptation) without the authorisation of Conexant System Inc. */ /*==================================	
15	/* LIBRARY: mcutil.c */	
	/*==================*/	
	/**/	
	/**/	
20	/**/	
	#include "typcdef.h"	
	#include <malloc.h></malloc.h>	
25	#include <stdio.h></stdio.h>	
	#include <stdlib.h></stdlib.h>	
	/**/	
	/**/	
30	/**/ .	
	/*	
	/* FUNCTIONS : nrerror () */	
	/**/	
35	/* PURPOSE : Standard error handler. */	
	/** /* INPLIT ARGUMENTS : (char*) error text : output string. */	
	/* INPUT ARGUMENTS : _ (char*) error_text : output string.	
	/* OUTPUT ARGUMENTS : _ None.	
40	/**/	

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818

```
. /* RETURN ARGUMENTS : _ None.
  void nrerror ( char error_text [] )
5 {
         fprintf(stderr, "Run-time error...\n");
         fprintf(stderr,"%s\n",error_text);
         fprintf(stderr,"...now exiting to system...\n");
         exit(1);
10 }
15 /* FUNCTIONS : vector ()
  /* PURPOSE : Allocate a float vector with subscript range */
  /*
             v [nl, ..., nh].
  /*-----
20 /* INPUT ARGUMENTS : _ (INT32) nl.
              _ (INT32) nh.
  /* OUTPUT ARGUMENTS : _ None.
25 /* RETURN ARGUMENTS: _ (FLOAT32*): pointer to the allocated vector.*/
  FLOAT32 *vector( INT32 nl, INT32 nh)
   {
30
         FLOAT32 *v;
         v=(FLOAT32 *)calloc((unsigned) (nh-nl+1),sizeof(FLOAT32));
          if (!v) nrerror("allocation failure in vector()");
         return v-nl;
35 }
                                              */
40 /* FUNCTIONS : svector ()
```

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```
819
  /* PURPOSE : Allocate a short vector with subscript range */
          v [nl, ..., nh].
  /*
                                       */
5 /* INPUT ARGUMENTS : _ (INT32) nl.
 /* __(INT32) nh.
          */
  /* OUTPUT ARGUMENTS : _ None.
  /*____*/
10 /* RETURN ARGUMENTS: _(INT16*): pointer to the allocated vector. */
  INT16 *svector(INT32 nl, INT32 nh)
15
       INT16 *v;
       v=(INT16 *)calloc((unsigned) (nh-nl+1),sizeof(INT16));
       if (!v) nrerror("allocation failure in svector()");
       return v-nl;
20 }
  25 /* FUNCTIONS : ivector ()
  /* PURPOSE : Allocate an int vector with subscript range */
          v [nl, ..., nh].
  /*-----
30 /* INPUT ARGUMENTS : _ (INT32) nl.
  /*
           _ (INT32) nh.
  /* OUTPUT ARGUMENTS : _ None.
35 /* RETURN ARGUMENTS: _(INT32*): pointer to the allocated vector. */
  INT32 *ivector(INT32 nl, INT32 nh)
40
       INT32 *v;
```

```
v=(INT32 *)calloc((unsigned) (nh-nl+1),sizeof(INT32));
           if (!v) nrerror("allocation failure in ivector()");
           return v-nl;
5 }
10 /* FUNCTIONS : lvector ()
   /* PURPOSE : Allocate a long vector with subscript range */
                 v [nl, ..., nh].
15 /* INPUT ARGUMENTS : _(INT32) nl.
                 _ (INT32) nh.
   /* OUTPUT ARGUMENTS : _ None.
20 /* RETURN ARGUMENTS: _ (INT64*): pointer to the allocated vector. */
   INT64 *Ivector( INT32 nl, INT32 nh)
25
           INT64 *v,
            v=(INT64 *)calloc((unsigned) (nh-nl+1),sizeof(INT64));
            if (!v) nrcrror("allocation failure in lvector()");
            return v-nl;
30 }
                                                       */
35 /* FUNCTIONS : dvector ()
    /* PURPOSE : Allocate a double vector with subscript range */
                  v [nl, ..., nh].
40 /* INPUT ARGUMENTS : _ (INT32) nl.
```

```
821
           _(INT32) nh.
  /* OUTPUT ARGUMENTS : _ None.
  /*-----*/
5 /* RETURN ARGUMENTS: _(FLOAT64*): pointer to the allocated vector.*/
  FLOAT64 *dvector( INT32 nl, INT32 nh)
10
       FLOAT64 *v;
       v=(FLOAT64 *)calloc((unsigned) (nh-nl+1),sizeof(FLOAT64));
       if (!v) nrerror("allocation failure in dvector()");
       return v-nl;
15 }
     */
  20 /* FUNCTIONS : matrix ()
  /*_____*/
  /* PURPOSE : Allocate a float matrix with subscript range */
           m [nrl, ..., nrh][ncl, ..., nch].
25 /* INPUT ARGUMENTS : _ (INT32) nrl (low row).
           _ (INT32) nrh (high row).
           (INT32) ncl (low column).
  /*
           (INT32) nch (high column).
30 /* OUTPUT ARGUMENTS: _ None.
  /*____*/
  /* RETURN ARGUMENTS : _ (FLOAT32**): pointer to the allocated
35
  FLOAT32 **matrix( INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
  {
        INT32 i;
       FLOAT32 **m;
```

```
m=(FLOAT32 **) calloc((unsigned) (nrh-nrl+1),sizeof(FLOAT32*));
          if (!m) nrerror("allocation failure 1 in matrix()");
          m -= nrl;
 5
          for(i=nrl;i<=nrh;i++) {
                  m[i]=(FLOAT32 *) calloc((unsigned) (nch-ncl+1),sizeof(FLOAT32));
                  if (!m[i]) nrerror("allocation failure 2 in matrix()");
                  m[i] = ncl;
10
          return m;
   }
/* FUNCTIONS : smatrix ()
   /* PURPOSE : Allocate a short matrix with subscript range */
               m [nrl, ..., nrh][ncl, ..., nch].
20 /*-----
   /* INPUT ARGUMENTS: _ (INT32) nrl (low row).
                                                            */
   /*
                _ (INT32) nrh (high row).
                                                  */
   /*
                _ (INT32) ncl (low column).
                                                  */
                _ (INT32) nch (high column).
                                                   */
   /* OUTPUT ARGUMENTS : _ None.
                                                         */
   /* RETURN ARGUMENTS : _ (INT16**): pointer to the allocated matrix. */
30
   INT16 **smatrix( INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
   {
          INT32 i;
          INT16 **m;
35
          m=(INT16 **) calloc((unsigned) (nrh-nrl+1),sizeof(INT16*));
          if (!m) nrerror("allocation failure 1 in smatrix()");
          m = nrl;
40
          for(i=nrl;i<=nrh;i++) {
```

```
823
                 m[i]=(INT16 *) calloc((unsigned) (nch-ncl+1),sizeof(INT16));
                 if (!m[i]) nrerror("allocation failure 2 in smatrix()");
                 m[i] = ncl;
          }
5
          return m;
   }
*/
  /* FUNCTIONS : dmatrix ()
  /* PURPOSE : Allocate a double matrix with subscript range */
               m [nrl, ..., nrh][ncl, ..., nch].
   /* INPUT ARGUMENTS : _ (INT32) nrl (low row).
                _(INT32) nrh (high row).
              _ (INT32) ncl (low column).
                (INT32) nch (high column).
   /* OUTPUT ARGUMENTS: None.
   /* RETURN ARGUMENTS : _ (FLOAT64**): pointer to the allocated
                        matrix.
   FLOAT64 **dmatrix( INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
   {
          INT32 i;
          FLOAT64 **m;
30
          m=(FLOAT64 **) calloc((unsigned) (nrh-nrl+1),sizeof(FLOAT64*));
          if (!m) nrerror("allocation failure 1 in dmatrix()");
          m = nrl;
35
          for(i=nrl;i<=nrh;i++) {</pre>
                  m[i]=(FLOAT64 *) calloc((unsigned) (nch-ncl+1), sizeof(FLOAT64));
                  if (!m[i]) nrerror("allocation failure 2 in dmatrix()");
                  m[i] = ncl;
40
           }
```

```
return m;
   }
5
   /* FUNCTIONS : imatrix ()
   /* PURPOSE : Allocate an int matrix with subscript range */
10 /*
               m [nrl, ..., nrh][ncl, ..., nch].
   /* INPUT ARGUMENTS: _ (INT32) nrl (low row).
               _ (INT32) nrh (high row).
   /*
               _ (INT32) ncl (low column).
15 /*
               _ (INT32) nch (high column).
   /* OUTPUT ARGUMENTS: None.
   /* RETURN ARGUMENTS : _ (INT32**): pointer to the allocated matrix. */
   INT32 **imatrix( INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
   {
          INT32 i,**m;
25
          m=(INT32 **)calloc((unsigned) (nrh-nrl+1),sizeof(INT32*));
          if (!m) nrerror("allocation failure 1 in imatrix()");
          m = nrl;
30
          for(i=nrl;i<=nrh;i++) {
                 m[i]=(INT32 *)calloc((unsigned) (nch-ncl+1),sizcof(INT32));
                 if (!m[i]) nrerror("allocation failure 2 in imatrix()");
                 m[i] = ncl;
          }
35
          rcturn m;
   }
```

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```
825
  /* FUNCTIONS : submatrix ()
  /*-----
  /* PURPOSE : Allocate a float matrix with subscript range */
  /*
             m [nrl, ..., nrh][ncl, ..., nch].
5 /*-----
  /* INPUT ARGUMENTS: _ (INT32) nrl (low row).
             _ (INT32) nrh (high row).
             _ (INT32) ncl (low column).
             _ (INT32) nch (high column).
10 /*-----
  /* OUTPUT ARGUMENTS : _ None.
  /*____*/
  /* RETURN ARGUMENTS : _ (FLOAT32**): pointer to the allocated
                    matrix.
  FLOAT32 **submatrix( FLOAT32 **a, INT32 oldrl, INT32 oldrh, INT32 oldcl, \
                                        INT32 newrl, INT32 newcl)
20
         INT32 i,j;
        FLOAT32 **m;
         m=(FLOAT32 **) calloc((unsigned) (oldrh-oldrl+1),sizeof(FLOAT32*));
         if (!m) nrerror("allocation failure in submatrix()");
25
         m -= newrl;
         for(i=oldrl,j=newrl;i<=oldrh;i++,j++) m[j]=a[i]+oldcl-newcl;
         return m;
30 }
   35 /* FUNCTIONS : s3tensor ()
   /+____+/
   /* PURPOSE : Allocate a short tensor with subscript range */
            t [nrl,..., nrh][ncl,..., nch][ndl,...,ndh]. */
40 /* INPUT ARGUMENTS : _ (INT32) nrl.
```

```
826
                 _ (INT32) nrh.
                                                 */
   /*
                                                 */
                 _ (INT32) ncl.
                 _ (INT32) nch.
                 (INT32) ndl.
5 /*
                 (INT32) ndh.
   /* OUTPUT ARGUMENTS : _ None.
   /* RETURN ARGUMENTS : _ (INT16***): pointer to the allocated tensor.*/
INT16 ***s3tensor (INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
   {
           INT32 i,j,nrow=nrh-nrl+1,ncol=nch-ncl+1,ndep=ndh-ndl+1;
15
           INT16 ***t;.
           t=(INT16***)calloc((unsigned) (nrow+1), sizeof(INT16**));
           if (!t) nrerror("allocation failure 1 in s3tensor()");
           t+=1;
20
           t-=nrl;
           t[nrl]=(INT16**)calloc((unsigned) (nrow*ncol+1), sizeof(INT16*));
           if (!t[nrl]) nrerror("allocation failure 2 in s3tensor()");
           t[nrl]+=1;
25
           t[nrl]-=ncl;
           t[nrl][ncl]=(INT16*)calloc((unsigned) (nrow*ncol*ndep+1), sizeof(INT16));
           if (!t[nrl][ncl]) nrerror("allocation failure 3 in s3tensor()");
           t[nrl][ncl]+=1;
30
           t[nrl][ncl]-=ndl;
           for(j=ncl+1;j\leq nch;j++) t[nrl][j]=t[nrl][j-l]+ndep;
           for(i=nrl+1;i \le nrh;i++){
                   t[i]=t[i-1]+ncol;
35
                   t[i][ncl]=t[i-1][ncl]+ncol*ndep;
                   for(j=ncl+1;j \le nch;j++) t[i][j]=t[i][j-1]+ndep;
            }
           return t;
40 }
```

```
5 /* FUNCTIONS : i3tensor ()
   /* PURPOSE : Allocate an int tensor with subscript range */
                 t [nrl,..., nrh][ncl,..., nch][ndl,...,ndh]. */
                                                                */
10 /* INPUT ARGUMENTS : _ (INT32) nrl.
                                                    */
                 _(INT32) nrh.
                  _ (INT32) ncl.
                  _ (INT32) nch.
                  _ (INT32) ndl.
                  _ (INT32) ndh.
15 /*
   /* OUTPUT ARGUMENTS : _ None.
   /* RETURN ARGUMENTS : _ (INT32***): pointer to the allocated
20 /*
   INT32 ***i3tensor (INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
            INT32 i,j,nrow=nrh-nrl+1,ncol=nch-ncl+1,ndep=ndh-ndl+1;
25
            INT32 ***t;
            t=(INT32***)calloc((unsigned) (nrow+1), sizeof(INT32**));
            if (!t) nrerror("allocation failure 1 in i3tensor()");
30
            t+=1;
            t-=nrl;
            t[nrl]=(INT32**)calloc((unsigned) (nrow*ncol+1), sizeof(INT32*));
            if (!t[nrl]) nrerror("allocation failure 2 in i3tensor()");
            t[nrl]+=1;
35
            t[nrl]-=ncl;
            t[nrl][ncl]=(INT32*)calloc((unsigned) (nrow*ncol*ndep+1), sizeof(INT32));
            if (!t[nrl][ncl]) nrerror("allocation failure 3 in i3tensor()");
            t[nrl][ncl]+=1;
40
```

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828
           t[nri][ncl]-=ndl;
           for(j=ncl+1;j\leq nch;j++) t[nrl][j]=t[nrl][j-1]+ndep;
           for(i=nrl+1;i<=nrh;i++){
                   t[i]=t[i-1]+ncol;
 5
                   t[i][ncl]=t[i-1][ncl]+ncol*ndep;
                   for(j=ncl+1;j<=nch;j++) t[i][j]=t[i][j-1]+ndcp;
           }
10
           return t;
   }
15 /*===========
   /* FUNCTIONS : 13tensor ()
   /* PURPOSE : Allocate a long tensor with subscript range */
                 t [nrl,..., nrh][ncl,..., nch][ndl,...,ndh]. */
   /*
                                                               */
   /* INPUT ARGUMENTS: _(INT32) nrl.
                                                   */
   /*
                 _ (INT32) nrh.
                 (INT32) ncl.
                  _ (INT32) nch.
25 /*
                  _ (INT32) ndl.
                  _ (INT32) ndh.
   /* OUTPUT ARGUMENTS: _ None.
30 /* RETURN ARGUMENTS: _(INT64***): pointer to the allocated
   INT64 ***|3tensor (INT32 nrl, INT32 nrl, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndl)
35 {
            INT32 i,j,nrow=nrh-nrl+1,ncol=nch-ncl+1,ndep=ndh-ndl+1;
            INT64 ***t;
            t=(INT64***)calloc((unsigned) (nrow+1), sizeof(INT64**));
            if (!t) nrerror("allocation failure 1 in 13tensor()");
40
```

829

```
t+=1;
            t-=nrl;
            t[nrl]=(INT64**)calloc((unsigned) (nrow*ncol+1), sizeof(INT64*));
            if (!t[nrl]) nrerror("allocation failure 2 in l3tensor()");
 5
            t[nrl]+=1;
            t[nrl]-=ncl;
            t[nrl][ncl]=(INT64*)calloc((unsigned) (nrow*ncol*ndep+1), sizeof(INT64));
            if (!t[nrl][ncl]) nrcrror("allocation failure 3 in 13tensor()");
10
            t[nrl][ncl]+=1;
            t[nrl][ncl]-=ndl;
            for(j=ncl+1;j\leq nch;j++) t[nrl][j]=t[nrl][j-1]+ndep;
15
            for(i=nrl+1;i<=nrh;i++){
                    t[i]=t[i-1]+ncol;
                     t[i][ncl]=t[i-1][ncl]+ncol*ndep;
                    for(j=ncl+1;j \le nch;j++) t[i][j]=t[i][j-1]+ndep;
            }
20
            return t;
    }
25
   /* FUNCTIONS : Bitensor ()
   /* PURPOSE : Allocate a float tensor with subscript range */
                   t [nrl,..., nrh][ncl,..., nch][ndl,...,ndh]. */
30 /*
                                                                  */
    /* INPUT ARGUMENTS: _ (INT32) nrl.
                   _ (INT32) nrh.
                   _ (INT32) ncl.
                   _ (INT32) nch.
35 /*
                   _(INT32) ndl.
                   _ (INT32) ndh.
    /* OUTPUT ARGUMENTS: None.
```

```
830
   /* RETURN ARGUMENTS: __(FLOAT32***): pointer to the allocated
   /*
                            tensor.
 5 FLOAT32 ***f3tensor (INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndl)
   {
            INT32 i,j,nrow=nrh-nrl+1,ncol=nch-ncl+1,ndep=ndh-ndl+1;
            FLOAT32 ***t;
            t=(FLOAT32***)calloc((unsigned) (nrow+1), sizeof(FLOAT32**));
10
            if (!t) nrerror("allocation failure 1 in f3tensor()");
            t+=1;
            t-=nrl;
            t[nrl]=(FLOAT32**)calloc((unsigned) (nrow*ncol+1), sizeof(FLOAT32*));
15
            if (!t[nrl]) nrerror("allocation failure 2 in f3tensor()");
            t[nrl]+=1;
            t[nrl]-=ncl;
            t[nrl][ncl]=(FLOAT32*)calloc((unsigned) (nrow*ncol*ndep+1), sizcof(FLOAT32));
20
            if (!t[nrl][ncl]) nrcrror("allocation failure 3 in f3tensor()");
            t[nrl][ncl]+=1;
            t[nrl][ncl]=ndl;
            for(j=ncl+1;j\leq nch;j++) t[nrl][j]=t[nrl][j-1]+ndep;
25
            for(i=nrl+1;i<=nrh;i++){
                    t[i]=t[i-1]+ncol;
                    t[i][ncl]=t[i-1][ncl]+ncol*ndep;
                     for(j=ncl+1;j \le nch;j++) t[i][j]=t[i][j-1]+ndep;
30
            }
            return t;
    }
    /* FUNCTIONS : d3tensor ()
40 /* PURPOSE : Allocate a double tensor with subscript range */
```

```
t [nrl,..., nrh][ncl,..., nch][ndl,...,ndh]. */
                                                                 */
   /* INPUT ARGUMENTS : _ (INT32) nrl.
                                                    */
                  _ (INT32) nrh.
                  _ (INT32) ncl.
                  _ (INT32) nch.
                  _(INT32) ndl.
                  _ (INT32) ndh.
10 /* OUTPUT ARGUMENTS : _ None.
   /* RETURN ARGUMENTS : _ (FLOAT64***): pointer to the allocated
                            tensor.
15
   FLOAT64 ***d3tensor (INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
   {
            1NT32 i,j,nrow=nrh-nrl+1,ncol=nch-ncl+1,ndcp=ndh-ndl+1;
            FLOAT64 ***1;
20
            t=(FLOAT64***)calloc((unsigned) (nrow+1), sizeof(FLOAT64**));
            if (!t) nrerror("allocation failure 1 in d3tensor()");
            t+=1:
            t-=nrl;
25
            t[nrl]=(FLOAT64**)calloc((unsigned) (nrow*ncol+1), sizeof(FLOAT64*));
            if (!t[nrl]) nrerror("allocation failure 2 in d3tensor()");
            t[nrl]+=1;
            t[nrl]-=ncl;
30
            t[nrl][ncl]=(FLOAT64*)calloc((unsigned) (nrow*ncol*ndep+1), sizeof(FLOAT64));
            if (!t[nrl][ncl]) nrerror("allocation failure 3 in d3tensor()");
            t[nrl][ncl]+=1;
            t[nrl][ncl]-ndl;
35
            for(j=ncl+1;j<=nch;j++) t[nrl][j]=t[nrl][j-1]+ndcp;
            for(i=nrl+1;i<=nrh;i++){
                     t[i]=t[i-1]+ncol;
                     t[i][ncl]=t[i-l][ncl]+ncol*ndep;
                     for(j=ncl+1;j\leq nch;j++) t[i][j]=t[i][j-1]+ndep;
40
```

832

```
}
       rcturn t;
  }
5
  /* FUNCTIONS : free_vector ()
10 /*-----
  /* PURPOSE : free a float vector allocated with vector (). */
  /* INPUT ARGUMENTS: _(FLOAT32*) v.
           _ (INT32) nl.
15 /*
           (INT32) nh.
  /* OUTPUT ARGUMENTS : _ None.
  /* RETURN ARGUMENTS : None.
void free_vector(FLOAT32 *v, INT32 nl, INT32 nh)
  {
        free((char*) (v+nl));
25 }
30 /* FUNCTIONS : free_svector ()
  /* PURPOSE : free a short vector allocated with svector ().*/
  /* INPUT ARGUMENTS: _(INT16*) v.
                                           */
35 /*
            _ (INT32) nl.
            _ (INT32) nh.
  /* OUTPUT ARGUMENTS : _ None.
40 /* RETURN ARGUMENTS : _ None.
```

```
833
  void free_svector( INT16 *v, INT32 nl, INT32 nh)
  {
      free((char*) (v+nl));
  }
/* FUNCTIONS : free_ivector ()
  /* PURPOSE : free an int vector allocated with ivector (). */
  /*_-----*/
                                         */
15 /* INPUT ARGUMENTS : _ (INT32*) v.
  /*
           _ (INT32) nl.
           _(INT32) nh.
  /* OUTPUT ARGUMENTS : _ Nonc.
20 /*-----*/
  /* RETURN ARGUMENTS : _ Nonc.
  void free ivector(INT32 *v, INT32 nl, INT32 nh)
25 {
       free((char*) (v+nl));
  }
30
  /* FUNCTIONS : free_lvector ()
  /* PURPOSE : free a long vector allocated with lvector (). */
35 /*-----*/
  /* INPUT ARGUMENTS: _ (INT64*) v.
                                 */
  /*
           _ (INT32) nl.
                                 */
            _ (INT32) nh.
40 /* OUTPUT ARGUMENTS : _ None.
```

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```
834
  /* RETURN ARGUMENTS : _ None.
  /*______*/
5 void free lvector( INT64 *v, INT32 nl, 1NT32 nh)
  {
       free((char*) (v+nl));
  }
  /* FUNCTIONS : free dvector ()
15 /* PURPOSE : free a double vector allocated with dvector ().*/
  /* INPUT ARGUMENTS: _(FLOAT64*) v.
                                          */
           _ (INT32) nl.
           _ (INT32) nh.
20 /*-----
  /* OUTPUT ARGUMENTS: None.
  /* RETURN ARGUMENTS : _ None.
  25
  void free_dvector(FLOAT64 *v, INT32 nl, INT32 nh)
  {
       free((char*) (v+nl));
  }
30
  /* FUNCTIONS : free_matrix ()
  /* PURPOSE : free a float matrix allocated with matrix (). */
  /* INPUT ARGUMENTS: _(FLOAT32**) m.
           _ (INT32) nrl.
40 /*
          _ (INT32) nrh.
```

```
835
               _ (INT32) ncl.
               _ (INT32) nch.
   /* OUTPUT ARGUMENTS : _ None.
 5 /*----*/
   /* RETURN ARGUMENTS: None.
   void free_matrix(FLOAT32 **m, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
10 {
          INT32 i;
          for(i=nrh;i>=nrl;i--) free((char*) (m[i]+ncl));
          free((char*) (m+nrl));
15 }
20 /* FUNCTIONS : free_smatrix ()
                                                   */
   /* PURPOSE : free a short matrix allocated with smatrix (). */
   /* INPUT ARGUMENTS: _(INT16**) m.
25 /*
               _ (INT32) nrl.
                                             */
               _ (INT32) nrh.
               _ (INT32) ncl.
               _(INT32) nch.
30 /* OUTPUT ARGUMENTS: None.
   /* RETURN ARGUMENTS : _ None.
35 void free_smatrix( INT16 **m, INT32 nrl, INT32 nrh, 1NT32 ncl, INT32 nch)
   {
          INT32 i:
          for(i=nrh;i>=nrl;i--) free((char*) (m[i]+ncl));
40
          free((char*) (m+nrl));
```

```
}
  /* FUNCTIONS : free_dmatrix ()
  /* PURPOSE : free a double matrix allocated with dmatrix ().*/
  /+____+/
10 /* INPUT ARGUMENTS: _(FLOAT64**) m.
             _ (INT32) nrl.
              _ (INT32) nrh.
              (INT32) ncl.
              _(INT32) nch.
  /* OUTPUT ARGUMENTS : _ Nonc.
  /* RETURN ARGUMENTS : _ None.
20
  void free_dmatrix(FLOAT64 **m, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
   {
         INT32 i;
25
         for(i=nrh;i>=nrl;i--) free((char*) (m[i]+ncl));
         free((char*) (m+nrl));
   }
30
   /* FUNCTIONS : free_imatrix ()
   /* PURPOSE : free an int matrix allocated with imatrix (). */
   /* INPUT ARGUMENTS: _ (INT32**) m.
                                                  */
              _ (INT32) nrl.
              _ (INT32) nrh.
              _ (INT32) ncl.
              _ (INT32) nch.
40 /*
```

```
837
 /* OUTPUT ARGUMENTS : _ None.
 /*----*/
 /* RETURN ARGUMENTS : _ None.
void free imatrix( INT32 **m, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
 {
      INT32 i;
10
      for(i=nrh;i>=nrl;i--) free((char*) (m[i]+ncl));
      free((char*) (m+nrl));
 }
 /* FUNCTIONS : free submatrix ()
 /*____*/
20 /* PURPOSE : free a float submatrix allocated with submatrix (). */
 /+____*/
 /* INPUT ARGUMENTS: _ (FLOAT32**) b.
                           */
         _ (INT32) nrl.
 /*
         _ (INT32) nrh.
         _ (INT32) ncl.
25 /*
         _ (INT32) nch.
 /* OUTPUT ARGUMENTS : _ Nonc.
30 /* RETURN ARGUMENTS: None.
  void free_submatrix(FLOAT32 **b, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
35
      free((char*) (b+nrl));
```

```
838
  /* FUNCTIONS : free i3tensor ()
  /* PURPOSE : free an int tensor allocated with i3tensor ().
  /*_____
5 /* INPUT ARGUMENTS : _ (INT32***) t.
                                          */
  /*
            _ (INT32) nrl.
              _ (INT32) nrh.
                                          */
              _ (INT32) ncl.
              _ (INT32) nch.
              __(INT32) ndl.
10 /*
              _ (INT32) ndh.
  /* OUTPUT ARGUMENTS : _ None.
15 /* RETURN ARGUMENTS : _ None.
  void free_i3tensor(INT32 ***t, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
20
         free((char*) (t[nrl][ncl]+ndl-1));
         free((char*) (t[nrl]+ncl-l));
         frec((char*) (t+nrl-1));
   }
   /* FUNCTIONS : free_s3tensor ()
30 /* PURPOSE : free a short tensor allocated with s3tensor (). */
   /* INPUT ARGUMENTS : _ (INT16***) t.
                                                     */
               _ (INT32) nrl.
               _ (INT32) nrh.
35 /*
               _ (INT32) ncl.
               _ (INT32) nch.
               _(INT32) ndl.
               _ (INT32) ndh.
                                           */
40 /* OUTPUT ARGUMENTS : _ None.
```

```
839
  /* RETURN ARGUMENTS: None.
  5 void free_s3tensor(INT16 ***t, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
        free((char*) (t[nrl][ncl]+ndl-1));
        free((char*) (t[nrl]+ncl-l));
        free((char*) (t+nrl-1));
10 }
  */
15 /* FUNCTIONS : free_l3tensor ()
  /* PURPOSE : free a long tensor allocated with 13tensor (). */
  /*____*/
  /* INPUT ARGUMENTS: _(INT64***) t.
            (INT32) nrl.
20 /*
            (INT32) nrh.
            _ (INT32) ncl.
            _ (INT32) nch.
            _ (INT32) ndl.
            _ (INT32) ndh.
25 /*
  /* OUTPUT ARGUMENTS : _ Nonc.
  /* RETURN ARGUMENTS : _ None.
void free_13tensor(INT64 ***t, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
  {
        free((char*) (t[nrl][ncl]+ndl-1));
        free((char*) (t[nrl]+ncl-1));
35
        frec((char*) (t+nrl-1));
  }
40
```

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```
840
  */
  /* FUNCTIONS : free_f3tcnsor ()
  /* PURPOSE : free a float tensor allocated with f3tensor (). */
  /* INPUT ARGUMENTS: _(FLOAT32***) t.
  /*
             _ (INT32) nrl.
  /*
             _ (INT32) nrh.
             _ (INT32) ncl.
             _ (INT32) nch.
10 /*
             _ (INT32) ndl.
             (INT32) ndh.
  /* OUTPUT ARGUMENTS : _ None.
  /* RETURN ARGUMENTS : _ None.
  void free_f3tensor(FLOAT32 ***t, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
20 {
         free((char*) (t[nrl][ncl]+ndl-l));
         free((char*) (t[nrl]+ncl-l));
         free((char*) (t+nrl-1));
   }
25
  */
   /* FUNCTIONS : free_d3tensor ()
  /* PURPOSE : free a double tensor allocated with d3tensor (). */
   /* INPUT ARGUMENTS: _(FLOAT64***) t.
              _(INT32) nrl.
35 /*
              _(INT32) nrh.
   /*
              _(INT32) ncl.
              _(INT32) nch.
              _(INT32) ndl.
              _(INT32) ndh.
```

```
841
                                               */
  /* OUTPUT ARGUMENTS : None.
  /*____*/
  /* RETURN ARGUMENTS : _ None.
5
  void free_d3tensor(FLOAT64 ***t, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch, INT32 ndl, INT32 ndh)
        free((char*) (t[nrl][ncl]+ndl-1));
        free((char*) (t[nrl]+ncl-1));
10
        frec((char*) (t+nrl-1));
  }
*/
  /* FUNCTIONS .: convert_matrix ()
  /*____*/
  /* PURPOSE : allocate a float matrix m[nrl,...,nrh][ncl,...,nch] */
          that points to the matrix declared in the standard */
  /*
          C manner as a[nrow][ncol], where nrow=nrh-nrl+1 and */
20 /*
          ncol=nch-ncl+1. The routine should be called with */
  /*
          the address &a[0][0] as the first argument.
  /* INPUT ARGUMENTS : _ (FLOAT32*) a.
25 /*
             _ (INT32) nrl.
             _ (INT32) nrh.
             _(INT32) ncl.
             _ (INT32) nch.
30 /* OUTPUT ARGUMENTS: _ None.
  /*-----*/
  /* RETURN ARGUMENTS : _ (FLOAT32**) pointer to array of pointers to */
                                    */
   /*_____
35
  FLOAT32 **convert matrix( FLOAT32 *a, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
   {
         INT32 i,j,nrow,ncol;
         FLOAT32 **m;
40
```

3003000

```
842
         nrow=nrh-nrl+1;
         ncol=nch-ncl+1;
         m = (FLOAT32 **) calloc((unsigned) (nrow),sizcof(FLOAT32*));
         if (!m) nrerror("allocation failure in convert_matrix()");
5
         m -= nrl;
         for(i=0,j=nrl;i \le nrow-1;i++,j++) m[j]=a+ncol*i-ncl;
          return m;
  }
   /* FUNCTIONS : frec_convert_matrix ()
15 /* PURPOSE : free a matrix allocated by convert_matrix ().
   /* INPUT ARGUMENTS: _ (FLOAT32**) b.
               _ (INT32) nrl.
                                           */
   /*
               _ (INT32) nrh.
20 /*
               _ (INT32) ncl.
               _(INT32) nch.
                                                     */
   /* OUTPUT ARGUMENTS : _ Nonc.
25 /* RETURN ARGUMENTS: _ None.
   void free_convert_matrix(FLOAT32 **b, INT32 nrl, INT32 nrh, INT32 ncl, INT32 nch)
   {
30
          frcc((char*) (b+nrl));
   }
35
                      ------
                                                */
   /* FUNCTIONS : pdvector ()
   /* PURPOSE : Allocate a double a vector of pointers to
               double datawith subscript range
40 /*
```

```
843
             v [nl, ..., nh].
  /* INPUT ARGUMENTS : _ (INT32) nl.
             _ (INT32) nh.
  /* OUTPUT ARGUMENTS : _ None.
  /*____*/
  /* RETURN ARGUMENTS: _ (FLOAT64*); pointer to the allocated vector.*/
  10
  FLOAT64 **pdvector( INT32 nl, INT32 nh)
        FLOAT64 **v;
15
         v = (FLOAT64 **) calloc((unsigned) (nh-nl+1), sizeof(FLOAT64 *));
         if (!v) nrerror("allocation failure in pdvector()");
20
         return v-nl;
25
   /* FUNCTIONS : free_pdvector ()
  /* PURPOSE : free a vector of pointer to double allocated */
                with pdvector ().
   /* INPUT ARGUMENTS: _(FLOAT64 **) v.
              _(INT32 ) nl.
              _(INT32 ) nh.
40 /* OUTPUT ARGUMENTS : _ None.
```

	/*	844 */	
	/* RETU	URN ARGUMENTS : _ None.	*/
		ec_pdvector(FLOAT64 **v, INT32 nl, INT32 nh)	
		/ *	*/
10		free((char*) (v+nl));	*/
		/*return;	~,
15		/*	*/
	}		
20	/*	·*	1
۷۷			_
			*

```
845
    */
  /* Conexant System Inc.
5 /* 4311 Jamboree Road
  /* Newport Beach, CA 92660
  /* Copyright(C) 2000 Conexant System Inc.
10 /* ALL RIGHTS RESERVED:
  /* No part of this software may be reproduced in any form or by any */
  /* means or used to make any derivative work (such as transformation */
  /* or adaptation) without the authorisation of Conexant System Inc. */
  */
15 /* PROTOYPE FILE: mcutil.h
  /*-----*/
  /*-----*/
                   (char []);
  void
        nrerror
                   (INT32, INT32);
  INT16 *svector
                               (INT32, INT32);
  FLOAT32
              *vector
                   (INT32, INT32);
25 INT32 *ivector
                   (INT32, INT32);
  INT64 *Ivector
                         (INT32, INT32);
  FLOAT64
              *dvcctor
              **matrix
                               (INT32, INT32, INT32, INT32);
  FLOAT32
  INT16 **smatrix
                         (INT32, INT32, INT32, INT32);
                               (INT32, INT32, INT32, INT32);
              **dmatrix
30 FLOAT64
                         (INT32, INT32, INT32, INT32);
  INT32 **imatrix
              **submatrix
                               (FLOAT32 **, INT32, INT32, INT32, INT32, INT32);
  FLOAT32
                         (INT32, INT32, INT32, INT32, INT32, INT32);
  INT16 ***s3tensor
                         (INT32, INT32, INT32, INT32, INT32, INT32);
  INT32 ***i3tensor
                         (INT32, INT32, INT32, INT32, INT32, INT32);
35 INT64 ***13tensor
                               (INT32, INT32, INT32, INT32, INT32, INT32);
  FLOAT32
              ***f3tensor
                               (INT32, INT32, INT32, INT32, INT32, INT32);
  FLOAT64
              ***d3tensor
                         (FLOAT32 *, INT32, INT32);
  void
        free_vector
  void
                         (INT16 *, INT32, INT32);
        free_svector
                         (INT32 *, INT32, INT32);
40 void
        free_ivector
```

DESCRIPTION AND ADDRESS 1

```
846
                                  (INT64 *, INT32, INT32);
           free_lvcctor
   void
                                  (FLOAT64 *, INT32, INT32);
           free_dvector
   void
                                  (FLOAT32 **, INT32, INT32, INT32, INT32);
   void
           free_matrix
                                  (INT16 **, INT32, INT32, INT32, INT32);
           free_smatrix
   void
                                  (FLOAT64 **, INT32, INT32, INT32, INT32);
 5 void
           free_dmatrix
                                  (INT32 **, INT32, INT32, INT32, INT32);
   void
           free_imatrix
                                  (FLOAT32 **, INT32, INT32, INT32, INT32);
           free_submatrix
   void
           free_i3tensor
                                  (INT32 ***t , INT32, INT32, INT32, INT32, INT32, INT32);
   void
                                  (INT16 ***t, INT32, INT32, INT32, INT32, INT32, INT32);
   void
           free_s3tensor
                                  (INT64 ***t , INT32, INT32, INT32, INT32, INT32, INT32);
10 void
           free_13tensor
                                  (FLOAT32 ***t, INT32, INT32, INT32, INT32, INT32, INT32);
           free_f3tensor
   void
                                  (FLOAT64 ***t, INT32, INT32, INT32, INT32, INT32, INT32);
   void
           free_d3tensor
   FLOAT32
                   **convert matrix(FLOAT32 *, INT32, INT32, INT32, INT32);
                                  (FLOAT32 **, INT32, INT32, INT32, INT32);
15 void
           free_convert_matrix
                                          (INT32, INT32);
   FLOAT64
                   **pdvector
   void
                          (FLOAT64 **, INT32, INT32);
                             -- END ---
```

```
847
        */
        /* Conexant System Inc.
  5 /* 4311 Jamboree Road
         /* Newport Beach, CA 92660
         /* Copyright(C) 2000 Conexant System Inc.
         /*-----*/
10 /* ALL RIGHTS RESERVED:
         /* No part of this software may be reproduced in any form or by any */
         /* means or used to make any derivative work (such as transformation */
         /* or adaptation) without the authorisation of Conexant System Inc. */
         15 /* FILE: typedef.h
         \label{lem:bord_msc_ver} \mbox{\tt \#if defined(\_BORLANDC\_)} \parallel \mbox{\tt defined(\_WATCOMC\_)} \parallel \mbox{\tt defined(\_MSC\_VER)} \parallel \mbox{\tt defined(\_ZTC\_)} \parallel \mbox{\tt defined(\_MSC\_VER)} \parallel \mbox{\tt defined(\_ZTC\_)} \parallel \mbox{\tt defined(\_MSC\_VER)} \parallel \mbox{\tt defined(\_
20 defined( HIGHC ) | defined (_CYGWIN32_)
         typedef long int INT64;
         typedef long int INT32;
         typedef short int INT16;
         typedef unsigned short int UNS_INT16;
25 typedef short int FLAG;
          typedef double FLOAT64;
          typedef float FLOAT32;
          #elif defined( __sun)
          typedef long INT64;
30 typedef long INT32;
          typedef short INT16;
          typedef unsigned short UNS_INT16;
          typedef int FLAG;
          typedef double FLOAT64;
35 typedef float FLOAT32;
          #clif defined(__unix__) || defined(__unix)
          typedcf long INT64;
          typedef int INT32;
          typedef short INT16;
 40 typedef unsigned short UNS_INT16;
```

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 U1554U5∀1

848

	typedef int FLAG;
	typedef double FLOAT64;
	typedcf float FLOAT32;
	#clif defined(VMS) defined(VMS)
5	typedef long INT64;
	typedef long JNT32;
	typedef short INT16;
	typedef unsigned short UNS_INT16;
	typedef int FLAG;
10	typedef double FLOAT64;
	typedef float FLOAT32;
	#else
	#error COMPILER NOT TESTED typedef.h needs to be updated, see readme
	#endif
15	
	/*=====================================
	/**/
	/*

849

APPENDIX B

מוספר ב בוצור ובוכבומות

	850 	=======================================
		=====*/
Conexant System Inc.	*/	
4311 Jamboree Road	*/	
Newport Beach, CA 92660	*/	-
·	*/	
* Copyright(C) 2000 Conexant System Inc.	*/	
+	*/	
* ALL RIGHTS RESERVED:	*/	
* No part of this software may be reproduced in		
* means or used to make any derivative work (s		
* or adaptation) without the authorisation of Co	·	
*======================================		=======================================
* LIBRARY: gain_vq.tab *====================================	*/ 	
·		,
*	***	
* TABLES		
*		
·	,	
*======================================		
======================================		=====/
* Conexant System Inc.	*/	
* 4311 Jamboree Road	*/	
* Newport Beach, CA 92660	*/	
*		
/* Copyright(C) 2000 Conexant System Inc.	*/	
** Copyright(C) 2000 Conexant System Inc.	·	
	·	
/*	*/	
/* ALL RIGHTS RESERVED:	*/ any form or by any */	
** ** ALL RIGHTS RESERVED: ** No part of this software may be reproduced in ** means or used to make any derivative work (some some some some some some some some	*/ any form or by any */ such as transformation */ onexant System Inc. */	
** ** ALL RIGHTS RESERVED: ** No part of this software may be reproduced in ** means or used to make any derivative work (s	*/ any form or by any */ such as transformation */ onexant System Inc. */	======*/

40 FLOAT64 gp4_tab[TAB_SIZE_GVQ_4D][GVQ_VEC_SIZE_4D]={

```
851
                      0.59090763,
                                     0.64920781,
                                                   0.64610492},
       0.60699869,
   {
                                     0.64210982,
                                                   0.63130892},
       0.68101613,
                      0.65403889,
   {
                                     0.65643759,
                                                   0.75672188},
       0.63010482,
                      0.56235514,
   {
       0.68659859,
                      0.57330071,
                                     0.58744429,
                                                   0.81576573},
   {
 5 {
       0.64851060,
                      0.64851060,
                                     0.71451327,
                                                   0.71451327},
                                     0.63967832,
                                                   0.64221364},
       0.77682415,
                      0.68550346,
   {
                      0.63785293,
                                     0.74037768,
                                                   0.76724979},
   {
       0.60611942,
       0.70282156,
                      0.70854395,
                                     0.71397421,
                                                   0.70622913},
   {
                                     0.78733321,
                                                   0.81281683},
       0.60640355,
                      0.64369532,
   {
10 {
                      0.75678142,
                                     0.67673117,
                                                    0.64600357},
       0.79313256,
                                     0.67719160,
                                                    0.67719160},
       0.78871769,
                      0.78871769,
   {
                                     0.74410403,
                                                   0.76624484},
   {
       0.78750339,
                      0.65818283,
       0.68666216,
                      0.71349168,
                                     0.80131080,
                                                   0.81487027},
   {
                      0.77007419,
       0.81564295,
                                     0.73645773,
                                                   0.73009256},
   {
       0.87690114,
                      0.83990779,
                                     0.69391460,
                                                    0.62411652},
15 {
                      0.72676490,
                                     0.88398786,
                                                    0.66963377},
   {
       0.78900835,
       0.93272369,
                                                    0.75901092},
                      0.74226864,
                                     0.62990265,
   {
       0.76976621,
                      0.76976621,
                                     0.78405048,
                                                    0.78405048},
   {
                                                    0.65975429},
       0.87041644,
                      0.83542384,
                                     0.72726860,
   {
                                                    0.80041292},
20 {
       0.98828812,
                      0.57828812,
                                     0.70041292,
       0.78642005,
                      0.77917927,
                                     0.79153969,
                                                    0.78921747,
   {
   {
       0.72998353,
                      0.72998353,
                                     0.80237917,
                                                    0.88237917},
                                     0.76406702,
                                                    0.74795323},
       0.84850899,
                      0.79316290,
   {
                                                    0.77772334},
                                     0.78603639,
       0.86473350,
                      0.82733618,
   {
                                                    0.76672185},
                      0.86598073,
                                     0.76672185,
25 {
       0.86598073,
                                                    0.89448642},
                                     0.87026008,
   {
       0.75717804,
                      0.76443058,
       0.82990895,
                      0.82866571,
                                     0.84344056,
                                                    0.84482601},
   {
                                     0.93179791,
                                                    0.93179791},
   {
       0.73643129,
                      0.73643129,
                                                    0.84549563},
   {
       0.84091377,
                      0.84091377,
                                     0.84549563,
                                                    0.70990139},
                      0.93530253,
                                     0.75329477,
30 {
       0.96705390,
                                                    0.78242800},
   {
       0.82417511,
                      0.88417511,
                                     0.90242800,
                      1.00565081,
                                     0.73784063,
                                                    0.73784063},
       0.90565081,
   {
                                                    0.76745989},
                      0.89908568,
                                     0.80488500,
       0.93696854,
   {
                                     0.85073556,
                                                    0.84218753},
       0.88864729,
                      0.87038965,
   {
                      0.86005510,
                                     0.85177958,
                                                    0.84228257},
35 {
       0.91005475,
                                                    0.95972113},
   {
       0.73978618,
                      0.82370630,
                                     0.94989275,
       0.90821062,
                      0.90821062,
                                     0.84367096,
                                                    0.84367096},
   {
                                                    0.93452704},
   {
       0.82123084,
                      0.84572475,
                                     0.91624881,
   {
       0.84262163,
                      0.90568020,
                                     1.05247535,
                                                    0.73518775},
       0.89073030,
                                                    0.90276521},
40 {
                      0.89054064,
                                     0.90204609,
```

```
852
                      0.91935059,
                                     0.88462629,
                                                    0.87035097},
       0.93915442,
   {
       0.94939652,
                      0.86939652,
                                     1.02255193,
                                                    0.76255193},
   {
                      0.90270617,
                                     0.75324621,
                                                    0.95324621},
   {
       1.02270617,
        1.06002553,
                      0.97431233,
                                     0.80897117,
                                                    0.78133585},
 5 {
                      0.97983378,
                                     0.87774763,
                                                    0.87774763},
       0.97983378,
                                                    0.93152706},
       0.93569159,
                      0.93248467,
                                     0.93336567,
   {
                      0.97991575,
                                     0.89630971,
                                                    0.86870634},
   {
        1.01454689,
                                                    0.98329006},
       0.90992070,
                      0.90992070,
                                     0.98329006,
   {
   {
       0.88442519,
                      0.91454256,
                                     0.99448626,
                                                    1.00440409},
                                                    1.00200101},
10 {
       0.84818392,
                      0.87818392,
                                     1.06200101,
                                                    0.95989277},
       0.97346962,
                      0.96872528,
                                     0.96372065,
   {
                                                    0.96175669},
       0.97243125,
                      0.97243125,
                                     0.96175669,
   {
                                                    0.94701072},
                                     0.94874981,
   {
        1.10743055,
                      0.93511480,
        1.08620375,
                      1.04782334,
                                     0.98763661,
                                                    0.96029488},
   {
                                                    1.02876771},
15 {
        1.01282321,
                      1.01771063,
                                     1.03454061,
                                     0.95687290,
                                                    0.92553343},
        1.13502856,
                      1.06918208,
   {
        0.95295916,
                      0.98532512,
                                     1.09705441,
                                                    1.11982849},
   {
   {
        1.03699487,
                      1.03699487,
                                     1.05224994,
                                                    1.05224994},
                      0.93430309,
                                     1.15479496,
                                                    1.15479496},
   {
        0.93430309,
                                     1.00380677,
                                                    1.01380677},
20 {
        1.10604686,
                      1.08604686,
                                     1.13934841,
                                                    1.12350107},
        1.04618964,
                      1.07135141,
   {
                                     1.10434958,
                                                     1.07915085},
   {
        1.13404806,
                      1.11671807,
                                                     1.18008965},
                                     1.17914527,
   {
        1.04548516,
                      1.11455931,
   {
        1.18984056,
                      1.19371846,
                                     1.19065201,
                                                     1.18269596},
25 };
```

40 {

1.06343087,

0.38532917,

```
*/
  /* Conexant System Inc.
  /* 4311 Jamboree Road
                                                */
5 /* Newport Beach, CA 92660
  /*-----
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   /* means or used to make any derivative work (such as transformation */
   /* or adaptation) without the authorisation of Conexant System Inc. */
   /* FILE: FLOAT64 gainVQ_4_1024
FLOAT64 gainVQ_4_1024[MSMAX_4_1024][GVQ_VEC_SIZE_4D]= {
                                             0.05200000},
                                0.08030000,
      0.32430000,
                   0.14200000,
                                             0.20130000},
                                0.19540000,
                   0.29580000,
20 {
      0.36890000,
                                             0.17139738},
                   0.24252909,
                                0.20377341,
      0.44279383,
   {
                                             0.09180000},
                                0.13810000,
                   0.18810000,
      0.51880000,
                                             0.23960000},
                                0.26890000,
                   0.35830000,
      0.45280000,
                                             0.40950000},
                                0.37570000,
                   0.31750000,
       0.29830000,
                                             0.07790000},
25 {
       0.69840000,
                   0.17180000,
                                0.11820000,
                                             0.22210000},
                                0.27020000,
       0.67440000,
                   0.31330000,
                                             0.42710000},
                                0.40410000,
                   0.55970000,
       0.37190000,
   {
                                             0.31260000},
                                0.33640000,
                   0.47560000,
       0.60470000,
                                0.52590000,
                                             0.40260000},
       0.44840000,
                   0.41050000,
   {
                                             0.15330000},
                   0.26840000,
                                0.22640000,
       0.85080000,
. 30 {
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  /* Conexant System Inc.
  /* 4311 Jamborec Road
  /* Newport Beach, CA 92660
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15 /* or adaptation) without the authorisation of Conexant System Inc. */
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  /* Conexant System Inc.
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5 /* Newport Beach, CA 92660
  /*_____
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  /* FILE: FLOAT64 gp3_tab
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/* Conexant System Inc.
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 5 /* Newport Beach, CA 92660
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  /* FILE: FLOAT64 gainSQ_1_32
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30 2.00929650,
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40 1.00617023,
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40

885 0.86949491, 4,51286186, 3.52957861, 0.66628545, 5 0.50710786, 8.51290000, 7.51286186, 2.31398228, 1.83583729, 10 }; /* Conexant System Inc. 15 /* 4311 Jamborec Road /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc. 20 /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in any form or by any */ /* means or used to make any derivative work (such as transformation */ /* or adaptation) without the authorisation of Conexant System Inc. */ 25 /* FILE: FLOAT64 gainSQ_1_64 FLOAT64 gainSQ 1_64[MSMAX_1_64]={ 0.00500000, 30 0.01000000, 0.01613892, 0.02227783, 0.08039343, 0.13850902, 35 0.14834423, 0.15817945, 0.16593757,

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15
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   /* Conexant System Inc.
   /* 4311 Jamboree Road
   /* Newport Beach, CA 92660
20 /*-----
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25 /* means or used to make any derivative work (such as transformation */
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};

```
*/
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  */
15 /* FILE: FLOAT64 gainVQ_3_256
  FLOAT64 gainVQ_3_256[MSMAX_3_256][GVQ_VEC_SIZE_3D]={
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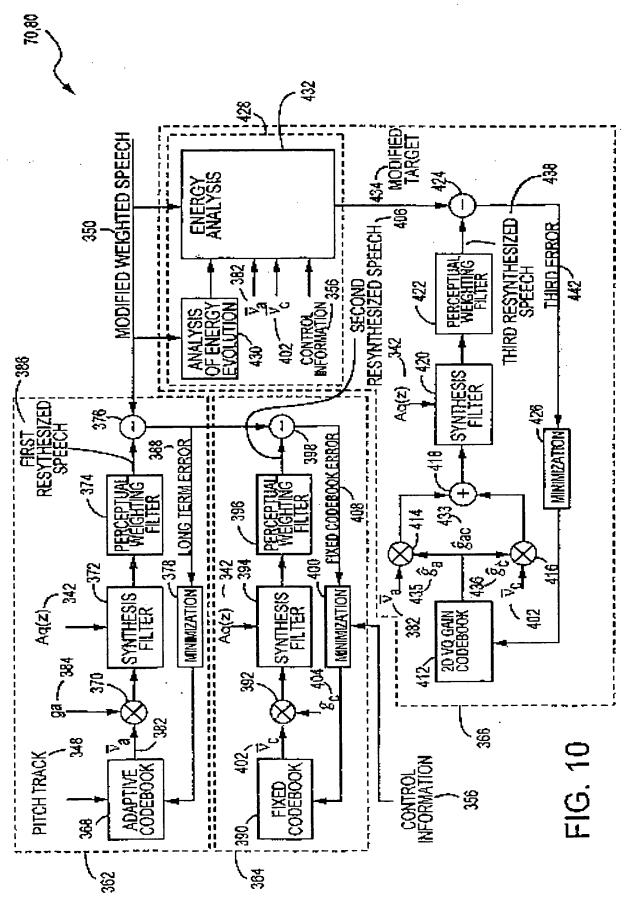
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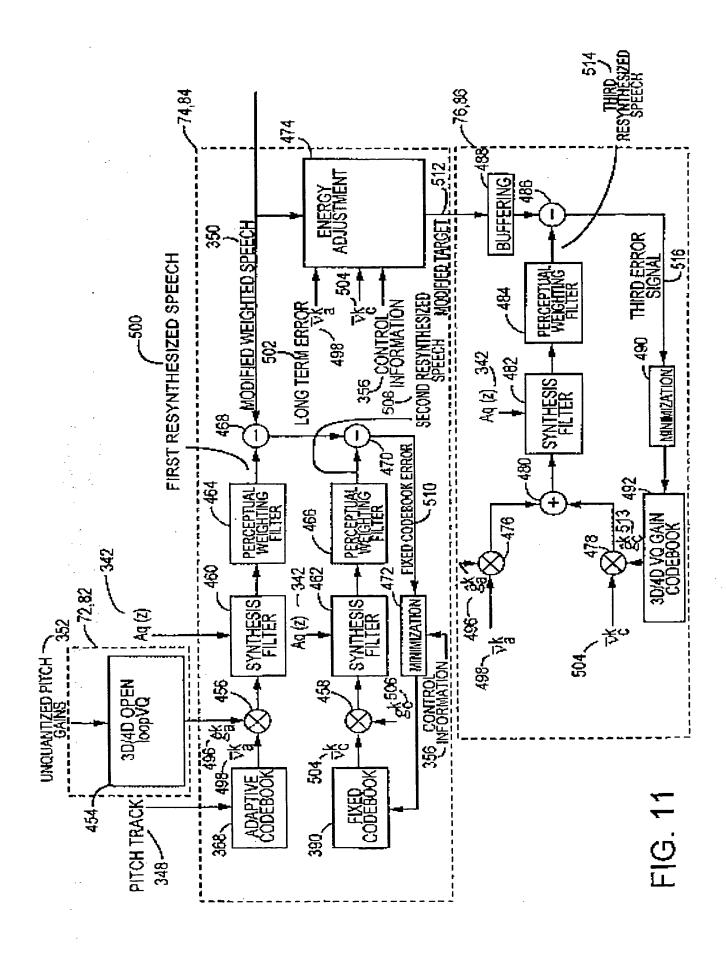
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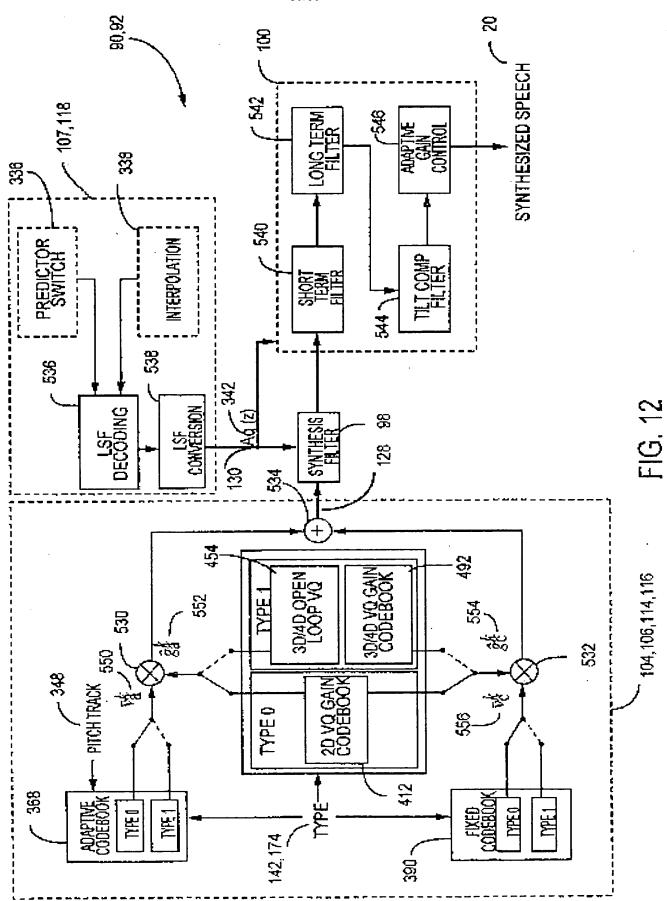
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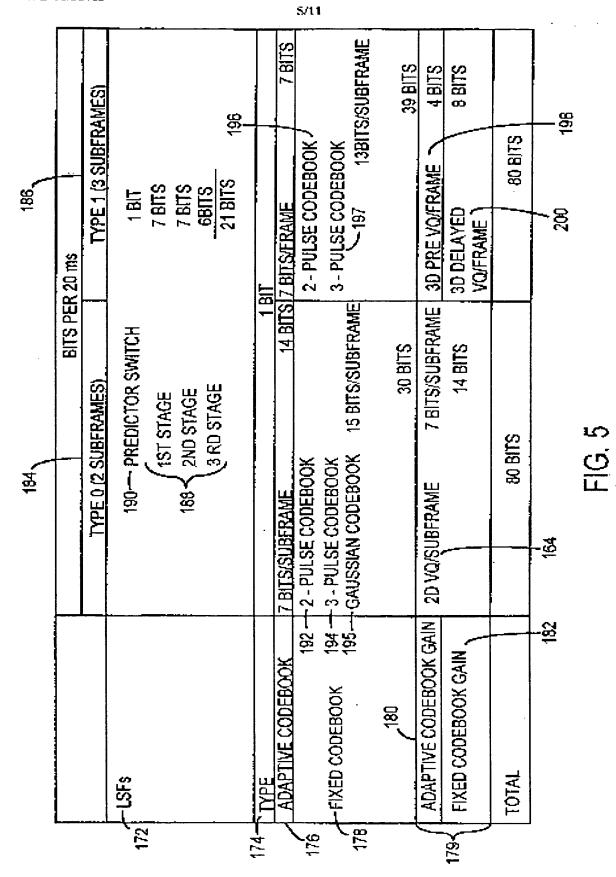




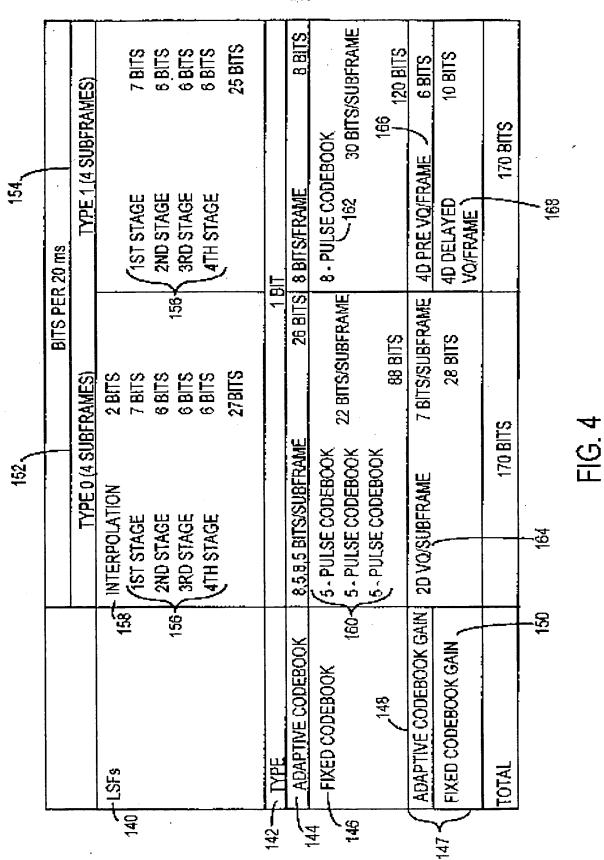
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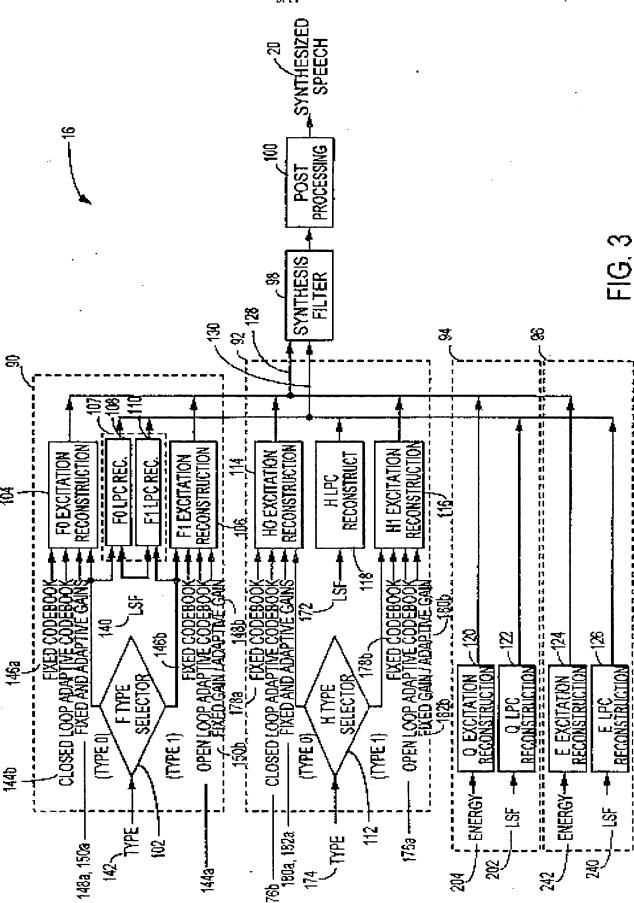


BITS PER 20 ms	206 - INTERPOLATION 2 BITS 7 BITS 7 BITS 208 2ND STAGE 6 BITS 8 BITS 3RD STAGE 6 BITS 6 BITS 1 STAGE 6 BITS 1 STAGE 1 BITS 1 BI	6 BITS/SUBFRAME 39 BITS	FIG. 6 7 / 12	BITS PER 20 ms 4 81TS 244 { 2ND STAGE 4 BITS 4 BITS 3 BITS 14 BITS 1	5 BITS/FRAME 16 BITS
	202~LSFs	204 ~ ENERGY TOTAL	·	240~LSFs	242 - ENERGY TOTAL

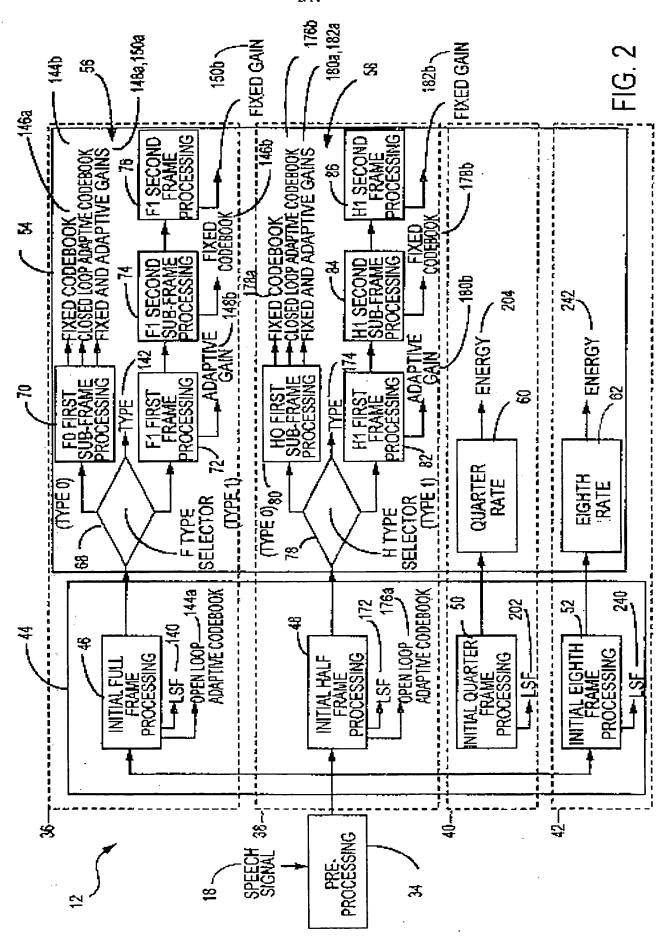


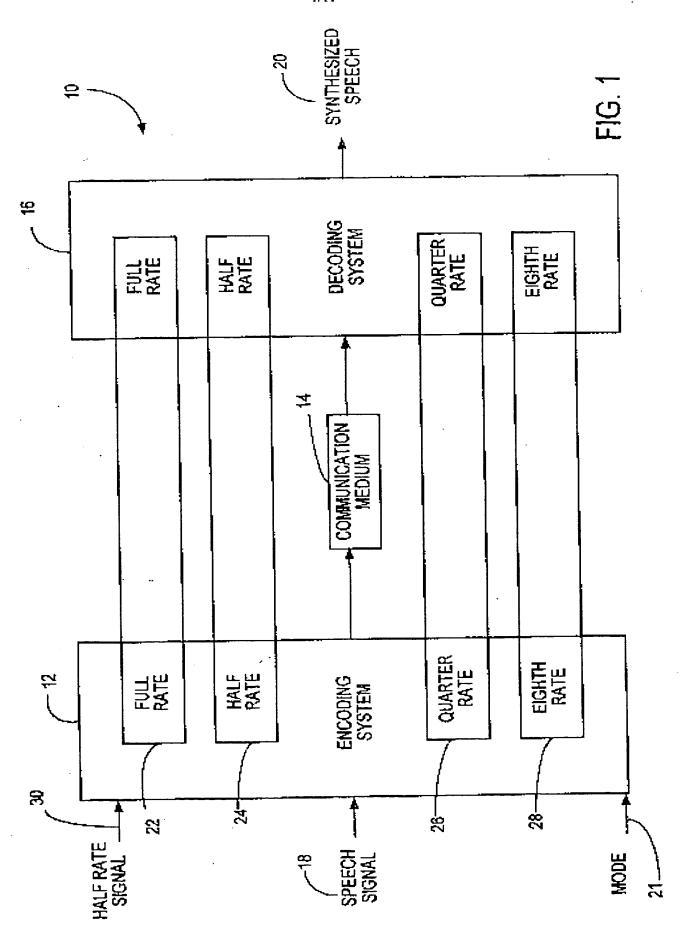
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INTERNATIONAL SEARCH REPORT

Information on patent family members

Intern 181 Application No PCT/US 00/25182

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
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			AU	3209595 A	04-03-1996
,			BR	95 0 6307 A	05-08-1997
			CN	1131994 A	25-09-1996
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			WO	9604646 A	15-02-1996
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CA 2239294	Α		NONE		

INTERNATIONAL SEARCH REPORT

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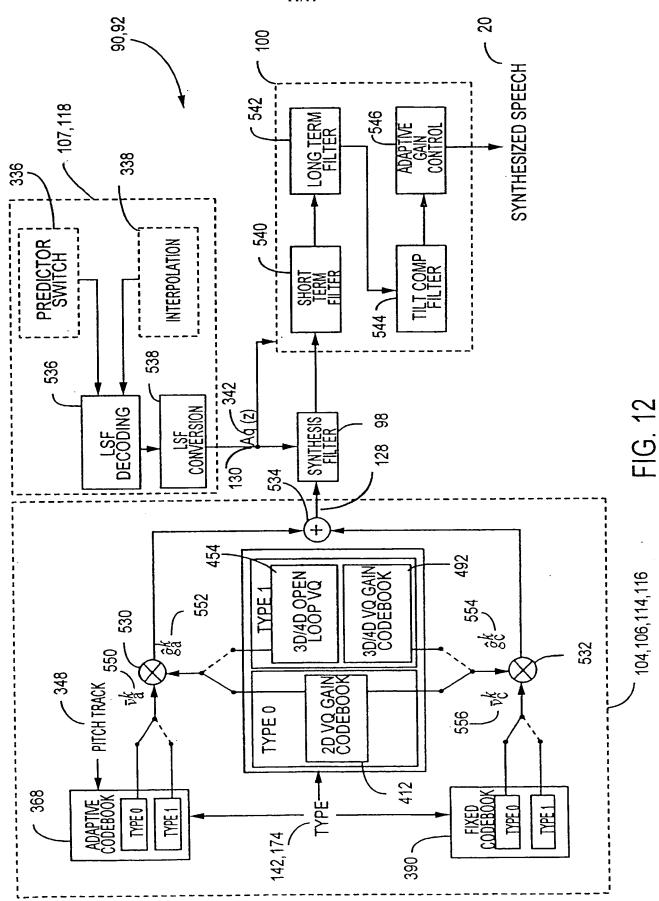
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C.(Continu	ation) DOCUMENTS CONSIDERED TO BE RELEVANT	
Category "	Citation of document, with indication where appropriate, of the relevant passages	Relevant to ctaim No.
X	OZAWA K ET AL: "M-LCELP SPEECH CODING AT 4 KB/S WITH MULTI-MODE AND MULTI-CODEBOOK" IEICE TRANSACTIONS ON COMMUNICATIONS, JP, INSTITUTE OF ELECTRONICS INFORMATION AND COMM. ENG. TOKYO, vol. E77B, no. 9, 1 September 1994 (1994-09-01), pages 1114-1121, XP000474108 ISSN: 0916-8516 figure 1	3
Y	* page 1115 "6) Gain quantization" * idem	2
Y,P	CA 2 239 294 A (FOODEEI M) 29 November 1999 (1999-11-29) page 4, line 23 - line 30 page 6, line 14 - line 30 page 13, line 12 - line 16	2
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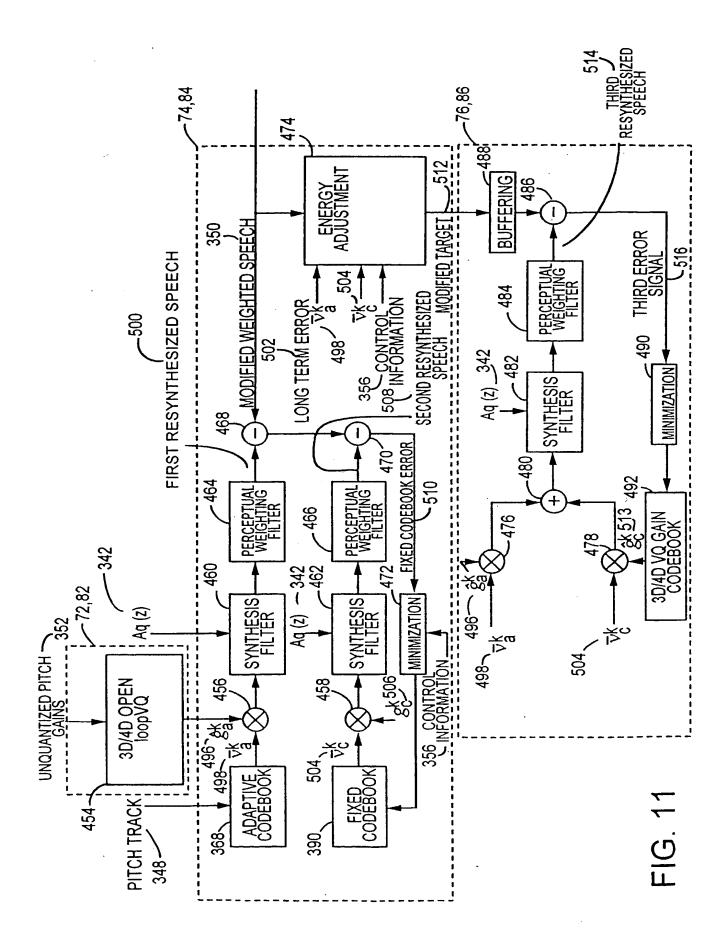
INTERNATIONAL SEARCH REPORT

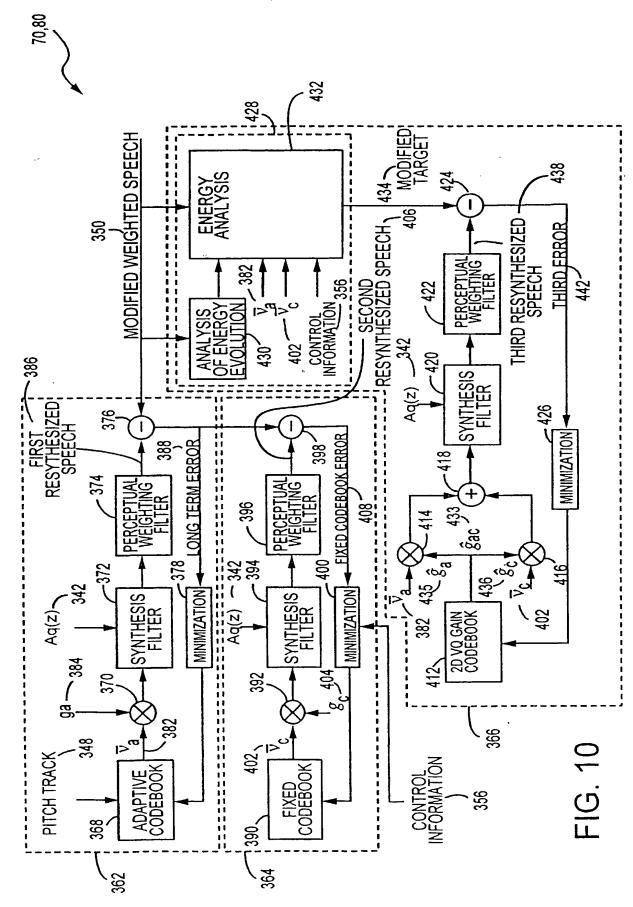
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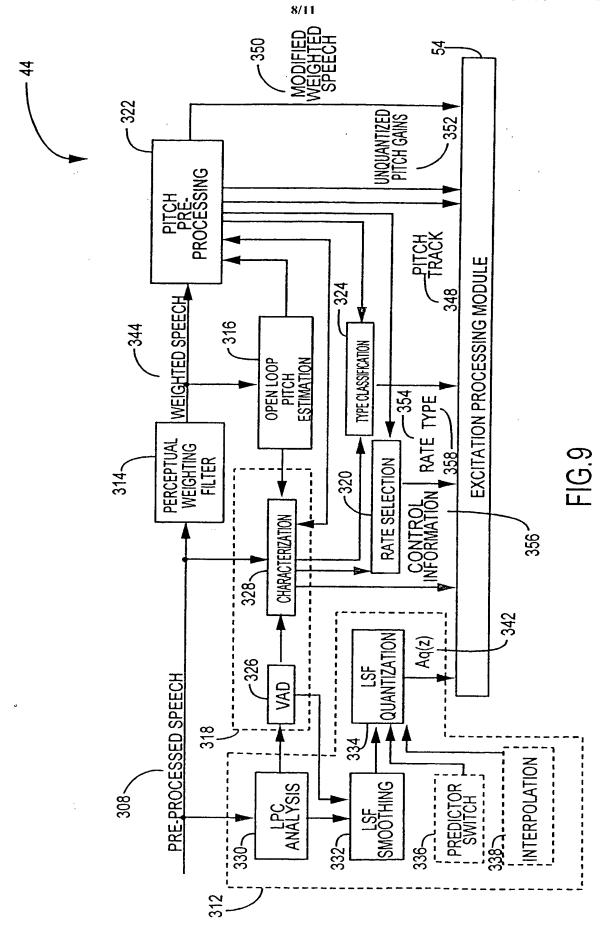
A. CLASSII IPC 7	G10L19/14						
According to	International Patent Classification (IPC) or to both national classifica	ation and IPC					
B. FIELDS SEARCHED							
	Minimum documentation searched (classification system followed by classification symbols) IPC 7 G10L						
	Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used)						
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, WPI Data							
C. DOCUME	ENTS CONSIDERED TO BE RELEVANT						
Category °	Citation of document, with indication, where appropriate, of the rele	evant passages	Relevant to claim No.				
Х	CELLARIO L ET AL: "CELP CODING A VARIABLE RATE"	Т	1				
X	EUROPEAN TRANSACTIONS ON TELECOMMUNICATIONS AND RELATED TECHNOLOGIES, IT, AEI, MILANO, vol. 5, no. 5, 1 September 1994 (1994-09-01), pa 69-79, XP000470681 ISSN: 1120-3862 figure 1 paragraph '04.3! US 5 911 128 A (DEJACO ANDREW P) 8 June 1999 (1999-06-08) column 6, line 50 - line 60 column 11, line 44 - line 46	· /	1				
X Furth	ner documents are listed in the continuation of box C.	X Patent family members are tisted	in annex.				
T tater document published after the international filing date or priority date and not in conflict with the application but cited to understand the priority date and not in conflict with the application but cited to understand the priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention siling date *X* document but published on or after the international filing date of another citation or other special reason (as specified) *O' document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed *T* tater document published after the international filing date or or priority date and not in conflict with the application but cited to understand the priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention or cannot be considered novel or cannot be considered novel or cannot be considered novel or cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. **A* document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention **C* document of particular relevance: the claimed invention cannot be considered novel or cannot be							
Date of the	actual completion of the international search	Date of mailing of the international sea	arch report				
1	9 December 2000	27/12/2000					
Name and n	nailing address of the ISA European Palent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nt.	Authorized officer Krembel, L					

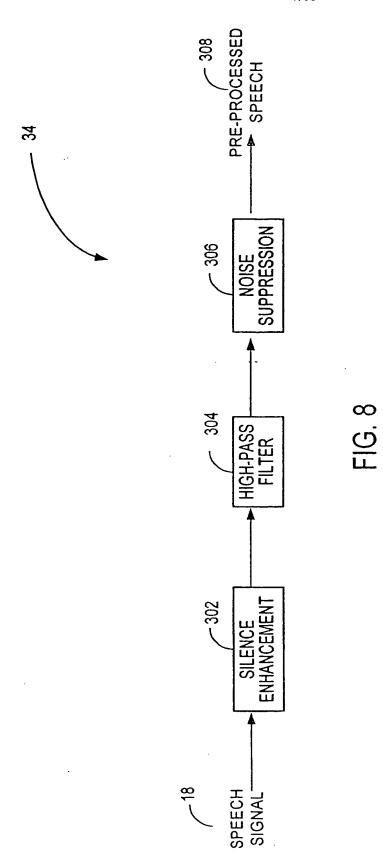
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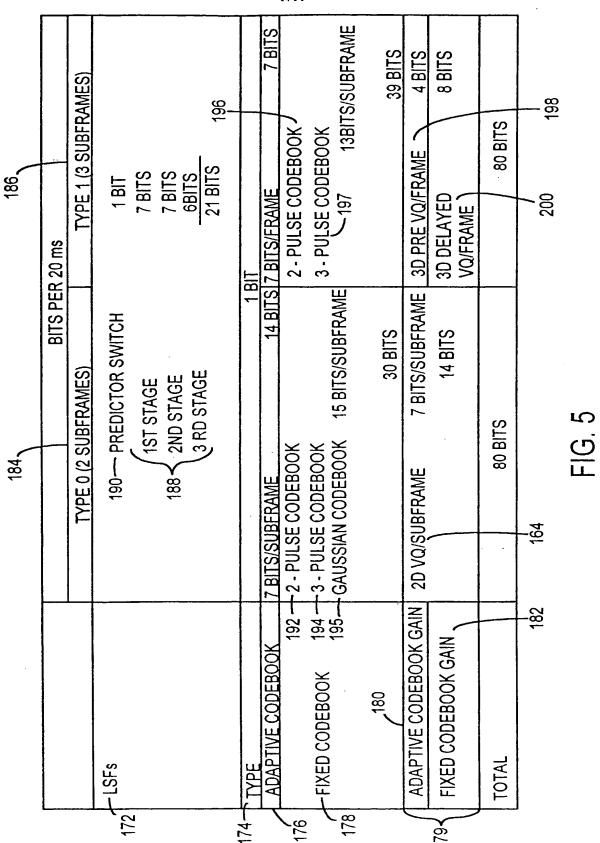


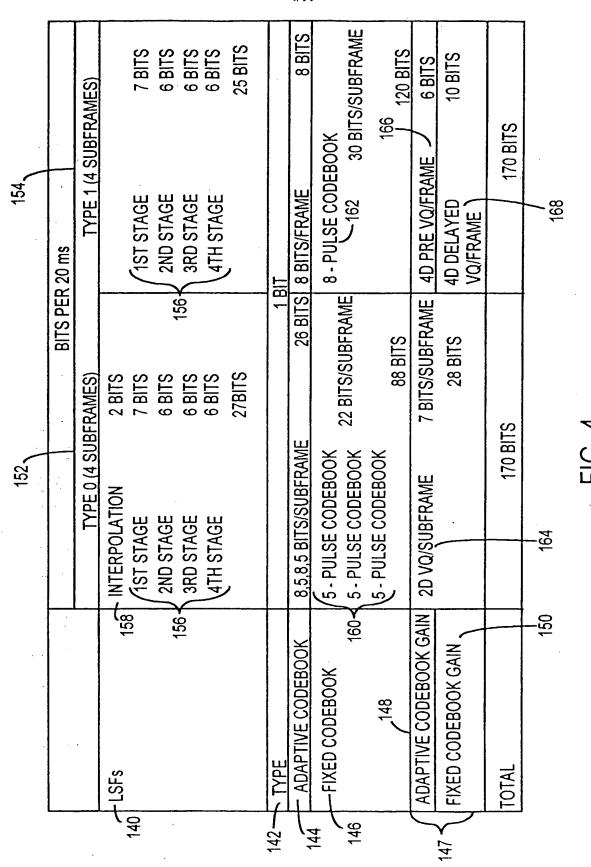


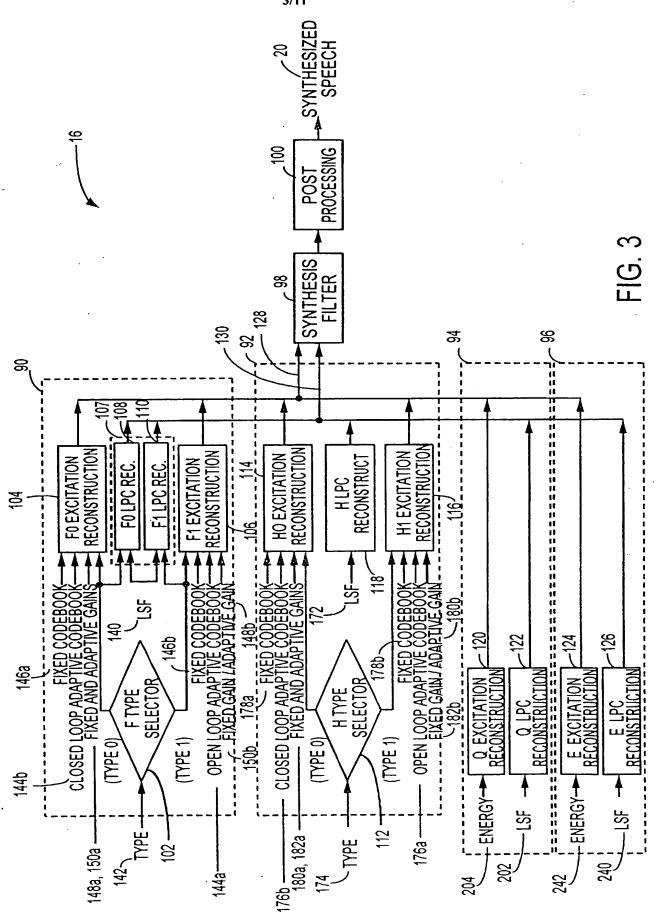


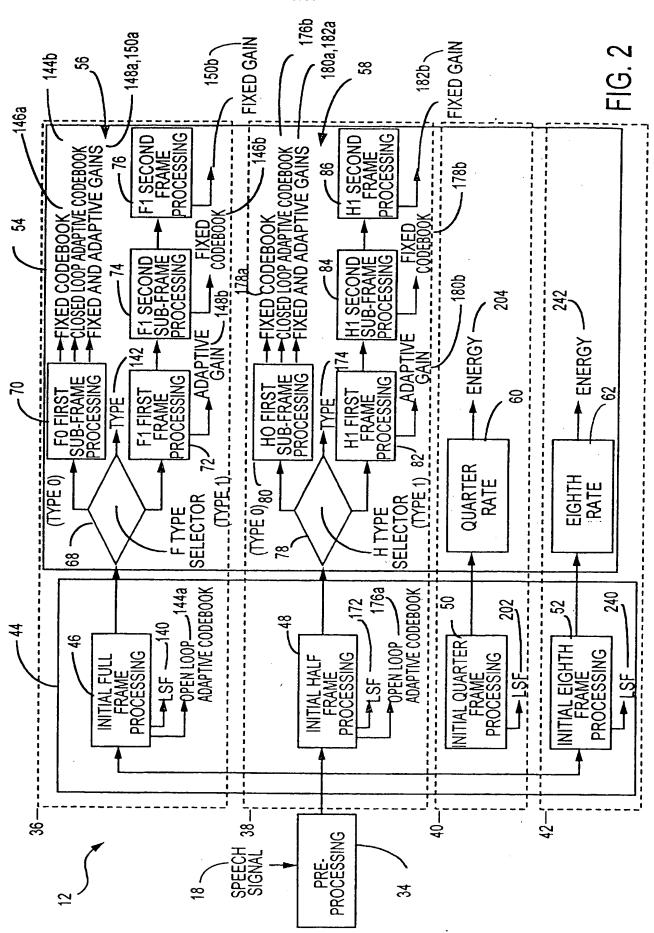
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		/ 1ST STAGE	
	208	/ 2ND STAGE	6 BITS
,		(3KU STAGE (4TH STAGE	6 BITS 27 BITS
204 - ENERGY		6 BITS/SUBFRAME	12 BITS
TOTAL		39 BITS	1
		FIG. 6	
		5	
		7/12	
		BITS PER 20 ms	0 ms
240—LSFs	244	/ 1ST STAGE 2ND STAGE	4 BITS 4 BITS
	-	L3KD SIAGE	3 BILS 11 BITS
242 — ENERGY		5 BITS/FRAME	5 BITS
TOTAL		16 BITS	(0
		FIG 7	
)	

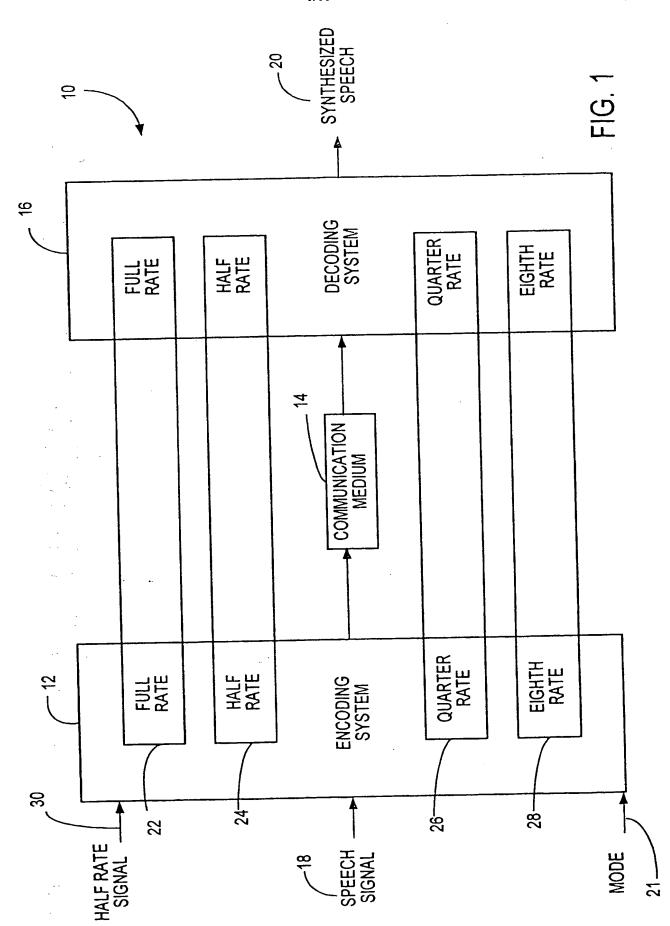
BNSDOCID: <WO__0122402A1 I_>











6	an adaptive codebook component (144, 176) forming a plurality of the bits in the bitstream, where the
7	adaptive codebook component (144, 176) comprises a long term predictor of the frame that is encoded in digital
8	form;
8	a fixed codebook component (146, 178) forming a plurality of the bits in the bitstream, where the fixed codebook
9	component (144, 176) comprises a long term residual of the frame that is encoded in digital form; and
10	a gain component (147, 179) forming a plurality of the bits in the bitstream, where the gain component
11	(147, 179) comprises an adaptive codebook gain and a fixed codebook gain that is encoded in digital form.

What is claimed is:

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- 1. A speech compression system (10) for encoding and decoding frames of a speech signal (18) to generate synthesized speech (20) comprising:
- a full-rate codec (22) operable to encode and decode the frames, the full-rate codec (22) being selectively activatable based on a rate selection and a type classification of each frame;
- a half-rate codec (24) operable to encode and decode the frames, the half-rate codec (24) being selectively activatable based on the rate selection and the type classification of each frame;
- a quarter-rate codec (26) operable to encode and decode the frames, the quarter-rate codec (26) being selectively activatable based on the rate selection; and
- an eighth-rate codec (28) operable to encode and decode the frames, the eighth-rate codec (28) being selectively activatable based on the rate selection.
- 2. An excitation-processing module (54) for encoding a fixed codebook gain and an adaptive codebook gain, where the gains are generated during encoding of a frame of a speech signal (18), comprising:
- a type selector module (68, 78) operable to selectively designate the type classification of the frame as Type Zero and Type One;
- a first subframe processing module (70, 80) that is operable to quantize the fixed codebook gain and the adaptive codebook gain when the type classification of the frame is Type Zero, the first subframe processing module (70, 80) comprising a two-dimensional vector quantizer (164) and a 2D gain quantization table that is searchable by the two-dimensional vector quantizer (164), where the 2D gain quantization table comprises a plurality of predetermined vectors that are selectable by the two-dimensional vector quantizer (164) to jointly encode the fixed codebook gain and the adaptive codebook gain;
- a first frame processing module (72, 82) that is operable to quantize the adaptive codebook gain when the type classification of the frame is Type One, the first frame processing module (72, 82) comprising a multi-dimensional pre vector quantizer (166, 198) and a pre-gain quantization table that is searchable by the multi-dimensional pre vector quantizer (166, 198), where the pre-gain quantization table comprises a plurality of predetermined vectors that are selectable by the multi-dimensional pre vector quantizer (166, 198) to encode the adaptive codebook gain; and
- a second frame processing module (76, 86) that is operable to quantize the fixed codebook gain based on the type classification of the frame as Type One, the second frame processing module (76, 86) comprising a multi-dimensional delayed vector quantizer (168, 200) and a delayed gain quantization table that is searchable by the multi-dimensional delayed vector quantizer (168, 200), where the delayed gain quantization table comprises a plurality of predetermined vectors that are selectable by the multi-dimensional delayed vector quantizer (168, 200) to encode the fixed codebook gain.
 - A bitstream that represents a frame of a speech signal (18) comprising:
- a type component (142, 174) forming a bit in the bitstream, where the type component (142, 174) provides a type classification of the frame and controls the coding;
- an LSF component (140, 172) forming a plurality of the bits in the bitstream, where the LSF component (140, 172) comprises a spectral representation of the frame that is encoded in digital form;

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```
963
    \{0, 5, 10, 15, 20, 25, 30, 35, 2, 7, 12, 17, 22, 27, 32, 37\},\
    {1, 6, 11, 16, 21, 26, 31, 36, 3, 8, 13, 18, 23, 28, 33, 38},
    {4, 9, 14, 19, 24, 29, 34, 39, 0, 0, 0, 0, 0, 0, 0, 0},
    {1, 6, 11, 16, 21, 26, 31, 36, 3, 8, 13, 18, 23, 28, 33, 38},
     {4, 9, 14, 19, 24, 29, 34, 39, 0, 0, 0, 0, 0, 0, 0, 0},
 );
 /*----*/
     5 pulses CB : 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
 /*----*/
 static short track_5_3_2[5][8]={
    {0, 1, 2, 3, 4, 6, 8, 10},
     {5, 9, 13, 16, 19, 22, 25, 27},
15
     {7, 11, 15, 18, 21, 24, 28, 32},
     {12, 14, 17, 20, 23, 26, 30, 34},
     {29, 31, 33, 35, 36, 37, 38, 39}
 };
20
       5 pulses CB : 5p \times 3b + 5b \text{ signs} = 20 \text{ bits}
  /*----*/
25
  static short track_5_3_1[5][8]={
     {0, 1, 2, 3, 4, 5, 6, 7},
     {8, 9, 10, 11, 12, 13, 14, 15},
     {16, 17, 18, 19, 20, 21, 22, 23},
     {24, 25, 26, 27, 28, 29, 30, 31},
     {32, 33, 34, 35, 36, 37, 38, 39}
  );
35 */
  /*---- END -----
40 */
```

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```
/*-----*/
 /* 1 pulse CB : 1 pulse x 4 bits/pulse + 1 signs = 5 (4) bits
 /*----*/
 static INT16 track_1_4_0[1][16]={
     {0, 4, 8, 12, 16, 20, 24, 28, 33, 38, 43, 48, 53, 58, 63, 68}
 };
10
 /*-----/
 /*----*/
15
 /*----*/
     8 pulses CB : 6p \times 3b + 2p \times 4b + 4b \text{ signs} = 30 \text{ bits}
 /*----*/
20 static INT16 track_8_4_0[8][16]={
    \{0, 5, 10, 15, 20, 25, 30, 35, 2, 7, 12, 17, 22, 27, 32, 37\},
    \{1, 6, 11, 16, 21, 26, 31, 36, 0, 0, 0, 0, 0, 0, 0, 0\}
   \{3, 8, 13, 18, 23, 28, 33, 38, 0, 0, 0, 0, 0, 0, 0, 0\}
   {4, 9, 14, 19, 24, 29, 34, 39, 0, 0, 0, 0, 0, 0, 0, 0},
25
    \{0, 5, 10, 15, 20, 25, 30, 35, 2, 7, 12, 17, 22, 27, 32, 37\},
    {1, 6, 11, 16, 21, 26, 31, 36, 0, 0, 0, 0, 0, 0, 0, 0},
    {3, 8, 13, 18, 23, 28, 33, 38, 0, 0, 0, 0, 0, 0, 0, 0},
    \{4, 9, 14, 19, 24, 29, 34, 39, 0, 0, 0, 0, 0, 0, 0, 0\}
30 );
 /*----*/
 35 /*----*/
 /*----*/
     5 pulses CB : 3p \times 4b + 2p \times 3b + 3b \text{ signs} = 21 \text{ bits}
 40
 static short track_5_4_0[5][16]={
```

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```
27, 29, 31, 33, 35, 37, 39, 41, 43, 45, 47, 49, 51));
 /*----*/
      3 pulses CB : 3 pulses \times 2 bits/pulse + 3 signs = 9 bits
5 /*----*/
 static INT16 track_3_2_0[3][4]={
       \{0, 3, 6, 9\}, \{1, 4, 7, 10\}, \{2, 5, 8, 11\}
 );
10
 /*----*/
      1 pulse CB : 1 pulse x 3 bits/pulse + 1 signs = 4 (3) bits
 /*----*/
15 static INT16 track_1_3_0[1][8]={
      {0, 5, 11, 17, 23, 29, 35, 41}
  };
20 /*----*/
  /*----*/
.25 /*-----*/
  /* 2 pulses CB : 2pulses x 6.5bits/pulse + 1 sign = 14 bits
  /*----*/
  static INT16 track_2_7_1[2][80]={
     {0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15,
30
      16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31,
      32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47,
      48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63,
      64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79},
 35
     \{0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15,
      16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31,
      32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47,
      48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63,
      64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79)
 40
 ` };
```

```
=*/
/* Conexant System Inc.
                                 */
 /* 4311 Jamboree Road
 /* Newport Beach, CA 92660
                                 */
 */
10 /* Copyright(C) 2000 Conexant System Inc.
 /* ALL RIGHTS RESERVED:
                                 */
 /* No part of this software may be reproduced in any form or by any */
 /* means or used to make any derivative work (such as transformation */
15 /* or adaptation) without the authorisation of Conexant System Inc. */
 /* LIBRARY: tracks.tab
 /*---- TABLES ------
 25 */
 static INT16 srchpuls[12] = {0, 1, 2, 3, 4, 6, 5, 7, 0, 7, 1, 6};
 /*----*/
2 pulses CB: 2pulses x 5bits/pulse + 2 signs = 12 bits
                                */
35 /*-------/
 static INT16 track_2_5_0[2][32]={
   {0,1,2,3,4,5,6,7,8,9,10, 12, 14, 16, 18, 20, 22, 24, 26,
   28, 30, 32, 34, 36, 38, 40, 42, 44, 46, 48, 50, 52},
40
   {1, 3, 5, 7, 9, 11,12,13,14,15,16,17,18,19,20,21,22,23, 25,
```

					939		
	{	0.00000000	0.00000000	0.00000000	0.00000000,	0.00000000,	0.00000000,
	0.00	000000 ,	0.00000000,	0.00000000,	0.00000000},		
	{	0.00000000	0.00000000	0.00000000	0.00000000,	0.00000000,	0.00000000,
	0.00	000000 ,	0.00000000,	0.00000000,	0.00000000},		
5	{	0.00000000	0.00000000	0.00000000	0.00000000,	0.00000000,	0.00000000,
	0.00	0000000,	0.00000000,	0.00000000,	0.00000000},		•
	{	0.00000000	0.00000000	0.00000000	0.00000000,	0.00000000,	0.00000000,
	0.00	0000000,	0.00000000,	0.00000000,	0.00000000},		
	{	0.00000000	0.00000000	0.0000000	0, 0.00000000,	0.00000000,	0.00000000,
10	0.00	, 000000	0.00000000,	0.00000000,	0.00000000},		
	{	0.00000000	0.00000000	0.0000000	0.00000000,	0.00000000,	0.00000000,
	0.00	0000000 ,	0.00000000,	0.00000000,	0.00000000},		
	{	0.00000000	0.0000000	0.0000000	0.00000000,	0.00000000,	0.00000000,
	0.00	, 000000	0.00000000,	0.00000000,	0.00000000},		
15	{	0.0000000	0.0000000	0.0000000	0,00000000,	0.00000000,	0.00000000,
	0.00	0000000 ,	0.00000000,	0.00000000,	0.00000000},		
	{	0.0000000	0.0000000	0.0000000	0,00000000,	0.00000000,	0.00000000,
	0.00	0000000,	0.00000000,	0.00000000,	0.00000000},		
	},						
20	} ;						
	/*				·*/		
							• /
						: # = # = # = # = # # # #	=======================================
25	/*			END	*/		**

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	{ 0.00000000, 0.00000000, 0.000000000,	958 0.00000000 ,	0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.0	•	•	
	{ 0.00000000, 0.00000000, 0.000000000,		0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.0		•	
	{ 0.00000000, 0.00000000, 0.00000000,		0.00000000,	0.00000000 ,
	0.00000000, 0.00000000, 0.00000000, 0.0			
	{ 0.00000000, 0.00000000, 0.00000000,		0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.0			
	{ 0.00000000, 0.00000000, 0.00000000,		0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.0			
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	0.00000000, 0.00000000, 0.00000000, 0.			
	{ 0.00000000, 0.00000000, 0.000000000,		0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.			
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	0.00000000, 0.00000000, 0.00000000, 0.			
	{ 0.00000000, 0.00000000, 0.000000000,		0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.			
-	{ 0.00000000, 0.00000000, 0.000000000,	0.00000000,	0.00000000,	0.00000000,
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	0.00000000, 0.000000000, 0.000000000, 0.	00000000},		
	$ \{ \ 0.000000000 \; , \ 0.000000000 \; , \ 0.000000000 \; , \\$	0.00000000,	0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.	00000000},		
25	$ \{ \ 0.00000000 \; , \ 0.00000000 \; , \ 0.000000000 \; , \\$	0.00000000,	0.00000000 ,	0.00000000,
	0.00000000, 0.00000000, 0.000000000, 0.			
	$ \{ 0.00000000 \; , 0.00000000 \; , 0.00000000 \; , \\$	0.00000000,	0.00000000,	0.00000000,
	0.00000000, 0.00000000, 0.00000000, 0.			
	$ \{ 0.00000000 \; , 0.00000000 \; , 0.00000000 \; , \\$		0.00000000,	0.00000000,
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	0.00000000, 0.00000000, 0.00000000, 0	•		
	(0.00000000, 0.00000000, 0.000000000,		0.00000000,	0.00000000 ,
	0.00000000, 0.00000000, 0.00000000, 0			
35	{ 0.00000000, 0.00000000, 0.000000000,		0.00000000,	0.00000000 ,
	0.00000000, 0.00000000, 0.00000000, 0			
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40	0.00000000, 0.00000000, 0.00000000, 0	.00000000},		

957 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000, 0.00000000. 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.000000000. 0.000000000, 0.00000000, 0.00000000. 0.00000000 5 { 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000, 10 0.00000000 . 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000. 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 15 { 0.00000000, 0.00000000, 0.00000000}, 0.00000000. 0.00000000, 0.00000000, 0.00000000, 0.00000000 0.00000000, 0.00000000, 0.00000000}, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000. 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.000000000, 20 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.000000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000. 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000. 25 { 0.000000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000 0.00000000, 0.00000000, 30 0.00000000, 0.00000000, 0.00000000, 0.00000000. 0.00000000, 0.00000000, 0.00000000. 0.00000000, 0.00000000, 0.00000000}, 0.00000000, 0.00000000. 0.00000000, 0.00000000, 0.00000000, 0.00000000. 0.00000000. 0.00000000. 0.00000000, 0.00000000, 0.00000000 0.00000000, 0.00000000, 0.00000000, 0.00000000. 0.00000000, 0.00000000, 0.00000000, 35 { 0.000000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000, 0.00000000}, 0.00000000, 40 0.00000000 . 0.00000000,

æ.

	{ -0.001777270.0	0.00112403	, 0.00121361, -	0.00446313,	-0.00190805,
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		00074135 , -0.00320684		0.00146160,	-0.00175724,
	•	926, -0.01098564,			
5	· ·	0242869, -0.00034958		0.00105102,	0.00117536,
	•	328, -0.00521109, -			•
		0.00070051		0.00261332,	-0.00489216,
	•	007, -0.00620281, -			
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10	0.00167806, -0.00123	525, -0.00775452, -	0.00448655},		
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	{ 0.00000000, 0.0	0.0000000 , 0.00000000	, 0.00000000,	0.00000000,	0.00000000 ,
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15	{ 0.00000000, 0.0	0.0000000 , 0.00000000	, 0.00000000,	0.00000000,	0.00000000 ,
	0.00000000, 0.000000	000, 0.00000000,	0.00000000},	•	
	{ 0.00000000, 0.0	0.0000000 , 0.00000000	, 0.00000000,	0.00000000,	0.00000000,
	0.00000000, 0.000000	000, 0.00000000,	0.00000000},		·
	{ 0.00000000, 0.0	0.0000000 , 0.0000000	, 0.00000000,	0.00000000,	0.00000000 ,
20	0.0000000, 0.00000	000, 0.00000000,	0.00000000},		
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	{ 0.00000000, 0.0	0.0000000 , 0.00000000	, 0.00000000,	0.00000000 ,	0.00000000,
	0.0000000 , 0.00000	000, 0.00000000,	0.00000000},		
25	{ 0.00000000, 0.0	0.0000000 , 0.00000000	, 0.00000000,	0.00000000,	0.00000000 ,
	·	000, 0.00000000,			
	{ 0.00000000, 0.0	0.00000000 , 0.000000000	, 0.00000000,	0.00000000,	0.00000000 ,
	ŕ	000, 0.00000000,			
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30		0.00000000,		•	
	-	0.00000000 , 0.000000000	•	0.00000000,	0.00000000 ,
		000, 0.00000000,			
	-	0.0000000 , 0.00000000		0.00000000 ,	0.00000000 ,
	·	0.0000000000000000000000000000000000000			
35	•	0.0000000 , 0.00000000		0.00000000 ,	0.00000000 ,
		0.00000000,			
		0.00000000	•	0.00000000,	0.00000000 ,
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	•	0.0000000, 0.0000000		0.00000000,	v.vvvvvvvv ,
40	0.00000000, 0.00000	0.00000000,	v.vvvvvvvv),		

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      0.00014292, -0.00202034, -0.00166715, -0.00079566,
                                                                          -0.00596721, -
                                                            -0.00315657,
  0.00141144, -0.00552633, 0.00226848, -0.00208710},
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5 { 0.00093111, -0.00165178, -0.00032798, 0.00295016,
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  0.00431917, 0.00469877, -0.00039486, 0.00895634},
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                                                            -0.00378623,
                                                                          0.00180324, -
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                                                                          0.00239334,
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   \{ -0.00174145, 0.00091289, -0.00316276, -0.00256852,
                                                                          0.00104166,
                                                             0.00151101,
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                                                                          -0.00256869, -
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                                                              0.00150767,
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                                                              0.00227109,
                                                                           -0.00205330, -
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    0.00336525, -
                                                              0.00279677,
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                                                                           -0.00030417, -
                                                              -0.00001751,
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                                                               0.00154925,
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  40 0.00314152, 0.00015458, -0.00658762, 0.01286261},
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30'900010 KMO 0122402A1_1 :

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   /* 4311 Jamborec Road
15 /* Newport Beach, CA 92660
   /* Copyright(C) 2000 Conexant System Inc.
   /* ALL RIGHTS RESERVED:
20 /* No part of this software may be reproduced in any form or by any */
   /* means or used to make any derivative work (such as transformation */
   /* or adaptation) without the authorisation of Conexant System Inc. */
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4	U U.UUI/1233, U.UU/01/3/, U.UU/102, U	- · · · · · · · · · · · · · · · · · · ·		

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   /* Conexant System Inc.
20 /* 4311 Jamborce Road
   /* Newport Beach, CA 92660
   /* Copyright(C) 2000 Conexant System Inc.
25 /* ALL RIGHTS RESERVED:
   /* No part of this software may be reproduced in any form or by any */
   /* means or used to make any derivative work (such as transformation */
   /* or adaptation) without the authorisation of Conexant System Inc. */
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                                                                                                         -0.00288294, -0.00428708, -
     0.00183602, -0.00071289, -0.00093030, -0.00067826},
     \{ -0.00223623, -0.00171246, -0.00046686, 0.00170002, 
                                                                                                          0.00244198
                                                                                                                                  0.00108873,
25 0.00191275, 0.00055247, 0.00040544, 0.00017678},
                                                                                                         -0.00553476
     { -0.00149753, -0.00154993, 0.00079974, 0.00221293,
                                                                                                                                  -0.00346923,
  0.00211231, -0.00154997, -0.00037169, -0.00069879},
           0.00289225, -0.00045208, 0.00151539, -0.00150161,
                                                                                                          -0.00144310,
                                                                                                                                 -0.00308428,
     0.00211248, 0.00132638, 0.00017002, 0.00056742},
30 \{ -0.00114496, 0.00024257, 0.00315614, -0.00436637, 
                                                                                                         -0.00261428
                                                                                                                                  0.00296444, -
     0.00371535, -0.00308495, -0.00033931, -0.00035859},
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                                                                                                          -0.00211159,
                                                                                                                                  0.00158957, -
     0.00093119, -0.00238958, -0.00021992, -0.00044901},
     \{-0.00071416, -0.00142483, -0.00099961, -0.00030932,
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35\ 0.01041545, -0.00518788, -0.00149510, -0.00175121},
                                                                                                          0.00000000,
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     0.00000000, 0.00000000, 0.00000000, 0.00000000}
                                                                                                          0.00000000, 0.00000000,
            0.00000000
                                   0.00000000, 0.00000000, 0.00000000,
     0.00000000, 0.00000000, 0.00000000, 0.00000000},
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911 -0.00261708, -0.00708436, -0.00241436, 0.00289209, 0.00368781 0.00275999, 0.00069364, 0.00112643, 0.00040431, 0.00059968}, 0.00816243, 0.00244675, 0.00260231, 0.00259305, 0.01015206, 0.01053691, 0.00002146, 0.00051176, -0.00072191, 0.00095204}, 0.00086895, 0.00419154, 0.00104835, -0.00633587, 0.00568223, 0.00322531, 0.00133821, 0.00097780, 0.00104668, 0.00127673}, -0.00387241, -0.00378492, 0.00043591, -0.00710766, -0.00551754, -0.00367991, -0.00182042, -0.00122951, -0.00064698, -0.00034216}, 0.00170909, -0.00010835, -0.00194941, 0.00459307, 0.01194545, 0.00266756, 10 0.00260933, -0.00076864, -0.00036982, -0.00138988}, 0.00572317, 0.00494218, -0.00200888, -0.00363662, -0.00552244 . -{ -0.00180246, 0.00125745, -0.00142807, -0.00116519, -0.00105954}, -0.00700839, -0.01278847, -0.00523787, -0.00443510, -0.00440996, -0.00465344, 0.00353442, -0.00161417, -0.00096975, -0.00051261}, $15 \{ 0.00070303, 0.00616162, -0.00457122, -0.01058358, -0.00684058, -0.00639819, -0.0068819,$ 0.00173663, -0.00185740, -0.00100444, -0.00115079}, $\{ -0.00451619, -0.00642493, 0.00633218, 0.00433383, -0.00216043,$ -0.00197399, -0.00291948, -0.00146477, 0.00004796, -0.00008603}, $\{-0.00139753, -0.00327792, -0.00410406, 0.00047675, -0.00586987, -0.00831267, -0.00885, -0.00$ 20 0.00310294, -0.00191523, -0.00096847, -0.00098327}, $\{ 0.00163188, 0.00087564, 0.00403069, 0.00512087, -0.00835469, -0.00591542, -0.00835469, -0.00591542, -0.00835469, -0.00835469, -0.00591542, -0.00835469, -0.0083546, -0.0083546, -0.008466, -0.008466, -0.00846, -0$ 0.00381267, -0.00157757, -0.00049671, -0.00114209}, 0.00232354, -0.00129666, -0.00119452, -0.00056966}, 25 }, $\{\{-0.00027304, -0.00013603, -0.00068813, 0.00046191, -0.00018108, -0.00143655, -0.00018108, -0$ 0.00143265, -0.00096053, -0.00029350, -0.00041718}, 0.00327783, -0.00262084, -0.00072357, -0.00152624, -0.00289651, 0.00242678, -0.00709684, -0.00418617, -0.00134299, -0.00114547}, 0.00272996, -0.00424907, -0.00511227, -0.00350740, -0.00574813, -0.00178766, 0.00298479, -0.00178375, -0.00080082, -0.00091910}, { -0.00257793, 0.00036133, 0.00264883, -0.00468006, -0.01043396, --0.00843945, 0.00919476, -0.00608852, -0.00215225, -0.00192788}, 0.00164921, { -0.00275632, -0.00064226, -0.00106406, -0.00928170, 0.00177093, 35 0.00330921, -0.00201606, -0.00095143, -0.00074924}, 0.00333945, 0.00294397, -0.00296495, 0.00327592, 0.00031042, -{ 0.00098576, 0.00304436, -0.00168756, -0.00096723, -0.00066603}, { -0.00081979, -0.00166735, 0.00165792, -0.00361096, -0.00656657, 0.00213311. 0.00599592, 0.00224467, 0.00019724, 0.00058344},

```
FLOAT64 B_85k[LP_85k][LQMA_85k][MAXLNp] = {
5 {
  {
               0.20991762, 0.34262841, 0.34478999, 0.36982213, 0.34486193,
     0.12600104,
                                                                             0.25866520
     0.19844608,
                  0.15549710,
                              0.10873224},
     0.09497737, 0.14557657, 0.23054897, 0.24608043, 0.27811978, 0.25669288,
                                                                             0.18951165
     0.14322552, 0.10976944, 0.07578385,
  },
  };
                                          */
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  /*_____*/
  /* FILE: FLOAT64 CBes_08k
FLOAT64 CBes 08k[MAXLTT 08k][LMSMAX_08k][MAXLNp] = {
       0.00269624, 0.00462087, -0.00621642, 0.00078393,
                                                        0.00203606,
                                                                    -0.00385235,
  0.00063855, -0.00073735, 0.00002790, -0.00040057},
35 { -0.00012394, -0.00089956, -0.00388915, -0.00432538, -0.00236658,
                                                                    -0.00101884, -
  0.00111489, -0.00046467, -0.00063866, -0.00059119},
     -0.00091016, 0.00047345,
                              0.00289452,
                                           0.00103649, -0.00014311,
                                                                   -0.00025130, -
  0.00192196, -0.00149023, 0.00030132, -0.00024519},
     -0.00259585, -0.00289246, -0.00766695, -0.00713197,
                                                       0.00507416,
                                                                    0.00050759, -
40 0.00035725, -0.00001324, 0.00003991, 0.00018587},
```

```
909
                                              0.39655069
                                                           0.43984539,
                                                                         0.42178869,
                                                                                      0.34869783
                                0.38110463,
                   0.26721569,
     0.19087084,
                    0.23847475,
                                  0.17468375},
      0.28691864,
   {
                                0.23402430, 0.25625744,
                                                           0.29236925,
                                                                        0.27922926,
                                                                                      0.22702581
     0.13428787,
                   0.16384420,
                                  0.11524615},
                    0.15394289,
      0.18550745,
 5,
                   0.10475287, 0.13371967, 0.16283702, 0.18493450,
                                                                                      0.14728004
                                                                         0.17783386,
     0.10410849,
      0.11948265,
                    0.10261001,
                                  0.08146250},
   },
10 {
   {
                                0.42536339, 0.40318214, 0.39778242,
                                                                         0.34731435,
                   0.25397094,
     0.14936742
                                  0.11001108},
      0.17583478,
                    0.12497067,
                                                          0.26478926,
                                                                         0.23018456,
                                                                                      0.15178193
                   0.15389237,
                                0.24021347, 0.24507006,
     0.09932127,
15
                                  0.06122567},
      0.11368182,
                    0.07674584,
   {
                                                           0.19320946,
                                                                         0.16507346,
                                                                                       0.10600940
                   0.10618676,
                                0.16013783, 0.17315564,
      0.07376684,
                                  0.03790307},
                    0.05146135,
      0.07680543,
20 {
                   0.08328096,
                                0.10640056, 0.12536714, 0.13771986,
                                                                         0.11589609,
                                                                                       0.07617342
     0.06434994,
                    0.03843949,
                                  0.03049829},
       0.05429825,
   },
   };
25
                                               */
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   */
40 /* FILE: FLOAT64 B_85k
```

```
908
   {
                                              0.86612158,
                                                           0.89184674,
                                                                         0.86520149,
                  0.84277373,
                                0.84833453,
     0.78697605
                              0.81292217,
                                           0.74918146},
                0.82649626,
   0.83213462,
                                0.54458026,
                                              0.57964919,
                                                           0.65907100,
                                                                         0.62423454,
     0.48850325,
                  0.51792467,
   0.57897302, 0.57510412,
                              0.53272744,
                                           0.49711640},
                                0.30887250.
                                              0.34767876,
                                                           0.41578426,
                                                                         0.39193895,
     0.27060547
                  0.28492453,
                              0.31077734,
                                            0.30569519},
   0.36463786,
                0.34570964,
10 {
                                                           0.19670950,
                                                                         0.19308363,
                  0.11859086,
                                0.13932375,
                                              0.16531784,
     0.10931949
                0.16239619, 0.14627386,
   0.17434501,
                                           0.14728680},
   },
   };
15
                                              */
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   /* means or used to make any derivative work (such as transformation */
   /* or adaptation) without the authorisation of Conexant System Inc. */
   30 /* FILE: FLOAT64 B 40k
   FLOAT64 B 40k[LP 40k][LQMA_40k][MAXLNp] = {
35 {
   {
      0.45782564, 0.59002827, 0.73704688, 0.73388197, 0.75903791,
                                                                       0.74076479,
                                                                                    0.65966007
      0.58070788, 0.52280647, 0.42738207,
   {
```

			=======
		/	====/
	Conexant System Inc.	*/	
	4311 Jamboree Road	* */	
	Newport Beach, CA 92660	•	•
•		*/	•
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	No part of this software may be reproduced in	any form or by any */	
	* means or used to make any derivative work (so		
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1	* or adaptation) without the authorisation of Col *====================================		====*/
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	* TABLES * * * Conexant System Inc. /* 4311 Jamboree Road /* Newport Beach, CA 92660 /* Copyright(C) 2000 Conexant System Inc.	*/ */ */ */ */ */	
	* TABLES	*/ */ */ */ */ */ */ */ */ */ */ */ */ *	
· · · · · · · · · · · · · · · · · · ·	*TABLES *	*/ */ */ */ */ */ */ */ */ */ */ */ any form or by any */	
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)	*TABLES ** ** ** *Conexant System Inc. /* 4311 Jamboree Road /* Newport Beach, CA 92660 /* /* Copyright(C) 2000 Conexant System Inc. /* /* ALL RIGHTS RESERVED: /* No part of this software may be reproduced in /* means or used to make any derivative work (software of the control	*/ */ */ */ */ */ */ */ */ */ */ */ */ *	-===+/
5	* TABLES	*/ */ */ */ */ */ */ */ */ */ */ */ */ *	-===*/

```
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   -0.050608, -0.081541, 0.011615, 0.254577, -0.114920},
   { 0.092684, -0.170455, -0.066080, -0.104846, 0.063964,
   -0.118125, -0.082160, 0.039378, 0.211586, -0.185354,
5 -0.185207, -0.020687, 0.064207, 0.086469, 0.054981,
   \hbox{-}0.041209, \ 0.119450, \ 0.052270, \hbox{-}0.011401, \hbox{-}0.010358,
    0.021328,\ 0.047320,\ 0.163357,\ -0.063909,\ -0.043402,
   -0.037520, -0.080772, -0.077397, -0.043713, 0.170929,
    0.173935, -0.110798, 0.010913, -0.267308, 0.154860,
10 -0.004086, -0.048883, 0.062513, -0.033936, 0.217462},
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   -0.015832, 0.044615, -0.050922, 0.151970, -0.269232,
   \hbox{-}0.103572,\ 0.024964,\ 0.075853,\ \hbox{-}0.028581,\ 0.045409,
   \hbox{-}0.060437,\ 0.030973,\ \hbox{-}0.070224,\ 0.212762,\ \hbox{-}0.005720,
15 0.013756, 0.014138, 0.182639, -0.017510, 0.022836,
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20 0.003457, -0.305420, -0.074054, 0.088384, -0.033983,
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    -0.168554, 0.085919, -0.085650, -0.138377, 0.021096,
    -0.215684, 0.191959, 0.063860, 0.009517, 0.035008,
25 0.177654, 0.126762, 0.055212, -0.008992, 0.022952,
    0.056175, -0.012106, 0.205292, 0.021328, -0.065147,
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30 -0.022587, -0.063537, 0.080261, 0.000346, -0.228381,
    0.112714, 0.047662, 0.114726, 0.127581, -0.164887,
    -0.011701, -0.071580, -0.011703, -0.050086, 0.012629,
    0.172233,\ 0.024553,\ 0.097856,\ -0.045110,\ 0.028485,
    0.053387, 0.206829, 0.082417, 0.106178, -0.175071}};
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    -0.089297, -0.108139, -0.100579, -0.092361, -0.086512,
5 -0.176998, 0.033879, -0.072401, -0.108418, 0.254736,
    -0.068285, 0.077788, 0.021740, -0.154785, 0.031473,
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10 -0.105963, 0.110935, -0.121537, -0.051934, 0.031338},
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    0.098726, -0.172410, -0.058015, 0.136373, 0.073529,
    -0.267444, -0.089580, -0.132521, -0.121546, 0.030034,
15 0.125941, -0.115521, 0.218909, 0.161758, 0.013384,
    0.090387, -0.053082, -0.045271, 0.031092, 0.002709,
    -0.093612, 0.150086, -0.043744, 0.207734, 0.059490,
    0.013707, -0.009697, -0.101506, 0.130092, -0.106058},
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20 -0.077562, -0.011722, -0.012750, -0.006706, -0.116933,
    -0.037242, 0.043246, 0.189936, -0.001128, 0.348653,
    -0.114783, 0.103494, 0.065052, 0.017331, 0.216982,
    -0.124355, 0.116175, 0.021859, 0.140445, -0.080528,
    -0.021540, -0.059484, -0.039156, -0.003790, -0.013488,
25 0.024023, 0.127027, -0.085937, 0.029984, 0.005821,
     0.113871, -0.155281, -0.188480, -0.138627, 0.038015,
    \{-0.055477, -0.078725, -0.227185, -0.069835, -0.083334,
    -0.103103, 0.126817, 0.097232, -0.148670, -0.003821,
    -0.014695, -0.006567, 0.220706, 0.003373, -0.233583,
30 -0.059080, 0.096967, 0.105144, 0.074281, 0.062384,
    -0.238312, -0.011260, -0.087787, 0.053234, 0.114154,
    -0.087050, -0.067738, 0.054583, 0.087169, 0.026215,
     0.022347, -0.002260, 0.024900, -0.085700, 0.274932,
    -0.029898, 0.187180, 0.083484, -0.032945, 0.011922},
35 { 0.060811, -0.013551, 0.132393, 0.173236, -0.134129,
    -0.025827, 0.146442, -0.144697, -0.165687, -0.041179,
     -0.069049, 0.065456, 0.066934, -0.111799, -0.155041,
     0.017693, 0.018106, 0.011958, -0.101361, 0.135540,
     -0.026952, -0.072444, -0.099615, 0.048850, 0.074227,
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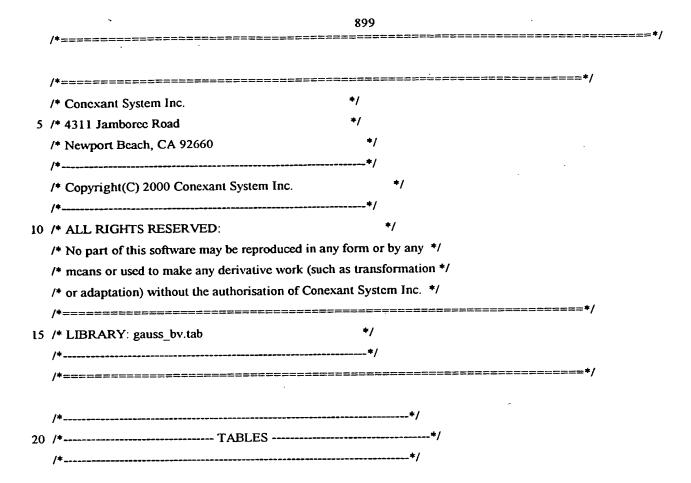
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   /* Conexant System Inc.
   /* 4311 Jamboree Road
 5 /* Newport Beach, CA 92660
   /* Copyright(C) 2000 Conexant System Inc.
   /* ALL RIGHTS RESERVED:
10 /* No part of this software may be reproduced in any form or by any */
   /* means or used to make any derivative work (such as transformation */
   /* or adaptation) without the authorisation of Conexant System Inc. */
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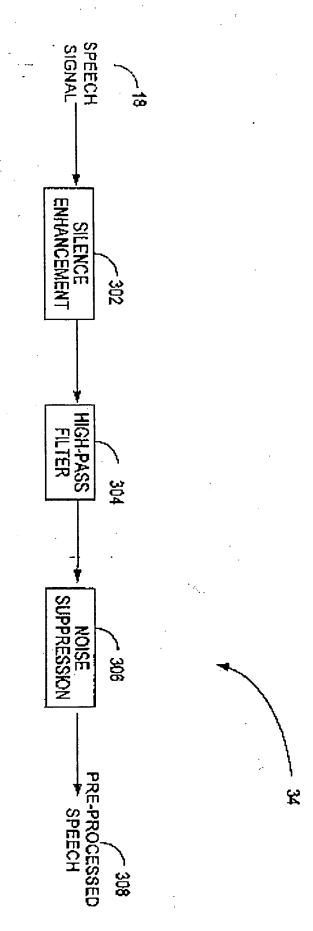
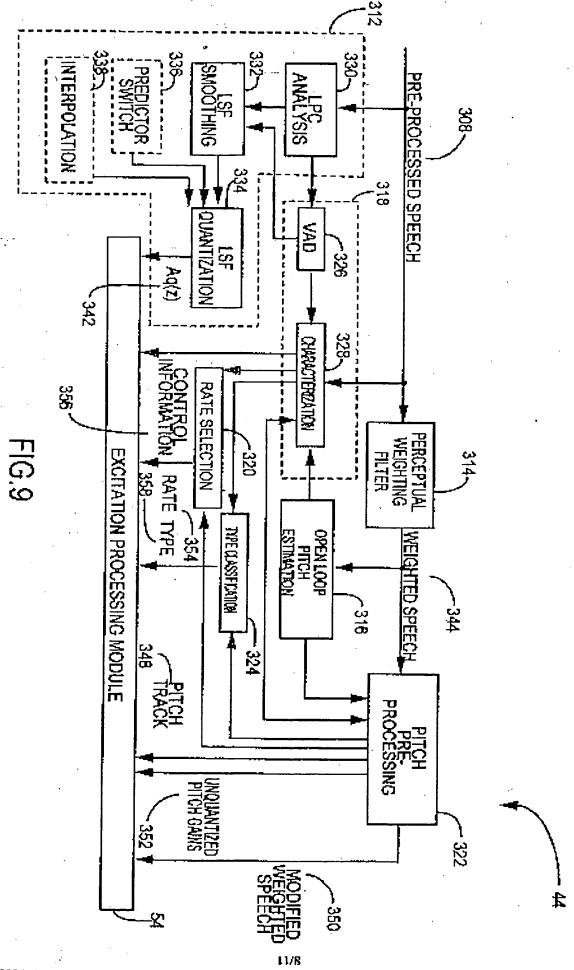


FIG. 8

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AVO UI/22402

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